

EtherCAT Communication

Communication Principles



- EtherCAT Basics
- Slave Structure
- Device Model
- Physical Layer
- Data Link Layer
 - Frame Structure
 - Addressing, Commands
 - Memory, SyncManager, FMMUs
 - Diagnosis
- Distributed Clocks
- Application Layer
 - State Machine
 - Mailbox (Mailbox Protocols)
 - Slave Information Interface (EEPROM)
- Device Profiles
 - Modular Devices
 - Drives
- Device Description
- Tools (Configuration Tool, Monitor, ...)
- EtherCAT Master
- Standard & References

EtherCAT: versatile system architecture

EtherCAT Basics

Slave Structure

Device Model (ISO/OSI)

Physical Layer

Data Link Layer

Frame Structure

Addressing

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Memory/Registers

SyncManager

FMMU

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Application Layer

State Machine

Mailbox

Mailbox Interface

EoE Ethernet

CoE CANopen

FoE File Access

SoE Servo Drive

ESI: Device Description

SII: Slave Information /IF

Device Profiles

Modular Devices

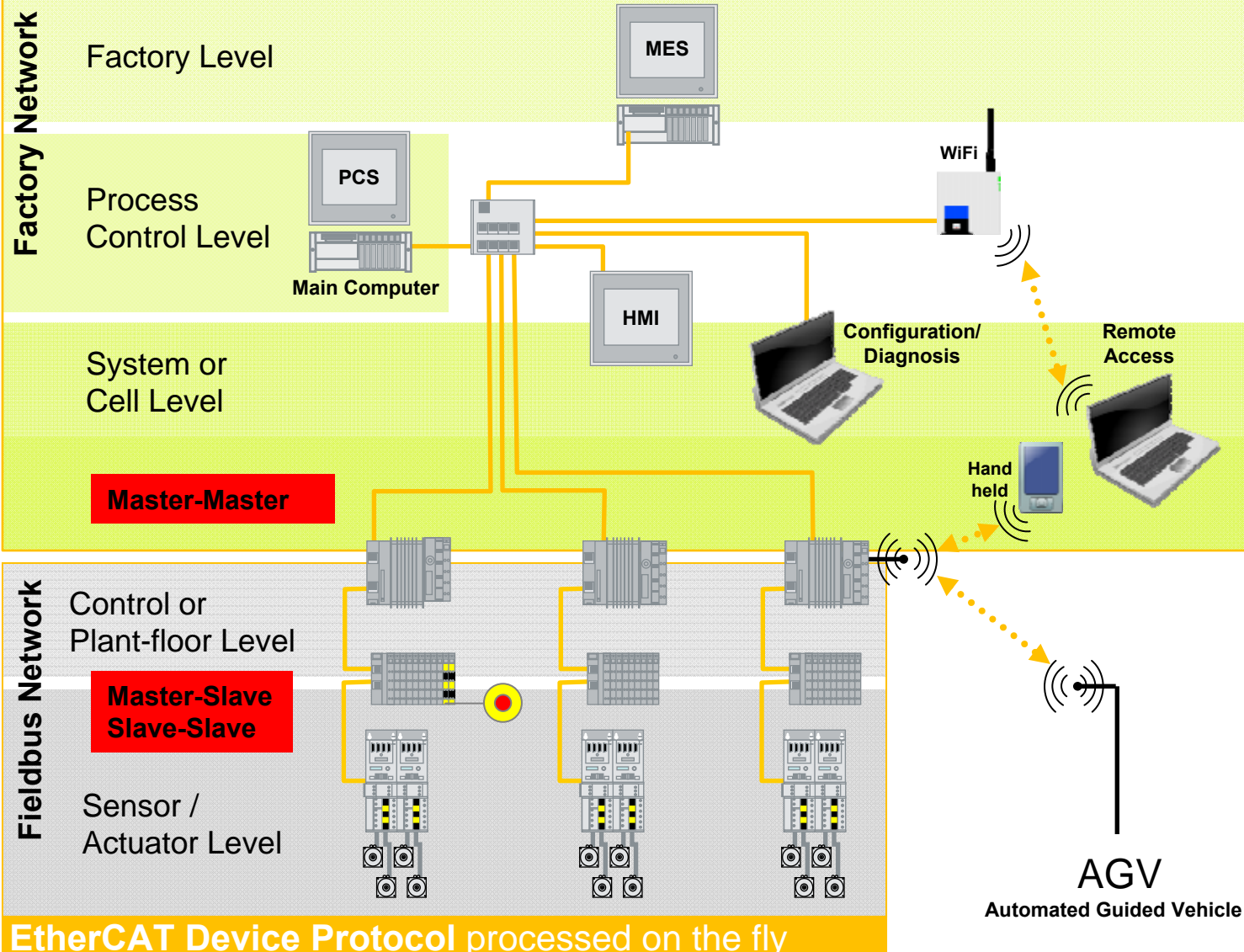
Drives

Configuration Tool

EtherCAT Master

Standards&Implementation

EtherCAT Automation Protocol



EtherCAT Basics

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EtherCAT Master

Standards&Implementation

- Communication between EtherCAT Master devices (Master-Master communication)
 - Communication between EtherCAT Master and visualization
 - Access to devices in underlying EtherCAT segments from the control level
 - Access from configuration tools
 - Configuration of the Master-Master communication
 - Configuration of underlying sub-devices (e.g. Drives, Gateways,...)
- Routing through EtherCAT Master
- Standard Ethernet interfaces
 - No strict requirements regarding cycle time and synchronization
 - Cycle time in the range of milliseconds
 - Use of standard infrastructure devices (switches)

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EtherCAT Master

Standards&Implementation

- Protocol for Master-Master communication
- Process data communication as well as Mailbox communication
- Same principles used as for EtherCAT Device communication
 - PDO Mapping
 - Mailbox protocols
 - Object dictionary
- Communication via standard Ethernet Network or other communication technologies, for example
 - Additional Ethernet port on Master device
 - Switchport
 - Wireless connection
- Master Object dictionaries contains information for configuration tools
- Telegram structure is part of the IEC 61158-x-12 series

Functional Principle: Ethernet “on the Fly”

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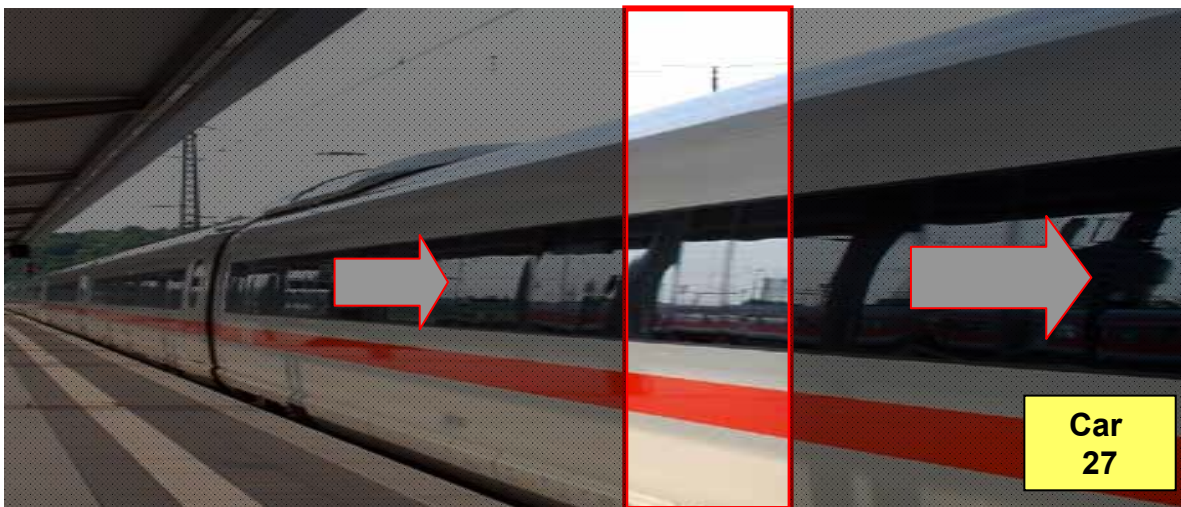
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Configuration Tool

EtherCAT Master

Standards&Implementation

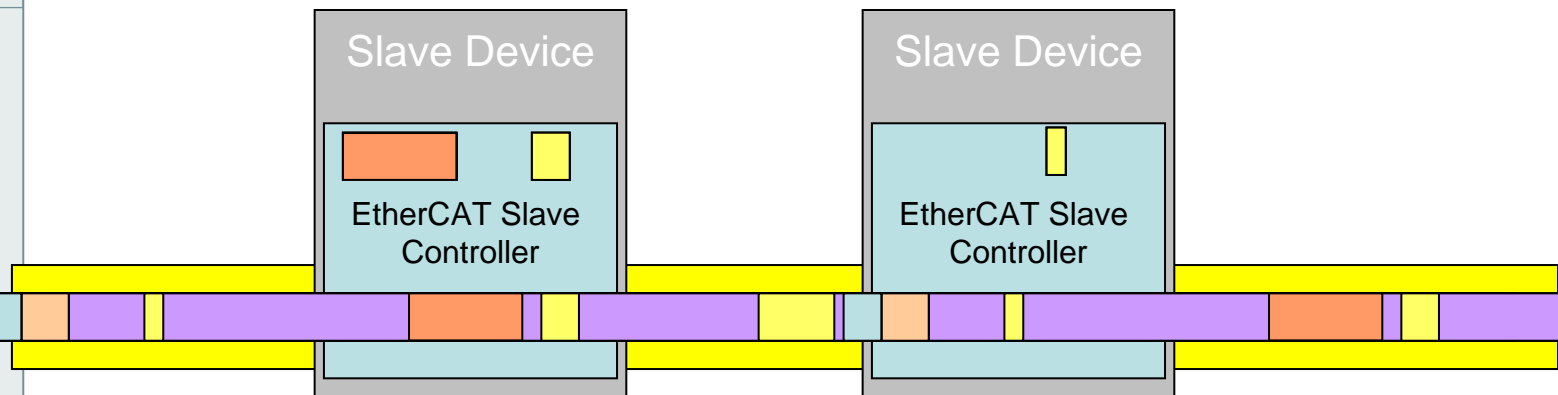


Analogy Fast Train:

- “Train” (Ethernet Frame) does not stop
- Even when watching “train” through narrow window one sees the entire train
- “Car” (Sub-Telegram) has variable length
- One can “extract” or “insert” single “persons” (Bits) or entire “groups” – even multiple groups per train

Functional Principle: Ethernet “on the Fly”

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- Standards&Implementation**



- Process data is extracted and inserted on the fly
- Process data size per slave almost unlimited (1 Bit...60 Kbyte, if needed using several frames)
- Compilation of process data can change in each cycle, e.g. ultra short cycle time for axis, and longer cycles for I/O update possible
- In addition asynchronous, event triggered communication

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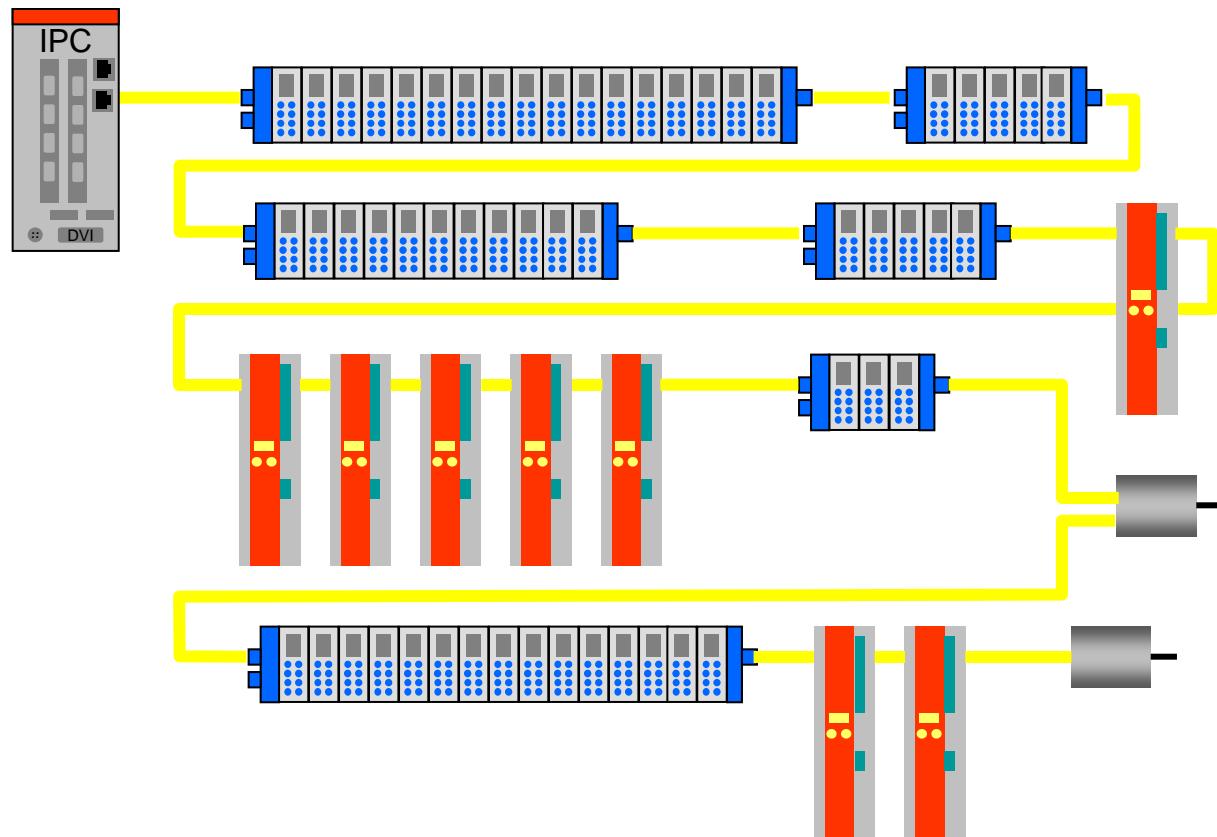
EtherCAT Master

Standards&Implementation

- Flexible Topology
 - Line
 - Daisy chain
 - Daisy chain with branches
 - Tree Structure
 - Star
 - Cable Redundancy
- Any number of physical layer changes possible
- Standard Ethernet 100m cable distance between 2 devices
- Fiber Optics for larger distances
- Up to 65,535 devices within one EtherCAT network possible

Topology – Line topology

- Arbitrary number of devices in a line
- Up to 65,535 devices



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Topology – Daisy Chain

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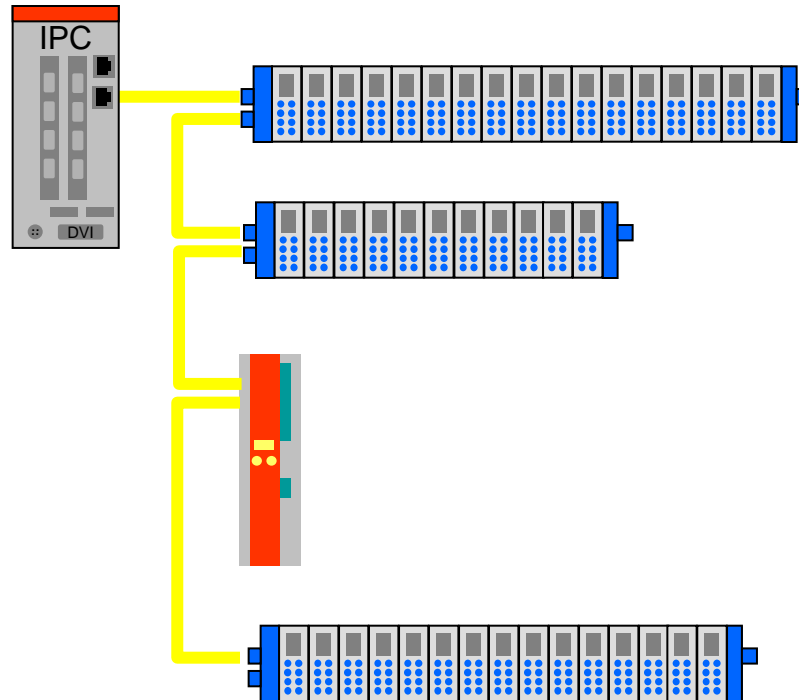
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Standards&Implementation



Topology – Daisy Chain with drop lines

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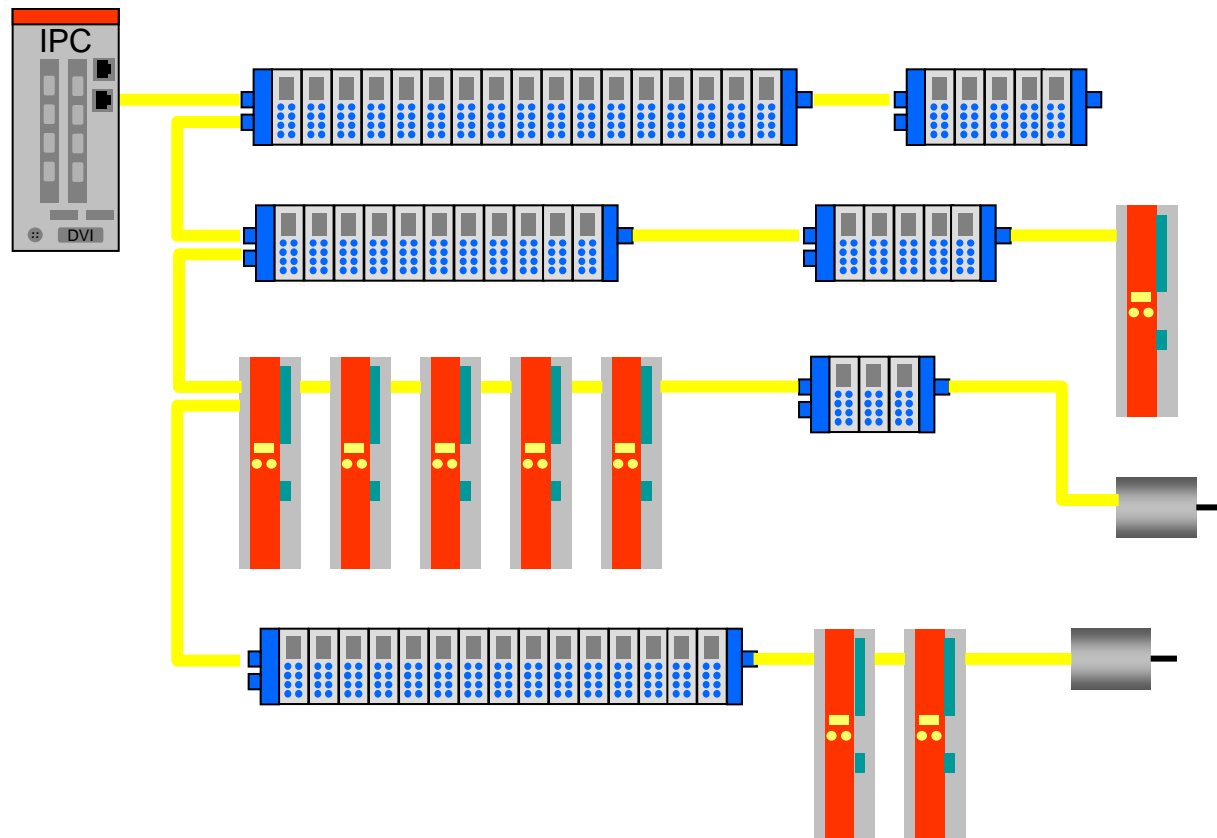
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Standards&Implementation



Topology – Tree structure

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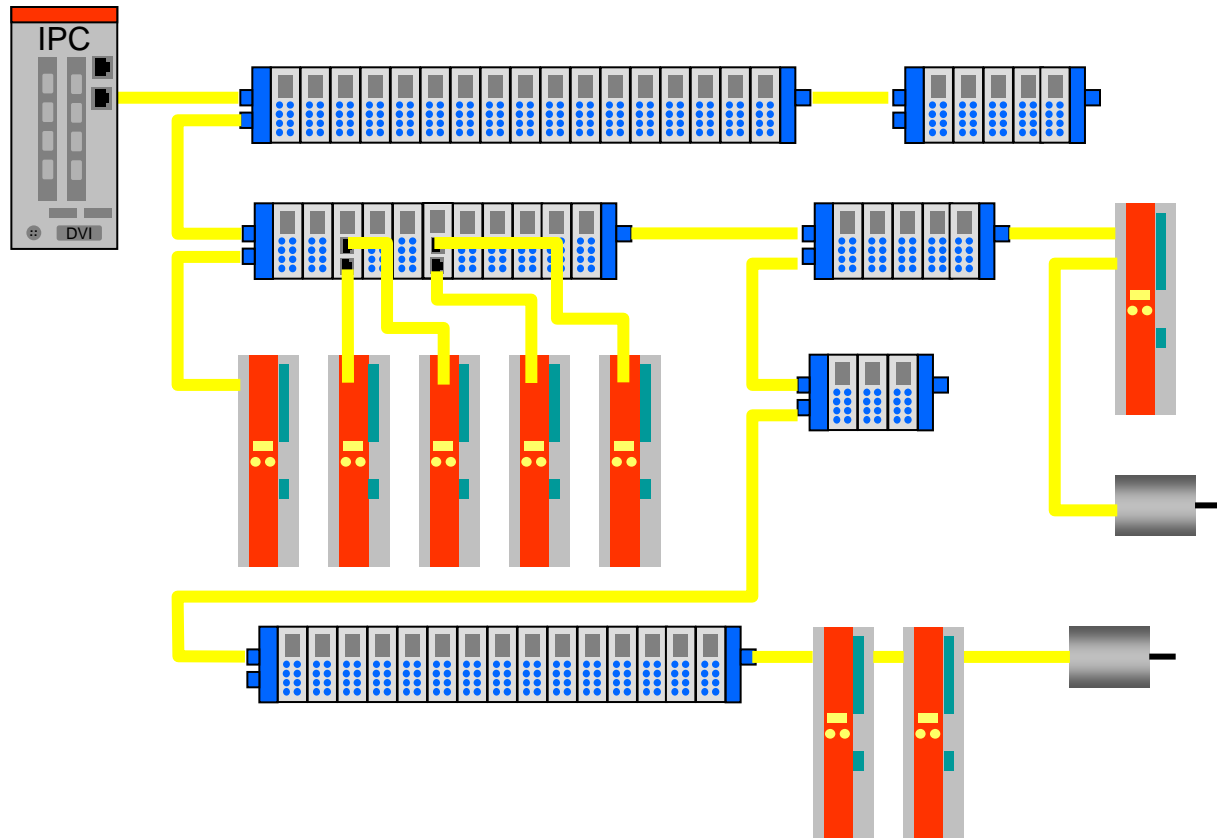
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Topology – Star topology with real time

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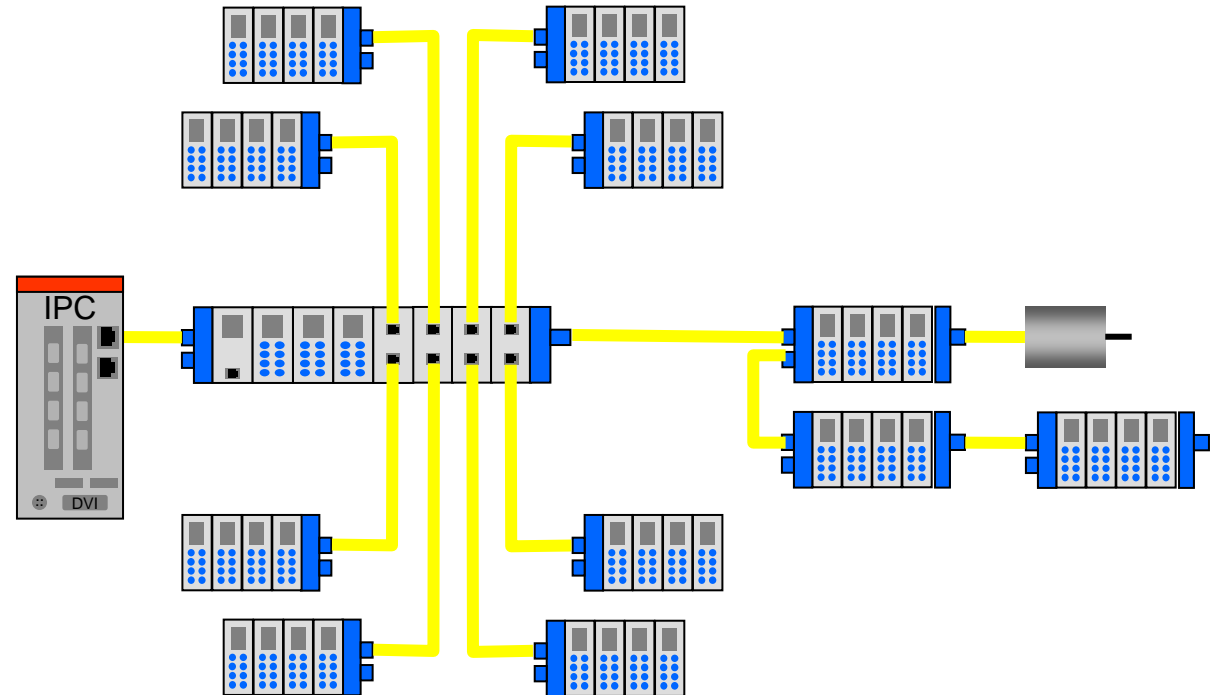
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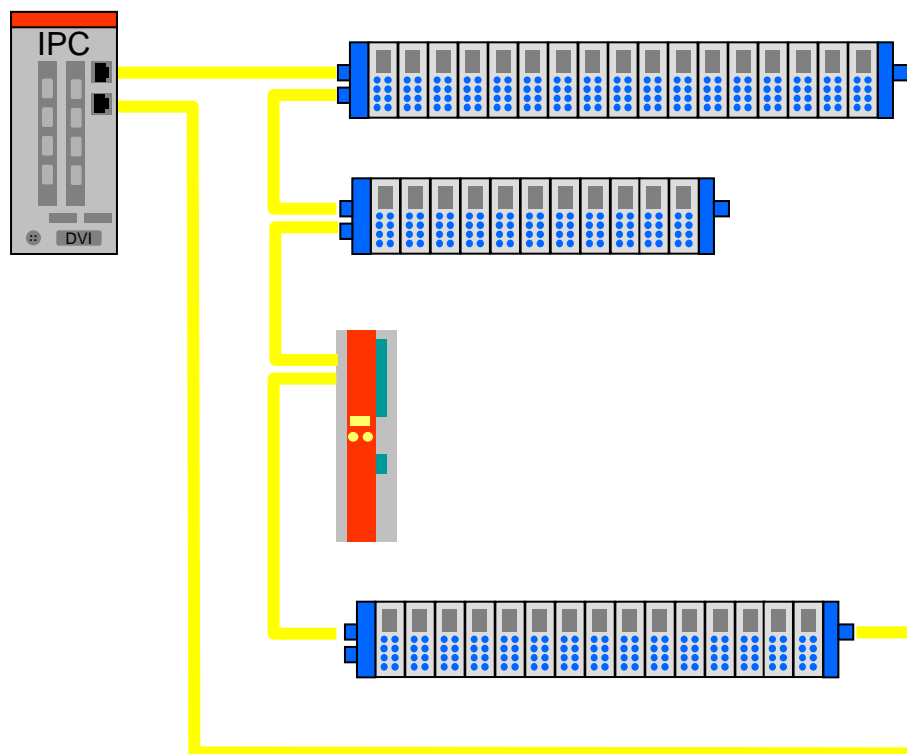
Device Profiles

Modular Devices
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Configuration Tool

EtherCAT Master

Standards&Implementation



- Only a second Ethernet Port is needed on the master – possible with all EtherCAT Slave devices

Without Redundancy: Normal Operation

EtherCAT Basics

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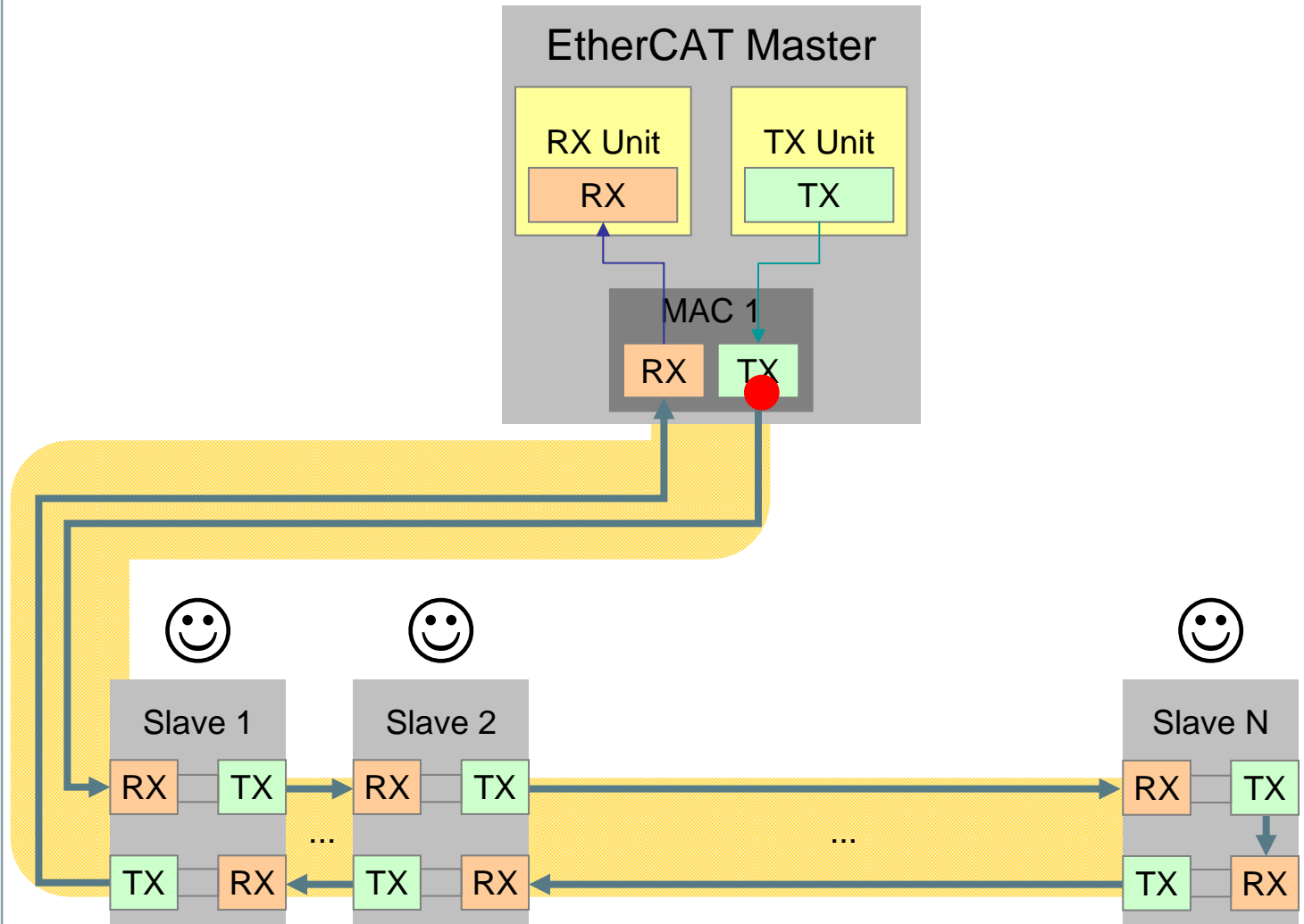
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EtherCAT Master

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Without Redundancy: Cable Failure

EtherCAT Basics

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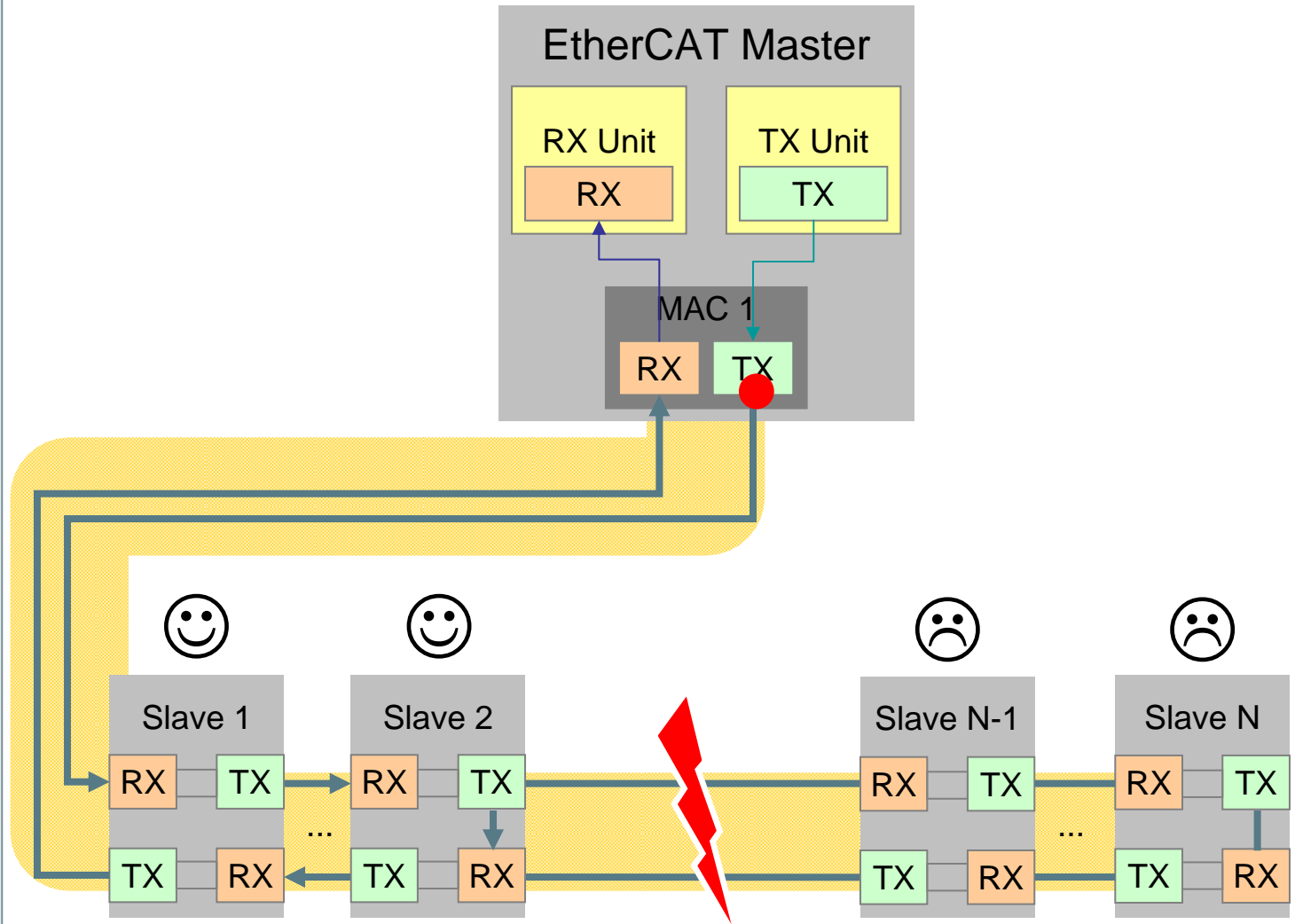
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Without Redundancy: Node or Cable Failure

EtherCAT Basics

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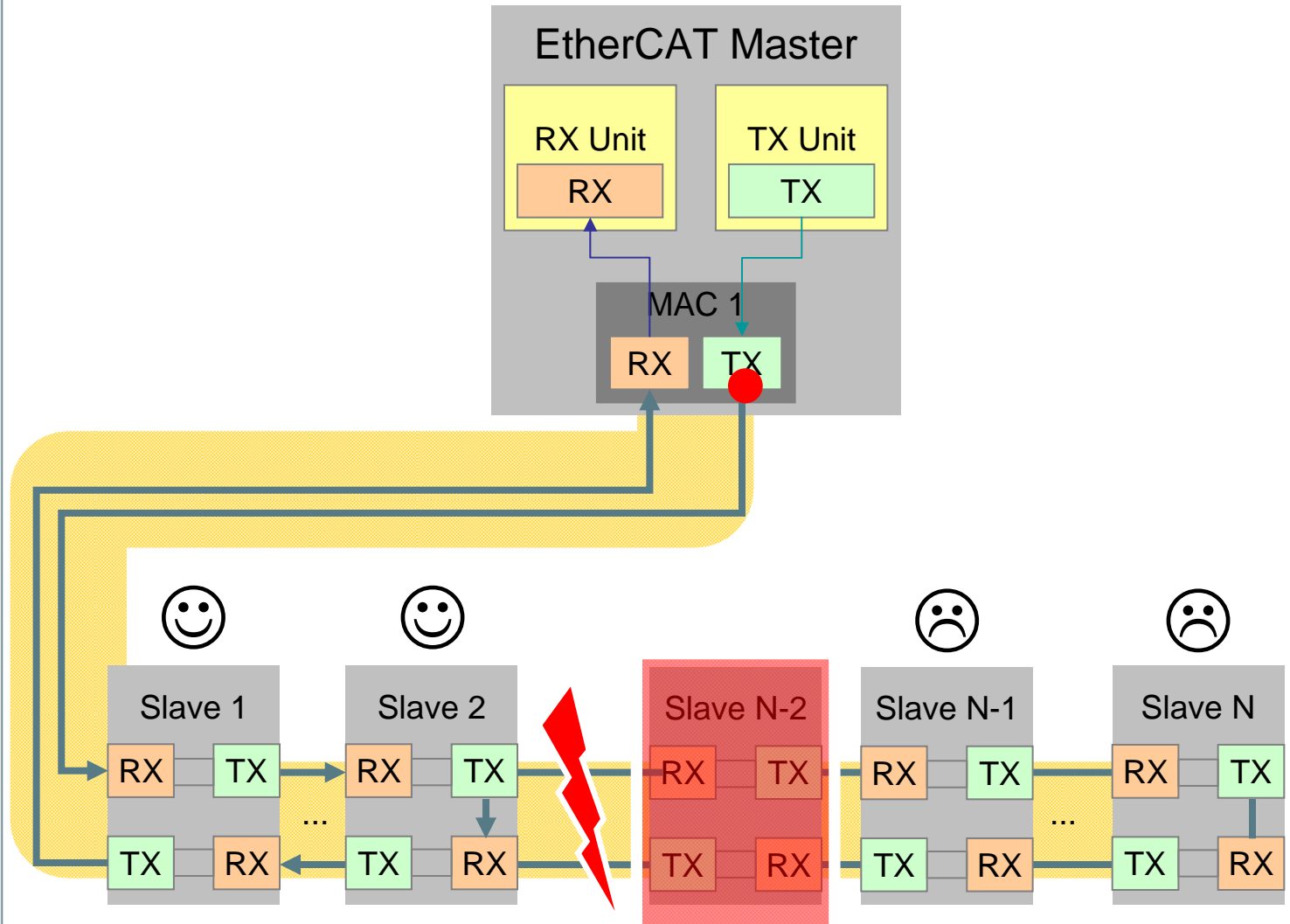
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With Redundancy: Normal Operation

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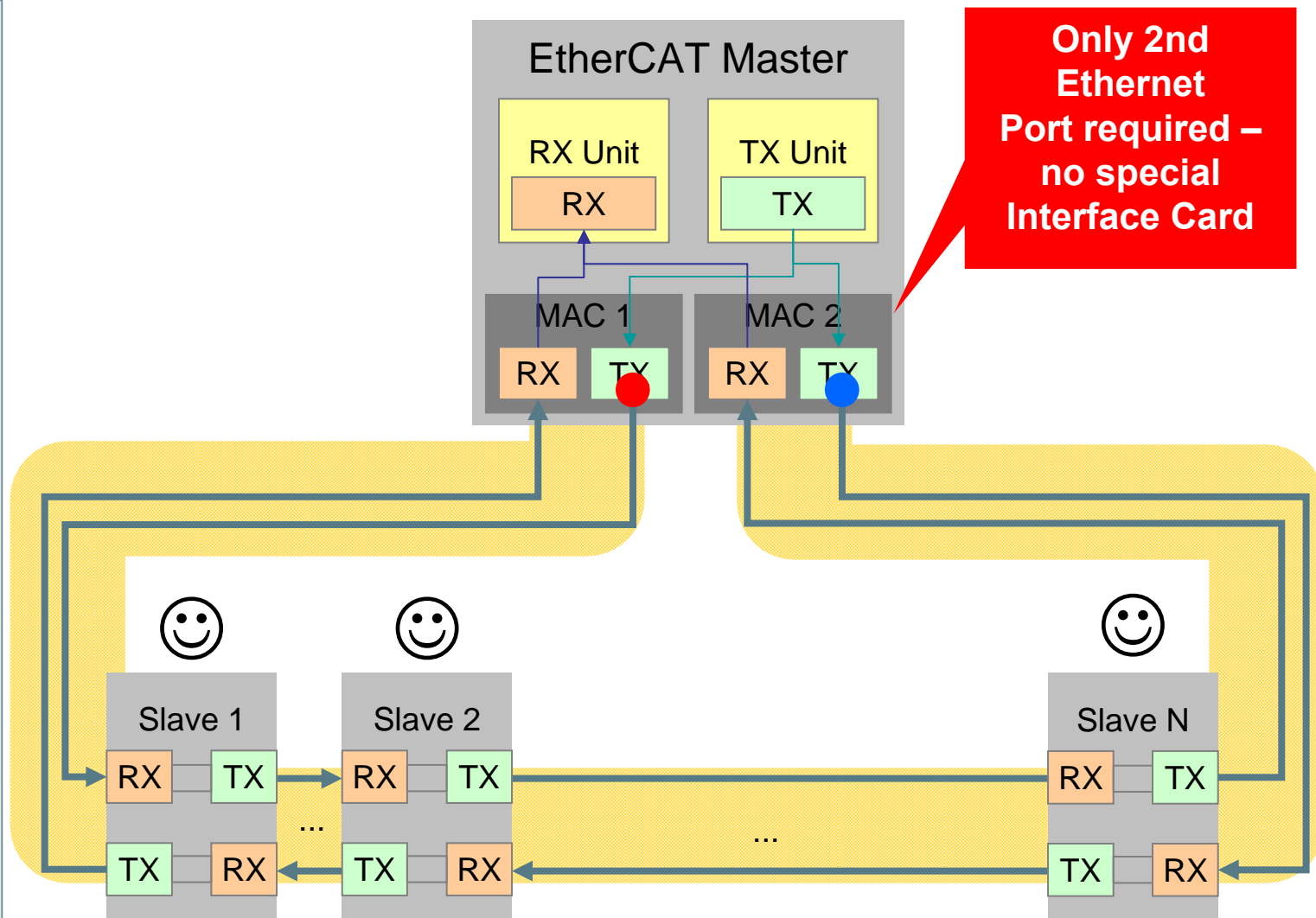
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With Redundancy: Normal Operation

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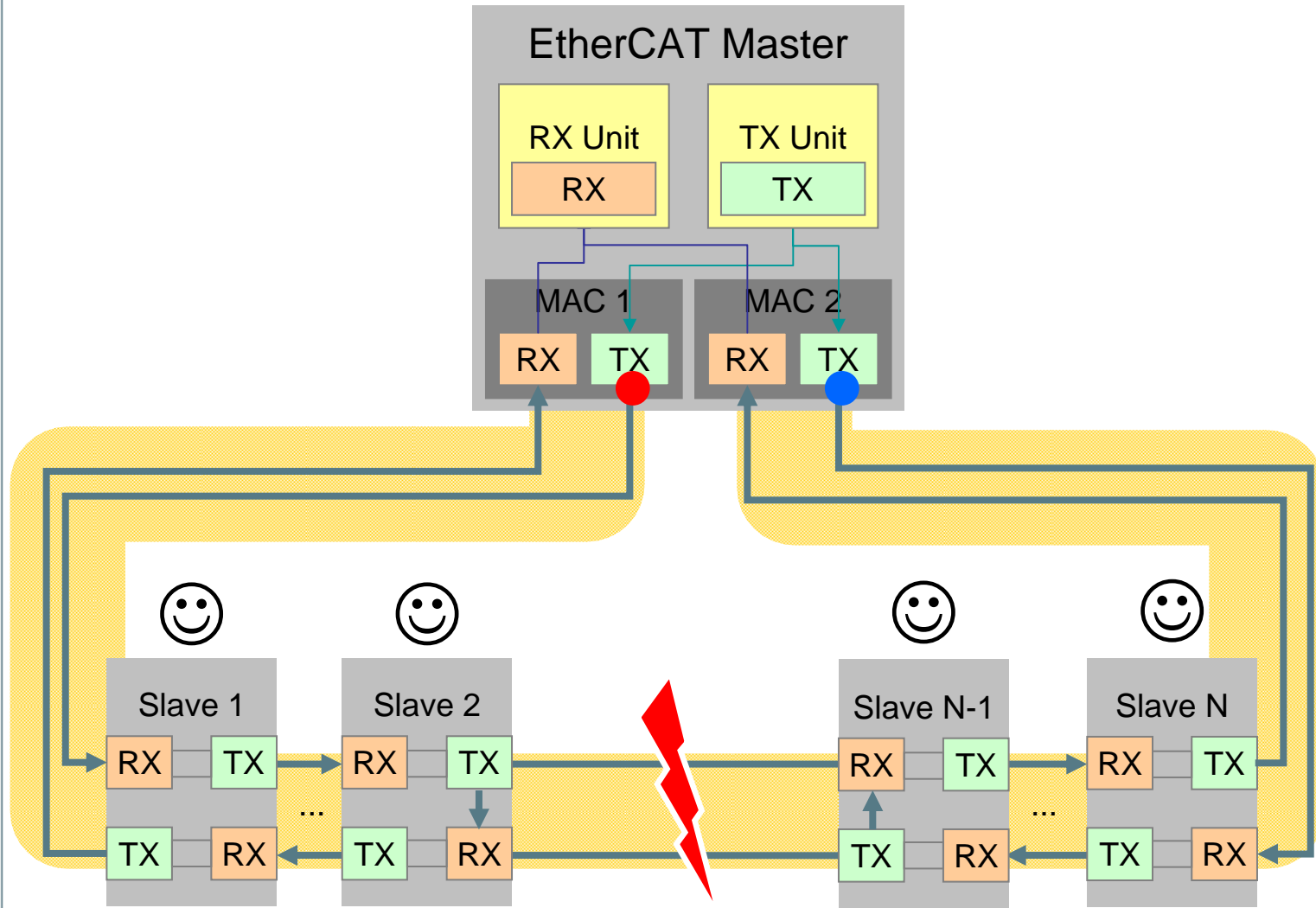
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With Redundancy: Node or Cable Failure

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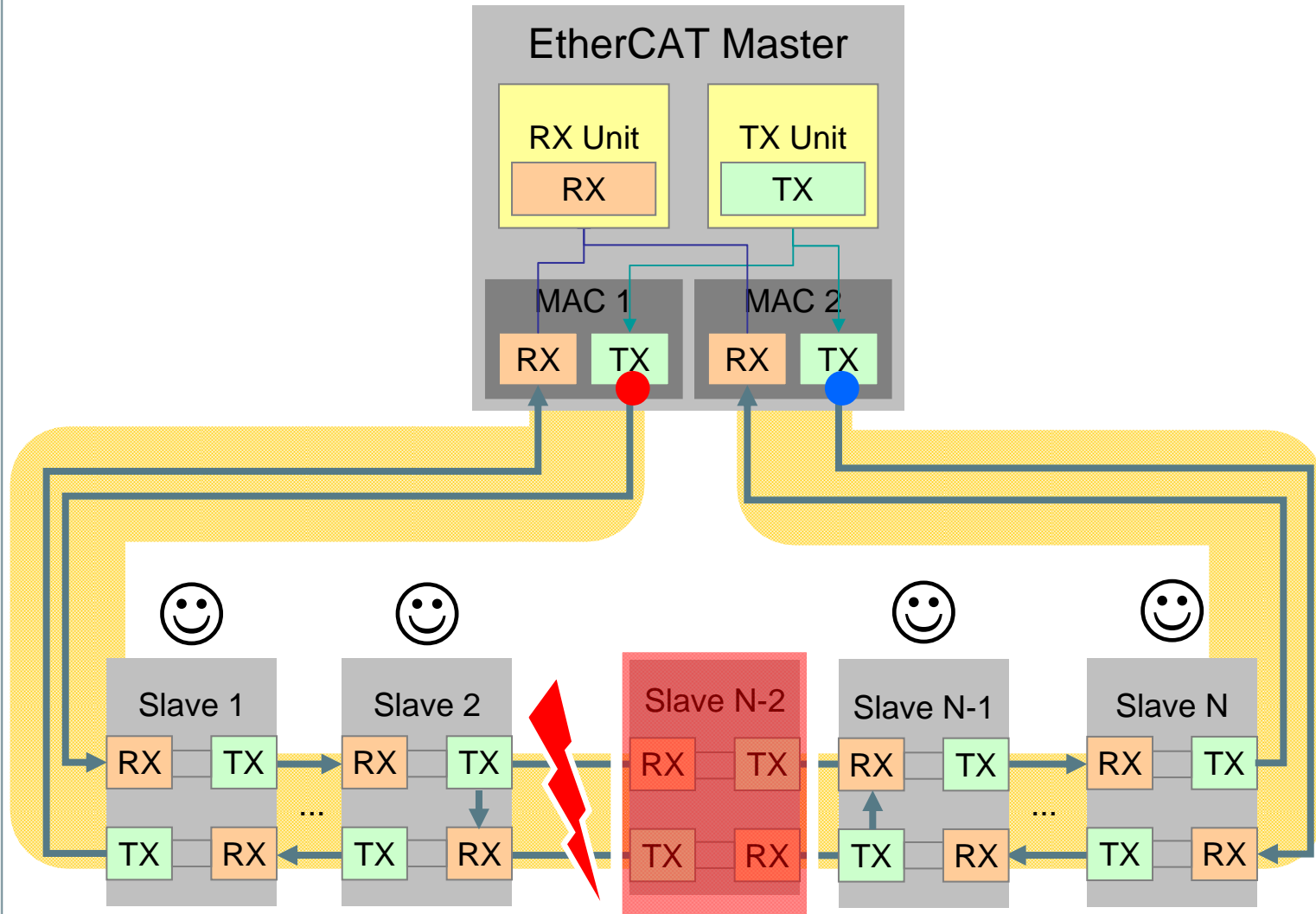
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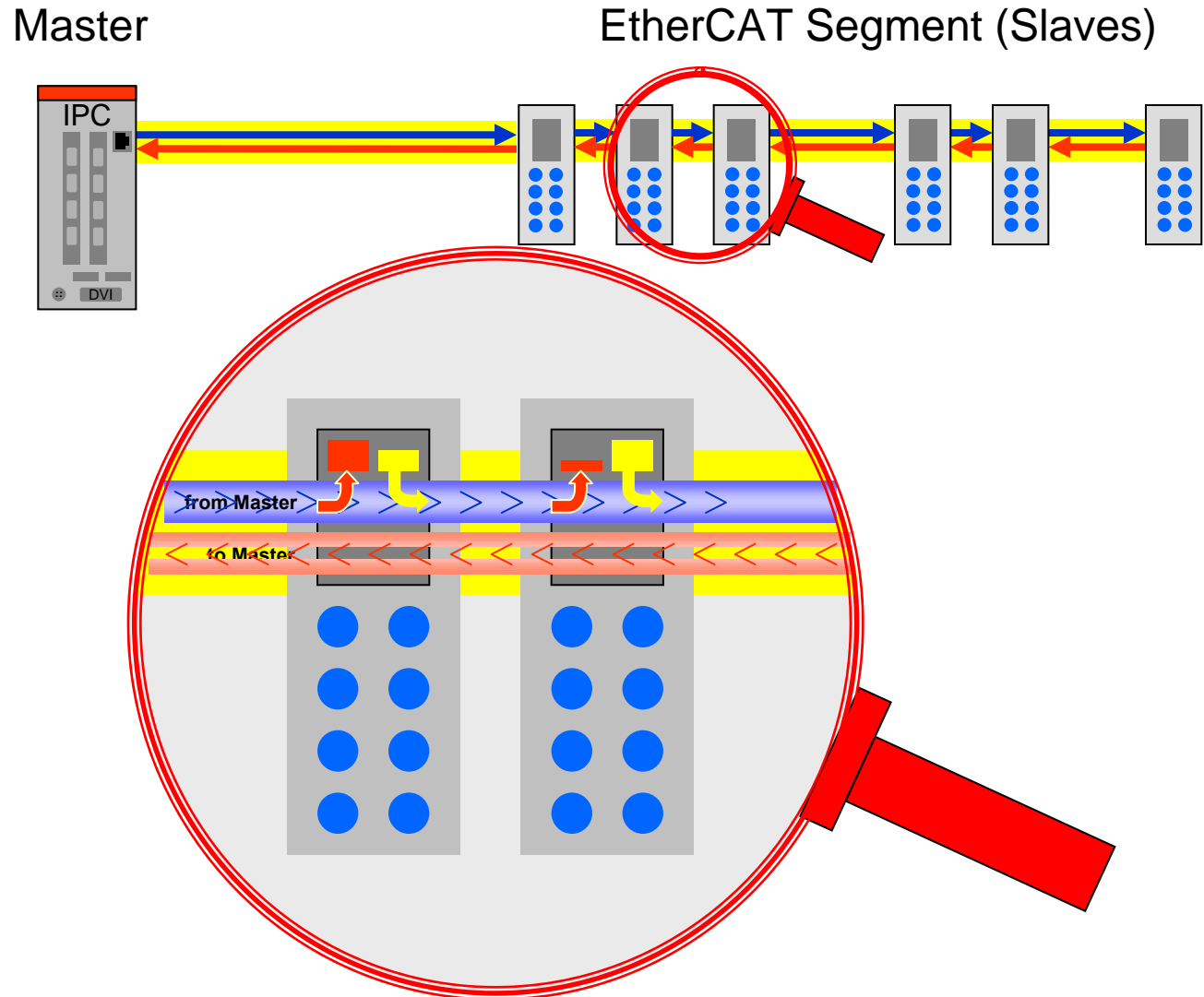
Configuration Tool

EtherCAT Master

Standards&Implementation



Frame Processing within one node



EtherCAT Basics

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EtherCAT Slave Structure

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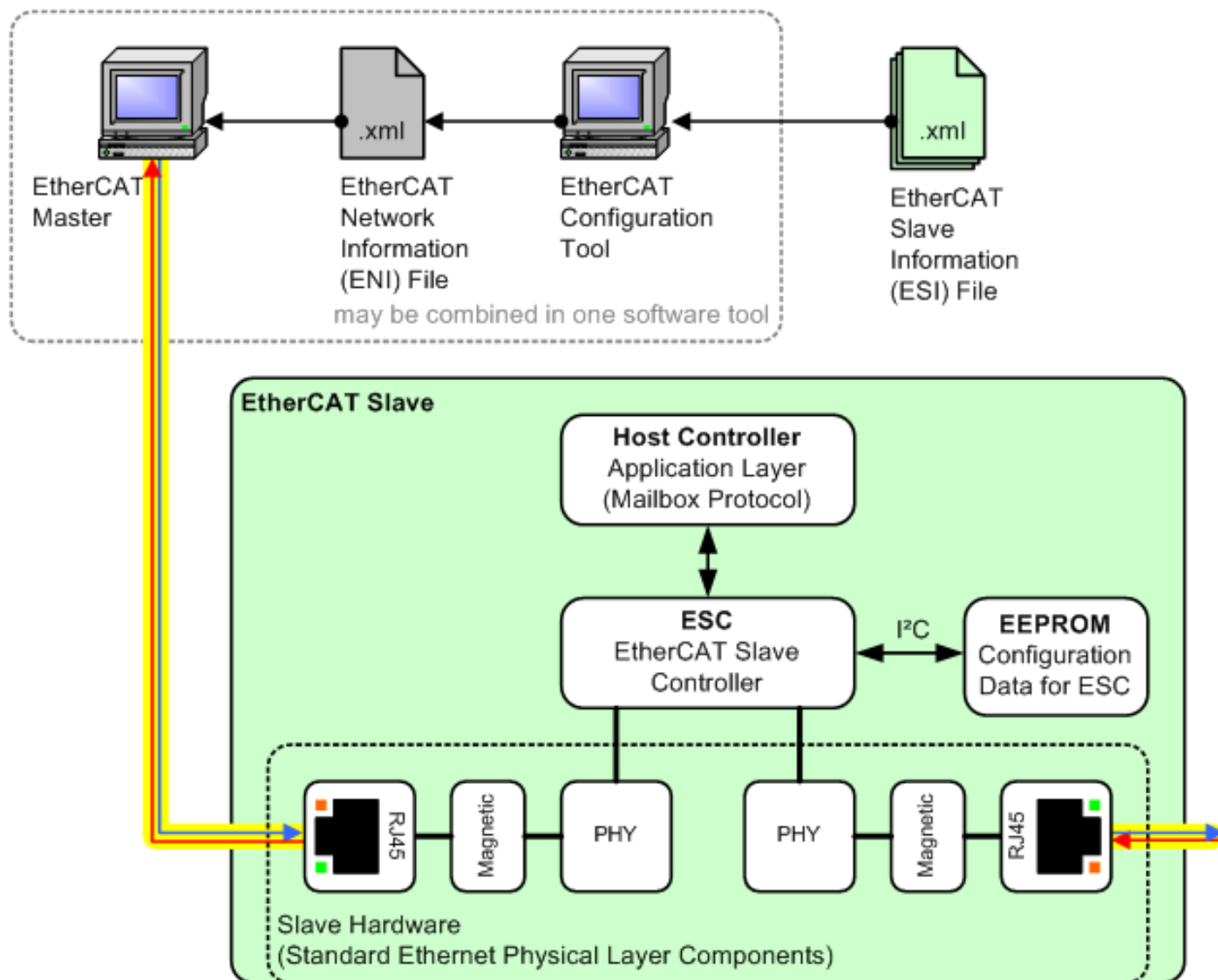
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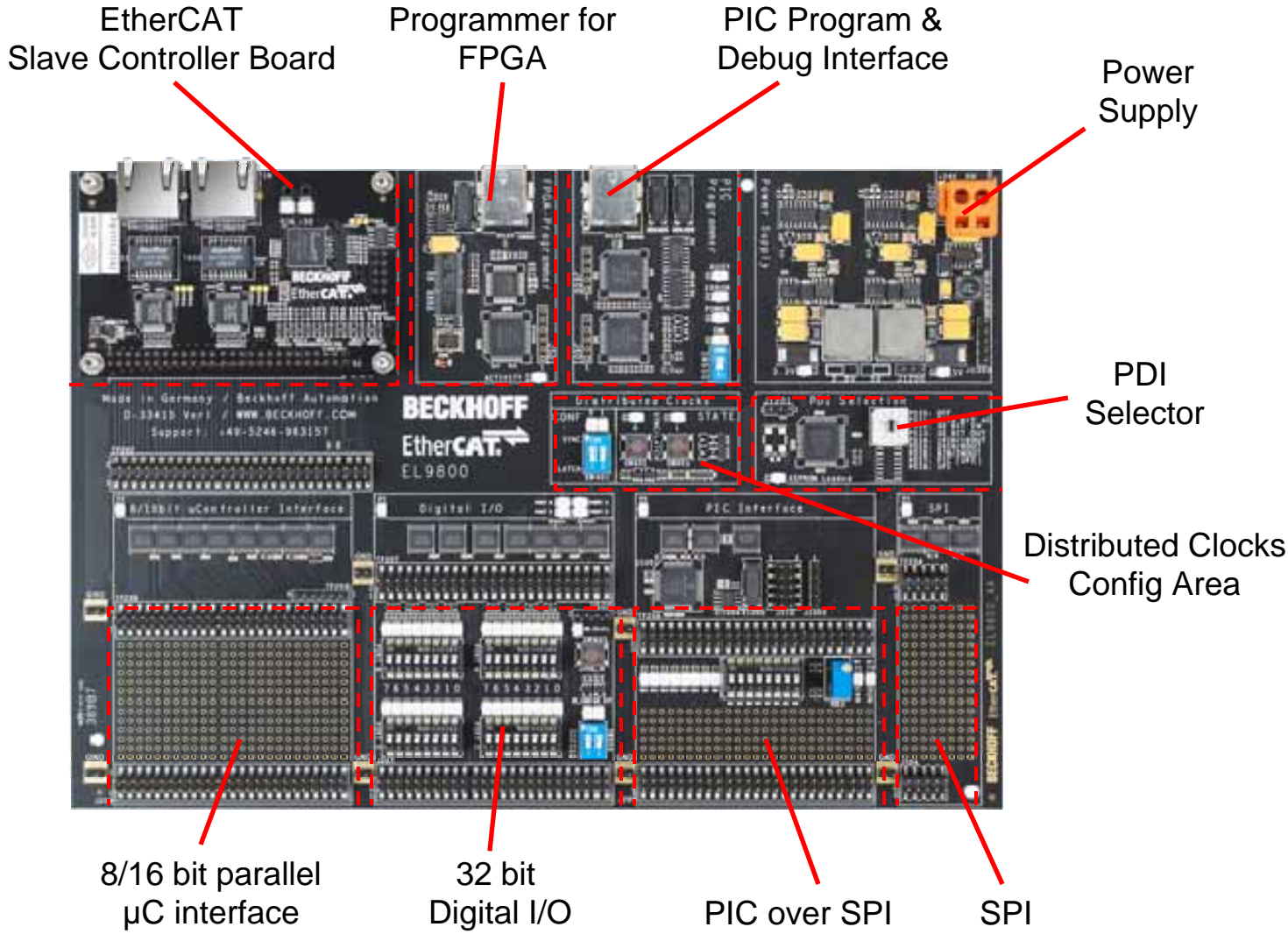
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Standards&Implementation



EtherCAT Slave Controller Board

EtherCAT Basics

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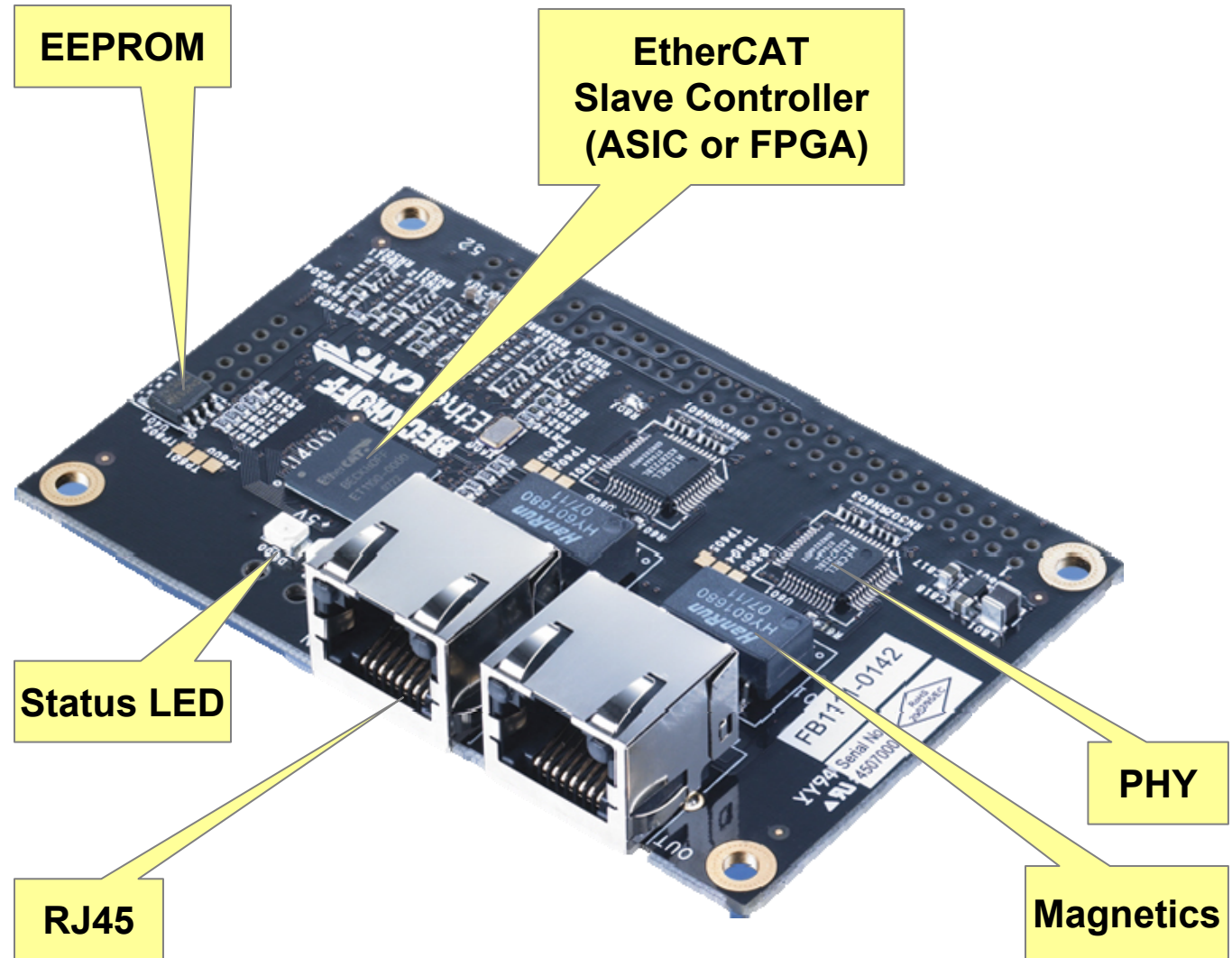
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* Post stamp design, not cost and space optimized

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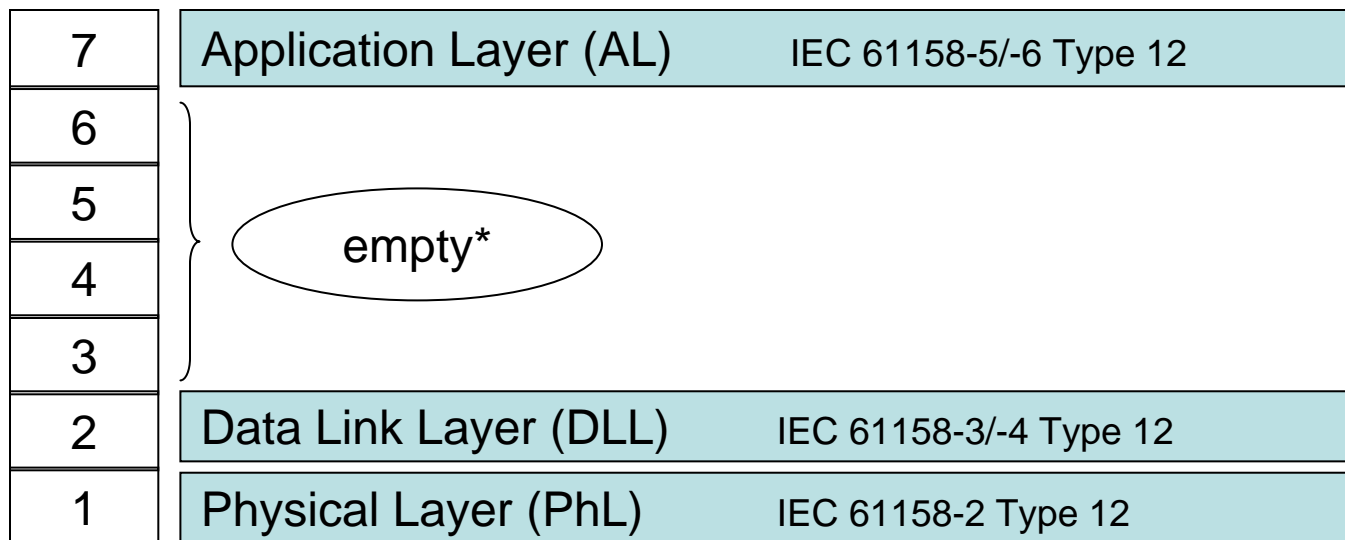
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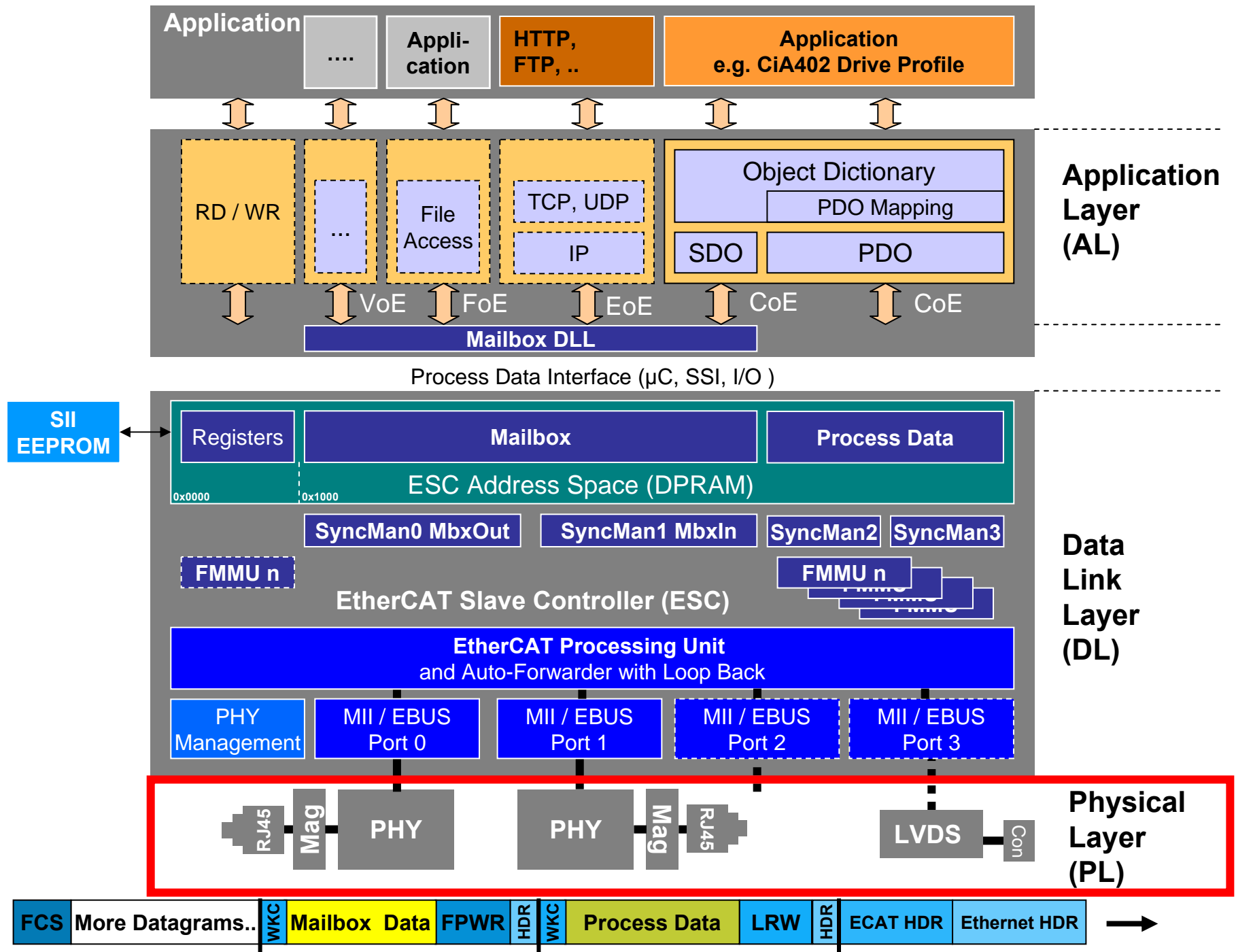
EtherCAT Master

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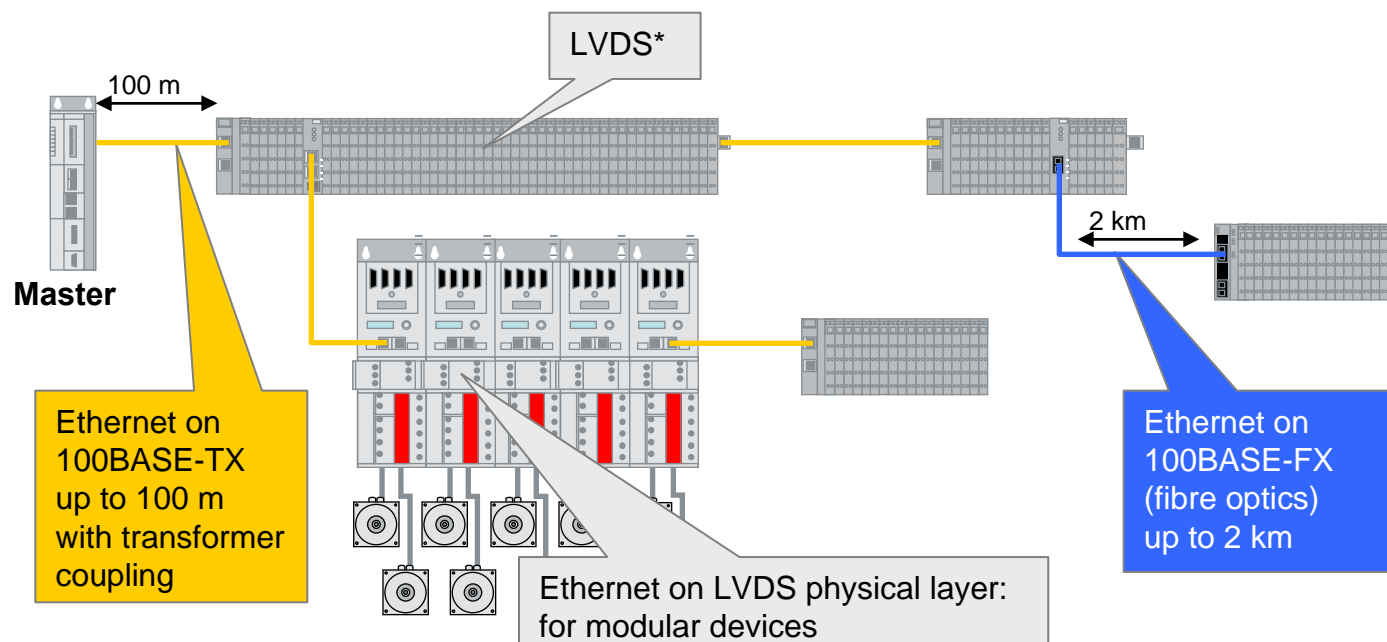
OSI
Layers



* „empty“ means that the layer behavior exists, but is not shown explicitly



- Ethernet Signal Variants of EtherCAT:
 - 100BASE-TX (up to 100 m between 2 nodes)
 - 100BASE-FX (up to 2 km between 2 nodes)
 - LVDS (for modular devices)



- Any number of physical layer changes allowed

EtherCAT Basics**Slave Structure****Device Model (ISO/OSI)****Physical Layer****Data Link Layer**

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Configuration Tool**EtherCAT Master****Standards&Implementation**

- 100 BASE-TX
 - Most popular physical layer for Fast Ethernet
 - Shielded twisted pair (STP) with 2 pairs of wires
 - Cable categories CAT5, 6, 7 can be used
 - RJ45 connector standard, M12 connector for IP67
 - PHY Support for auto negotiation and auto crossover recommended
- 100 BASE-FX
 - All media options possible
 - Simple solution for TX-to-FX converter
- E-BUS
 - Interface for low cost backplane applications
 - Widely used LVDS (Low Voltage Differential Signaling) adopted
 - Use Manchester Bit Coding
 - LVDS: Low Voltage Differential Signaling according to ANSI/TIA/EIA-644, also used in IEEE 802.3ae (10Gigabit Ethernet)

Frame Processing

Auto Forwarder and Loop Back

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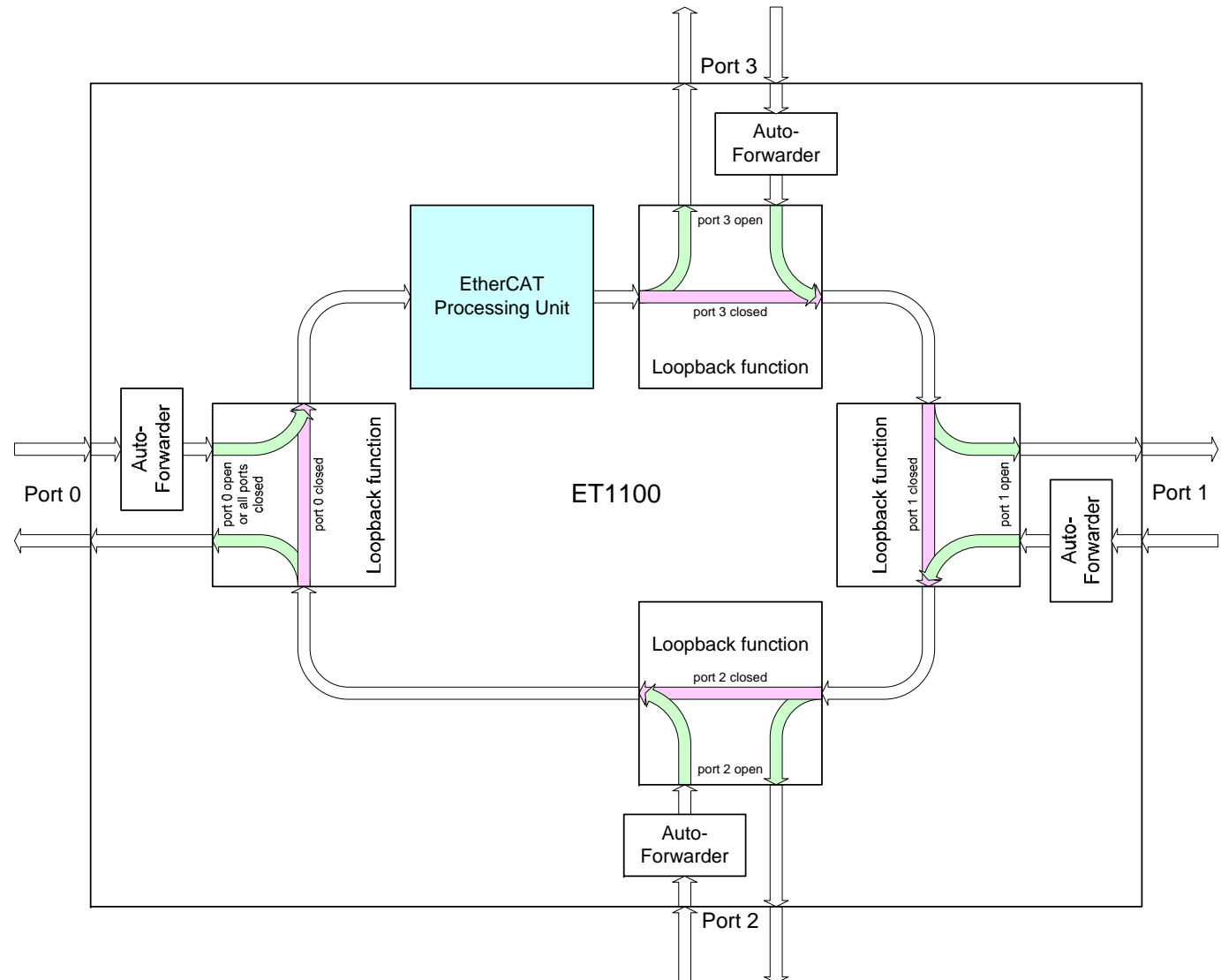
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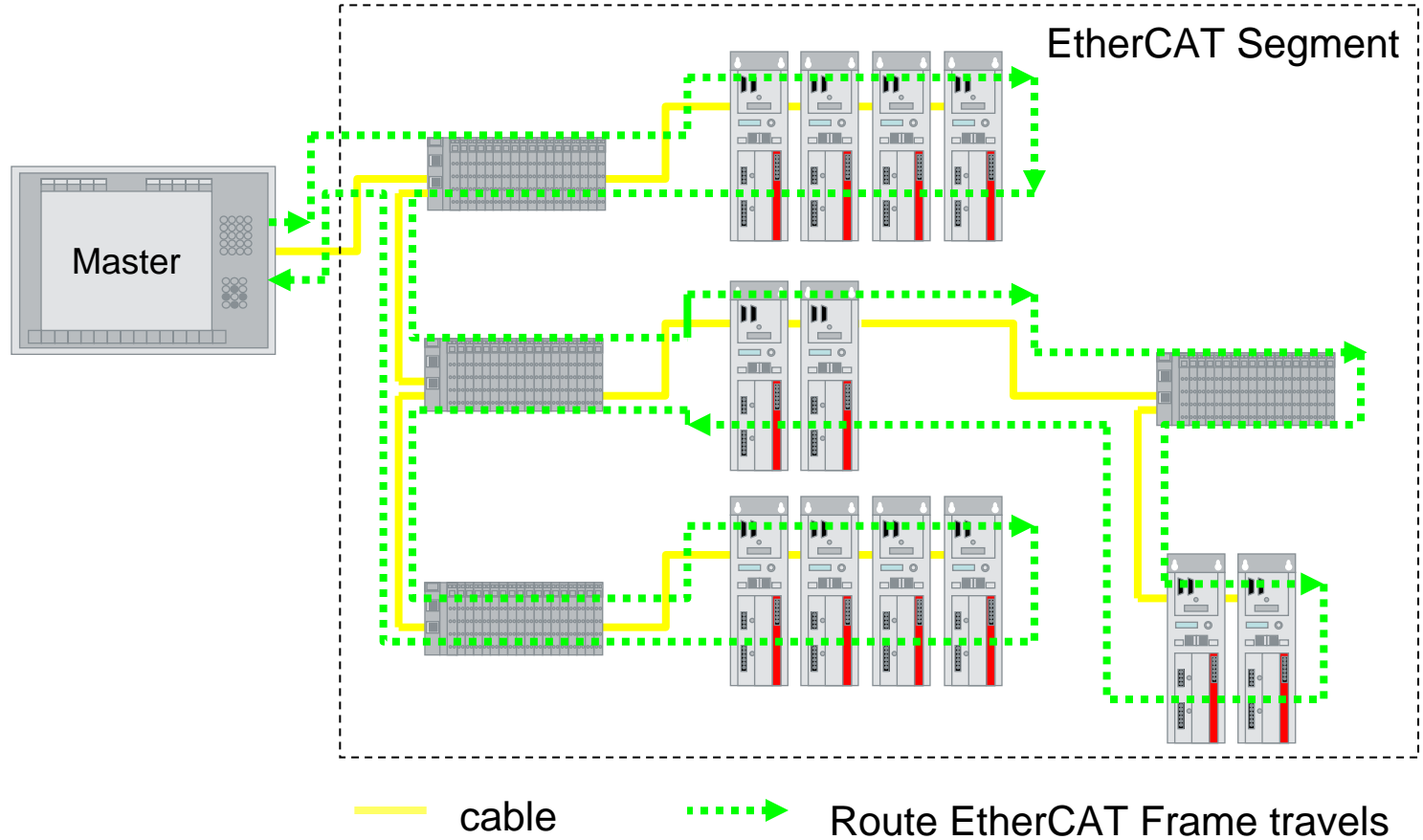
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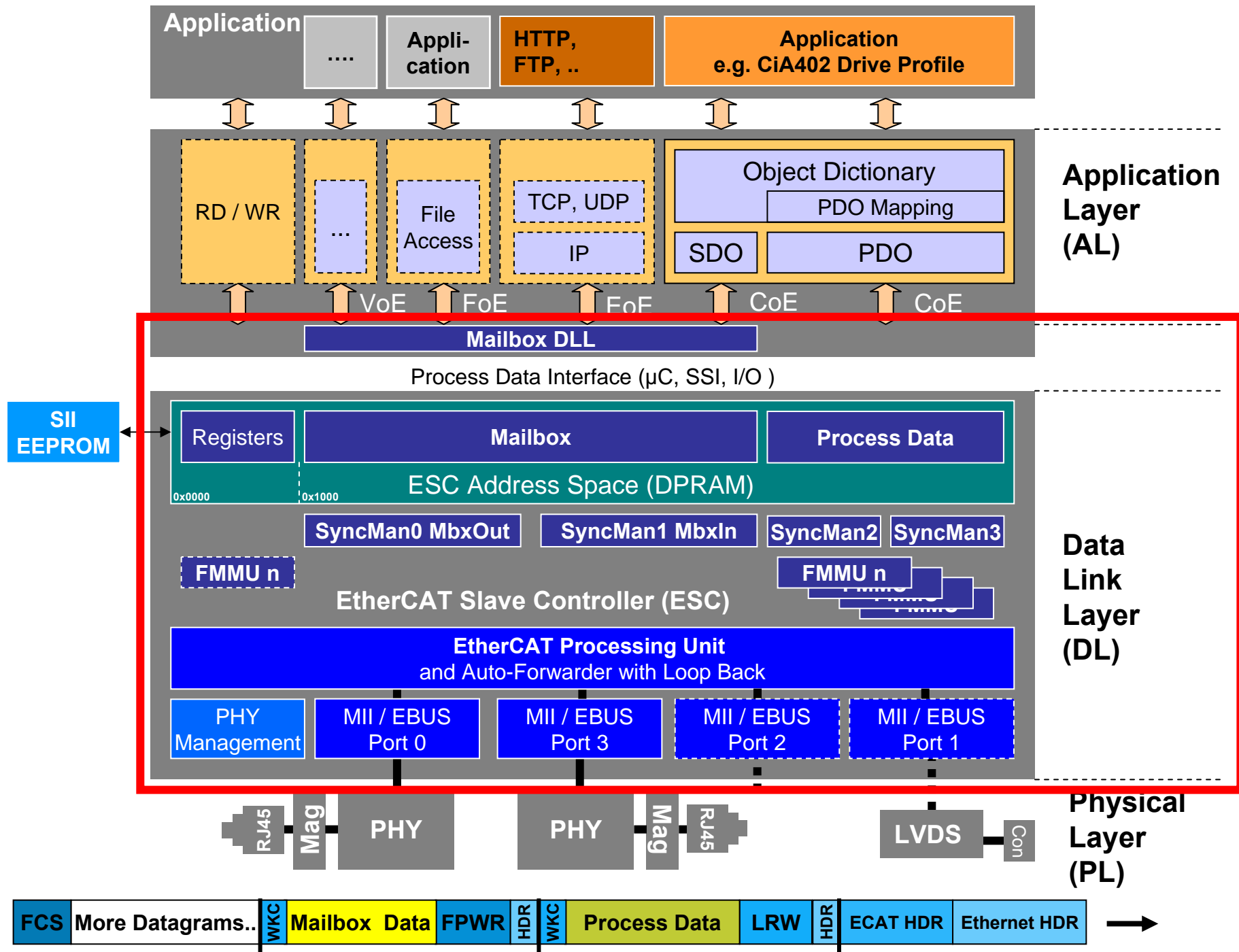
Standards&Implementation



Frame Processing Order on the System

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Purpose of Data Link Layer

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- Data Link Layer links Physical and Application Layer
- Data Link Layer takes care of the underlying communication infrastructure
 - Link Control
 - Access to Transceivers (PHY)
 - Addressing
 - Slave Controller configuration
 - EEPROM access
 - SyncManager configuration and management
 - FMMU configuration and management
 - Process Data Interface configuration
 - Distributed Clock
 - Set Up AL State Machine interactions

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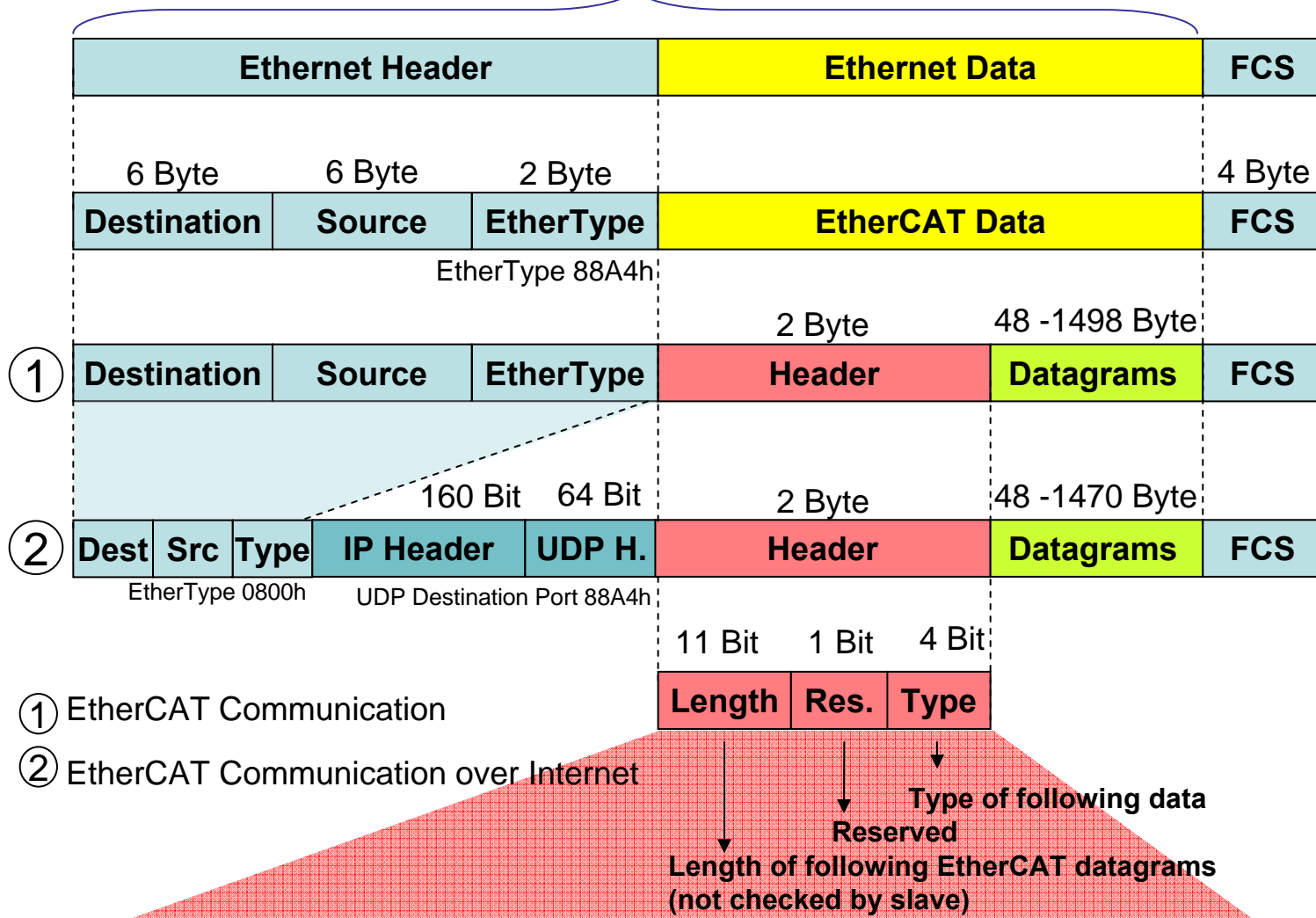
EtherCAT Master

Standards&Implementation

- Standard IEEE 802.3 Ethernet Frame
 - No special requirements for the master
 - Use of standard Ethernet infrastructure
- IEEE Registered EtherType: 88A4h
 - Optimized frame overhead
 - IP stack not required
 - Simple master implementation
- Additionally over UDP (IANA registered Port 88A4h)
 - EtherCAT communication over the Internet possible
 - Using of standard sockets
- Frame processing at Slave side
 - EtherCAT Slave Controller processes frame in hardware
- Communication Performance independent from processor power
 - no time critical reaction at slave side in software

Ethernet / EtherCAT Frame Structure

Ethernet Frame: max. 1514 Byte



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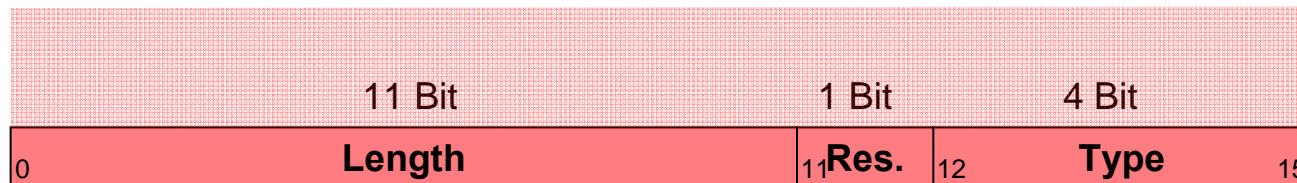
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Standards&Implementation



Type Meaning

0: **Reserved**

1: **EtherCAT Device Communication**
the only type that is evaluated by the ESC!

2,3: **Reserved**

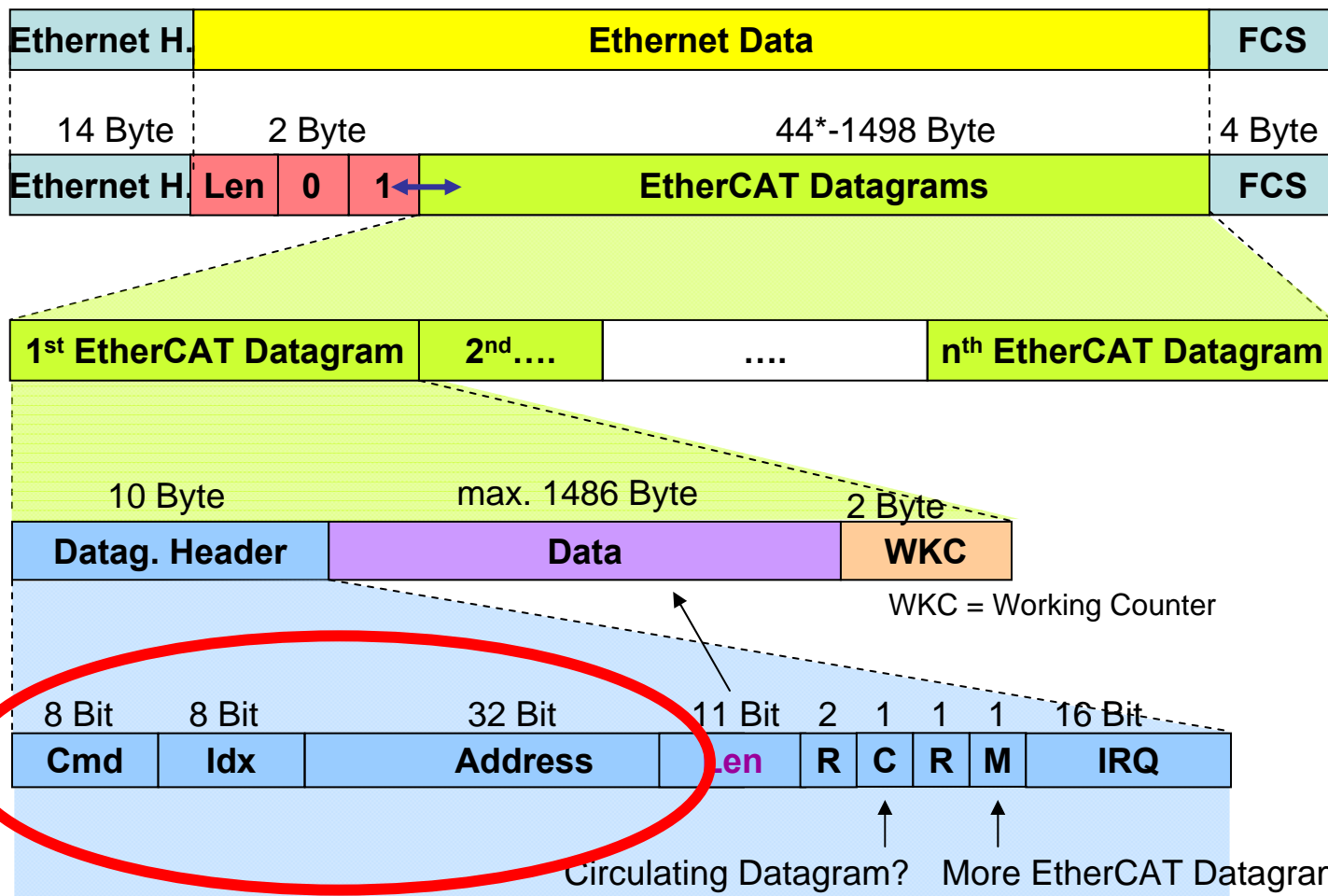
4: **EAP Process Data Communication**

5: **EAP Mailbox Communication**

6-15: **Reserved for future use**

EtherCAT Datagram Header Address

* add 1-32 padding bytes if Ethernet frame is less than 64



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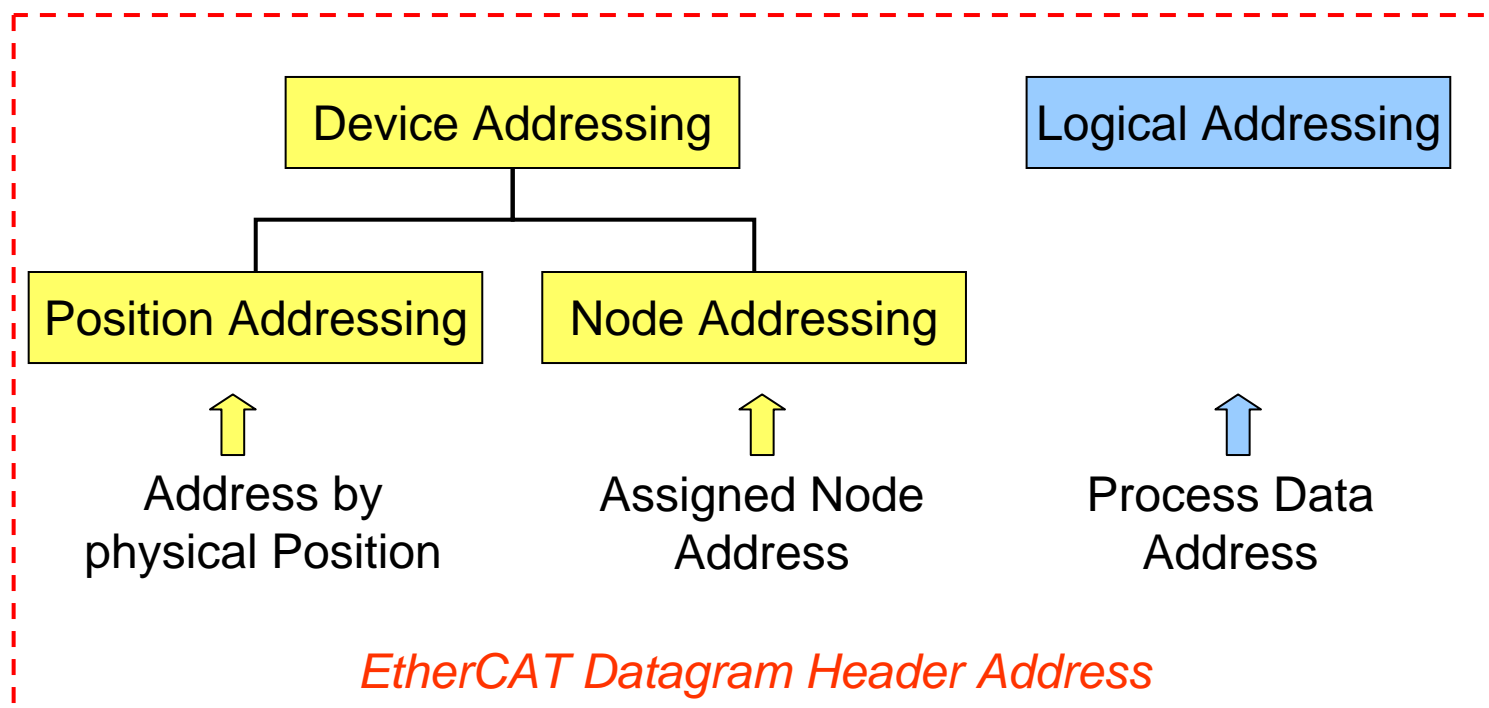
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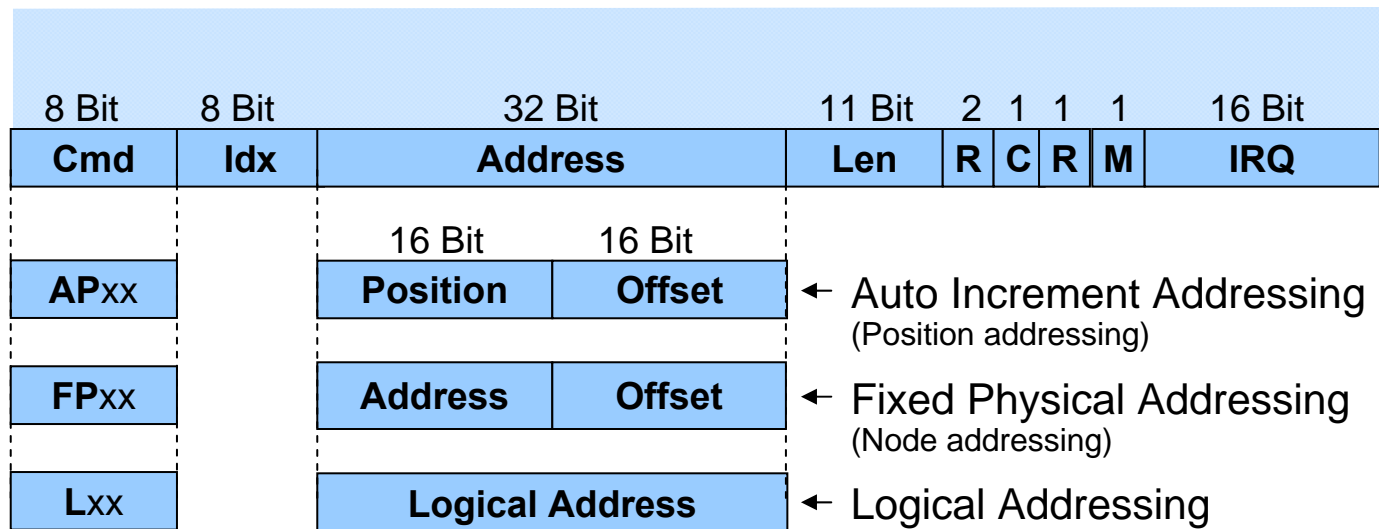
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Standards&Implementation



- 32 Bit address space

- used for 16 bit device addressing (position or fixed) (65,535 devices possible) and 16 bit for addressing local memory space of device (max. 64kByte)

or

- 32 bit logical addressing

Auto Increment Addressing

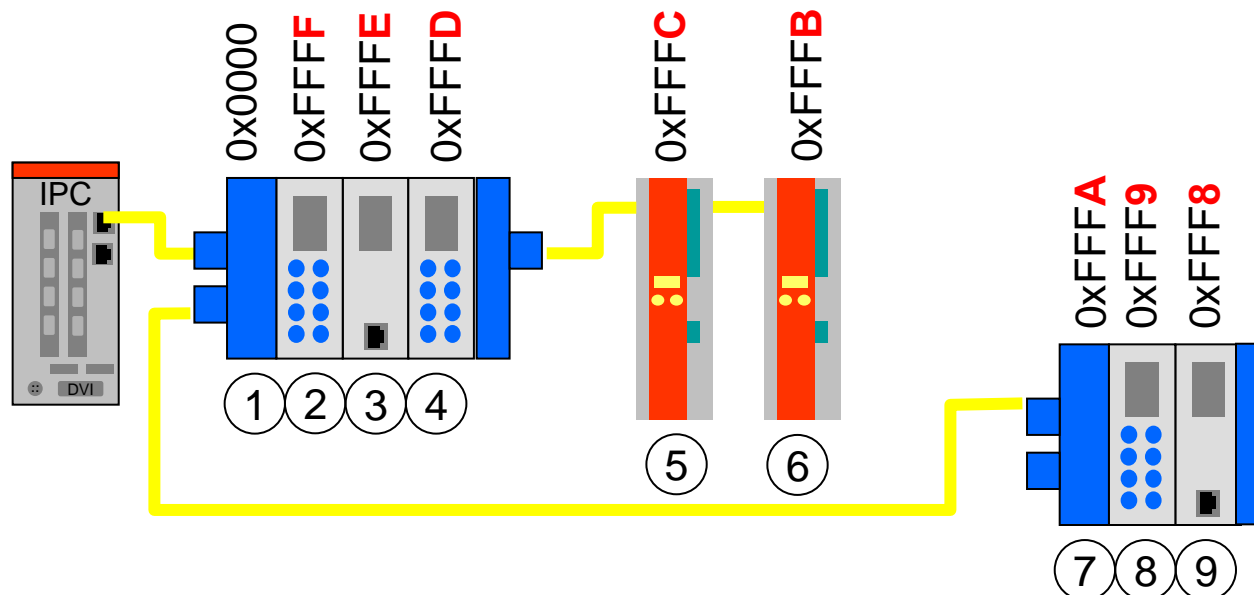
16 Bit

16 Bit

Position

Offset

- Negative Auto Increment Address for every slave depending on position (16 bit)
- Slave which reads address == 0x0000 is addressed
- Every slave increments address by 1
- Offset addresses local memory space of device
- Usually used during scan of hardware configuration



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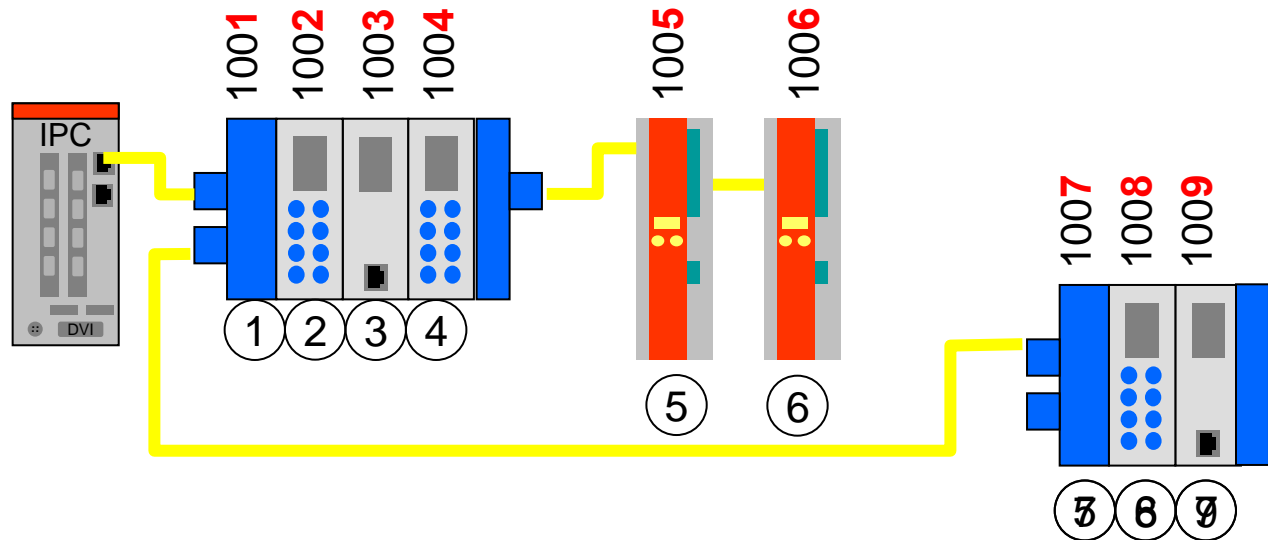
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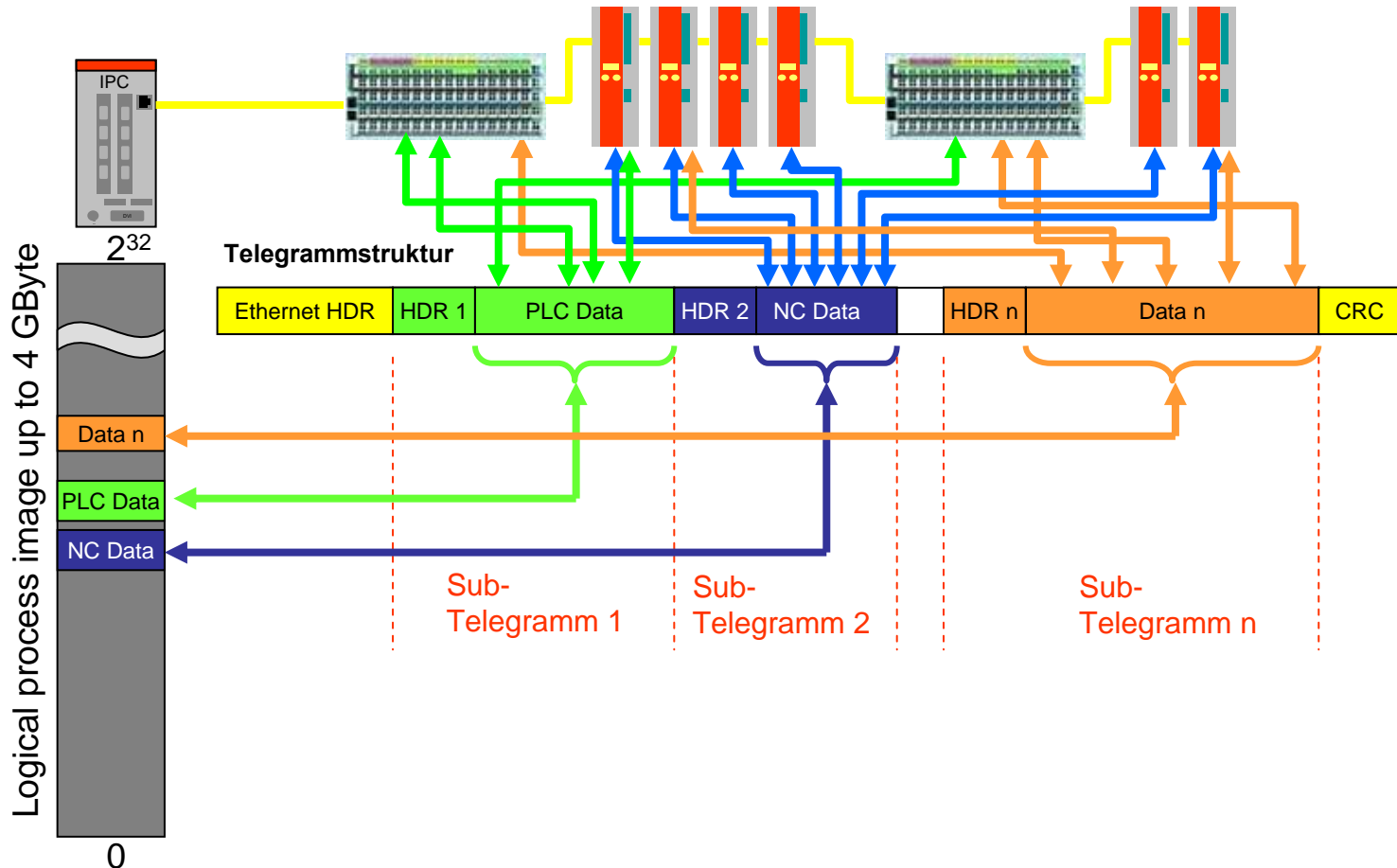
EtherCAT Master

Standards&Implementation

- Every Slave has a fixed address (16 bit)
- Usually assigned during hardware configuration scan
- Independent from slave position
- Fixed address lost after power loss



- Slave reads from/ writes its data into the 4 GByte sized Ethernet frame (fragmented)



- Different commands to optimize reading and writing for all access methods within a Fieldbus communication system

Cmd	Idx	Address	Offset	Len	R	C	R	M	IRQ
-----	-----	---------	--------	-----	---	---	---	---	-----

Cmd Type	Access	Address	Offset	Comment
NOP				No Operation
Auto Increment	R, W, RW, RMW	Position (increments)	Local Memory Address	Position value 0 (at entry) addressed
Fixed Address	R, W, RW, RMW	Address (configured)	Local Memory Address	Match address value to local address register
Broadcast	R, W, RW	(increments)	Local Memory Address	
Logical	R, W, RW	32 Bit logical address		

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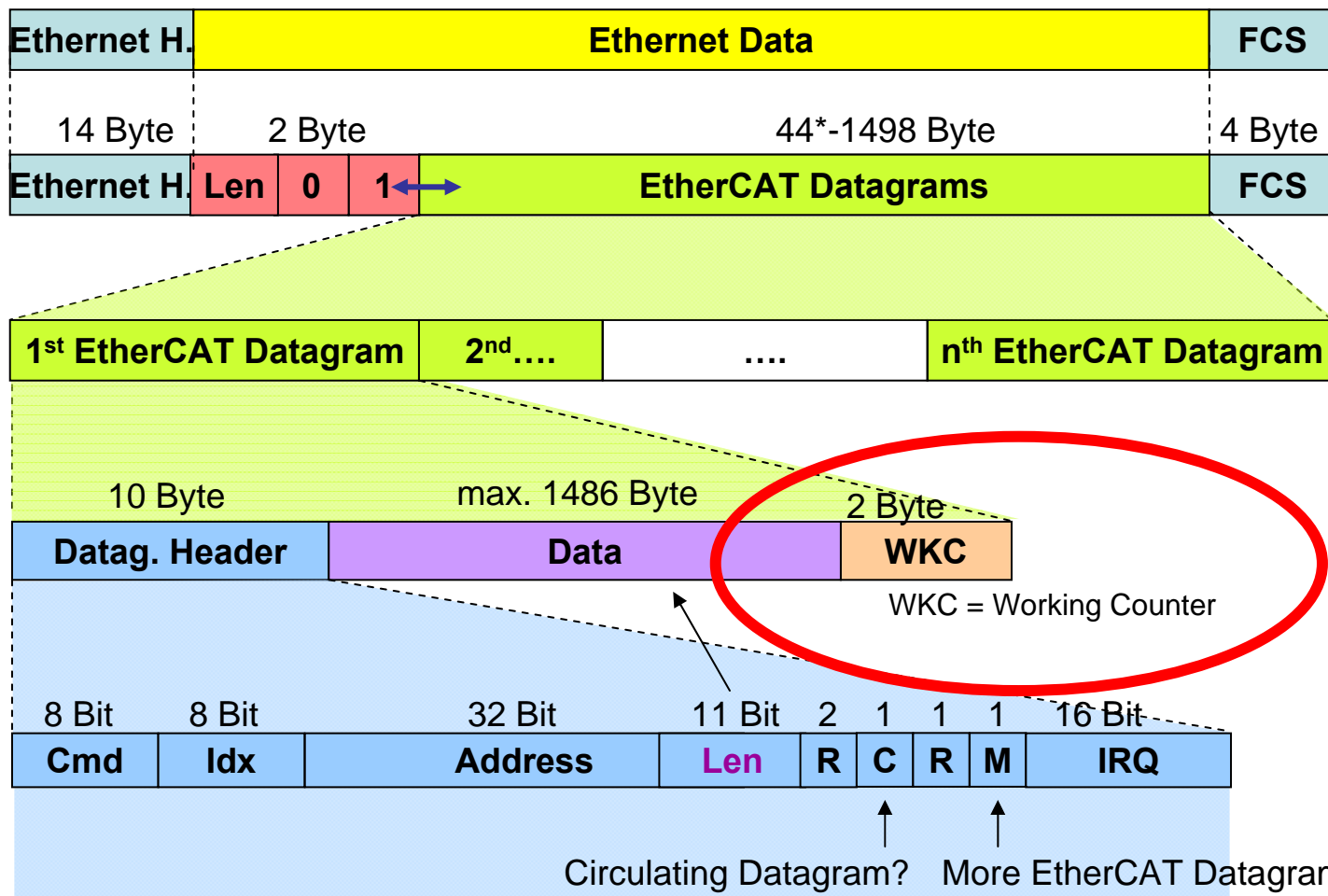
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Configuration Tool**EtherCAT Master****Standards&Implementation**

- **Broadcast Read**
 - Individual Bits of a Byte will be added with a bitwise OR operation between incoming data and local data
- **Read Write Actions**
 - Exchange of incoming data and local data (exception: Broadcast – see broadcast read)
- **Read Multiple Write Actions (RMW)**
 - Addressed Station will read the others will write

EtherCAT Datagram Header Address

* add 1-32 padding bytes if Ethernet frame is less than 64



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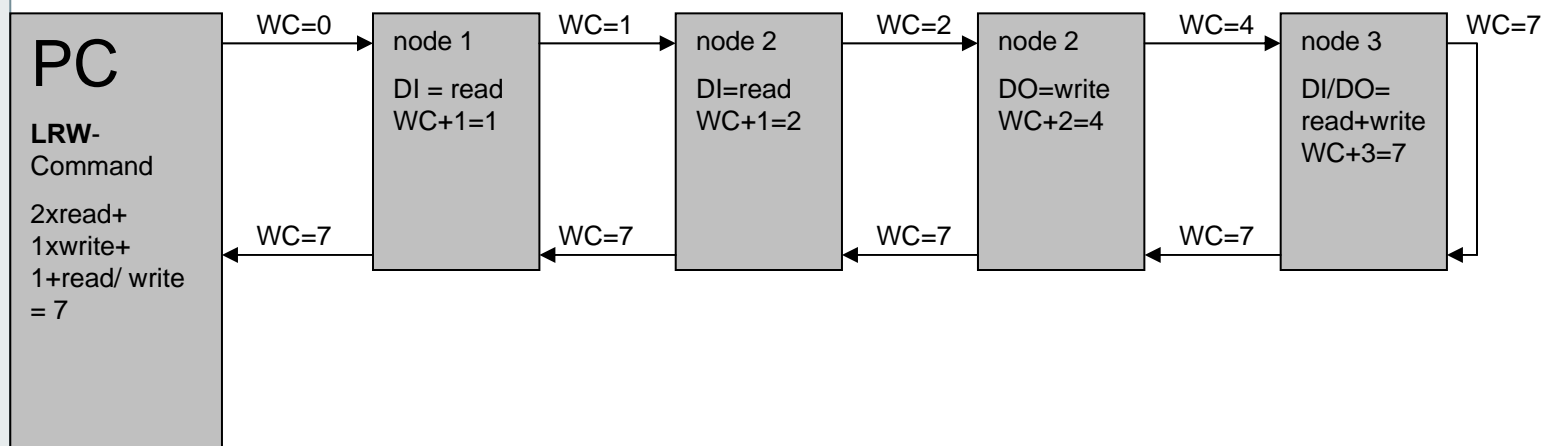
Drives

Configuration Tool**EtherCAT Master****Standards&Implementation**

- EtherCAT Datagram ends with a 16 Bit Working Counter
- Working Counter counts the number of interactions of devices addressed by an EtherCAT Datagram
- EtherCAT Slave Controller increments the Working Counter in hardware – if the controller is addressed and the addressed memory is accessible (Sync Manager)
- Each Datagram should have an expected Working counter value – calculated by the configuration tool
- The Master checks the valid processing of EtherCAT Datagrams by comparing the Working Counter with the expected value
- Special case: RW addressing methods will increment WKC by 2 for write access and by 1 for read access

Working Counter Example

- WKC valid: data of this datagram was written to and read from all addressed devices
- WKC invalid: memory of one or more devices was not accessible



Working Counter Example

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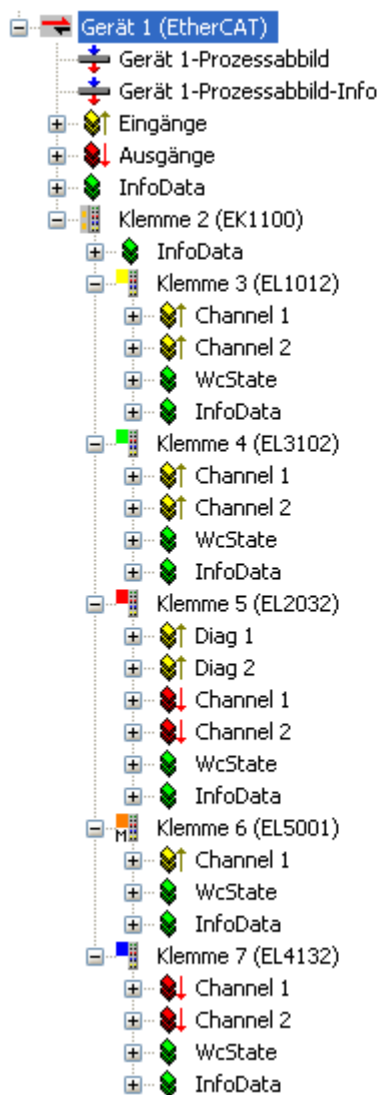
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EtherCAT Master

Standards&Implementation



WC+1

WC+1

WC+3

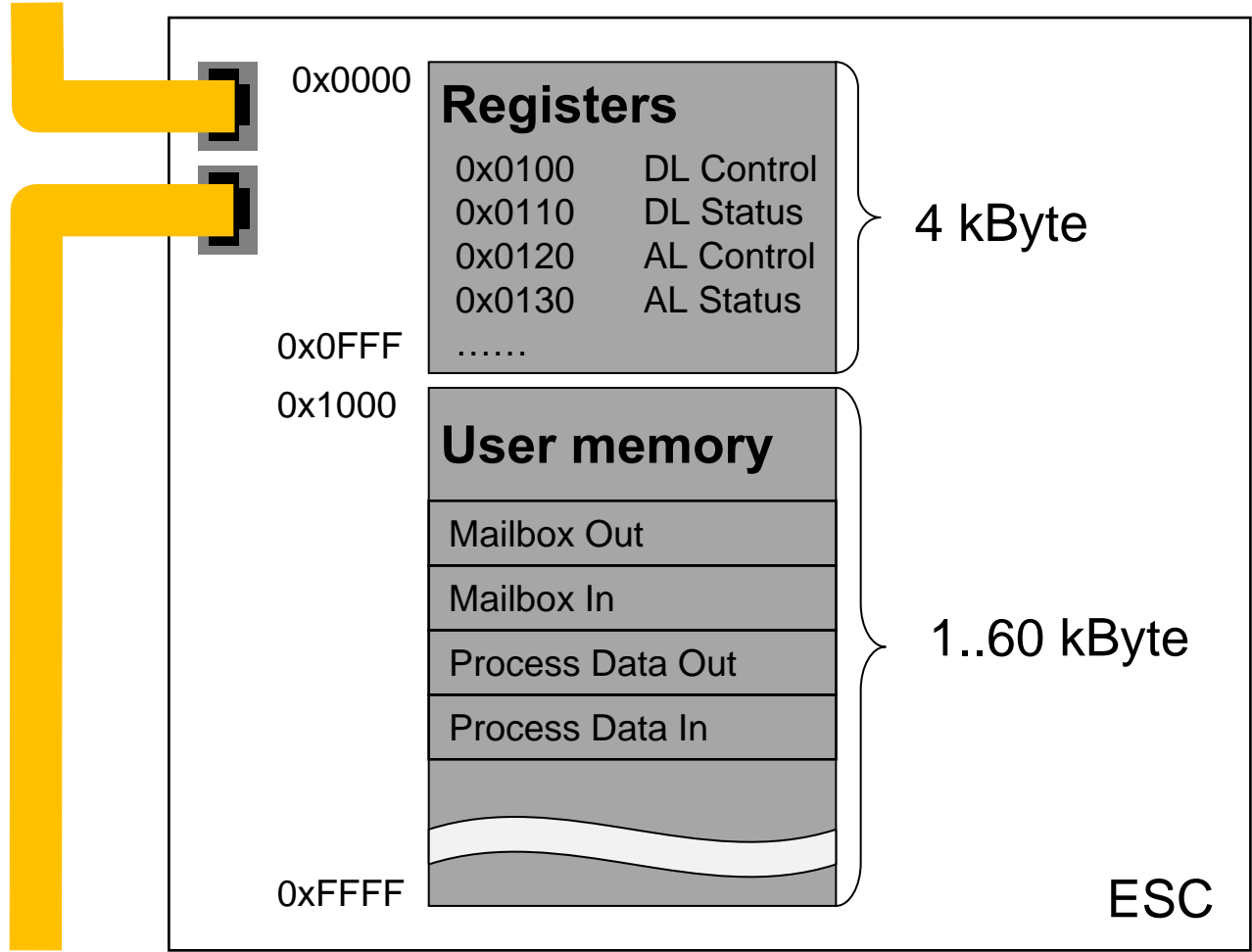
WC+1

WC+1

Frame	Cmd	Addr	Len	WC
0	LRW	0x00010000	1	3
0	LWR	0x00010800	4	1
0	LRD	0x00011000	12	3
0	LRD	0x00080000	1	3
0	BRD	0x0000000130	2	6

Nummer	Boxbezeichnung	Adresse
1	Klemme 2 (EK1100)	1001
2	Klemme 3 (EL1012)	1002
3	Klemme 4 (EL3102)	1003
4	Klemme 5 (EL2032)	1004
5	Klemme 6 (EL5001)	1005
6	Klemme 7 (EL4132)	1006

Local Address Space of ESC



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Configuration Tool**EtherCAT Master****Standards&Implementation**

- 64 kByte address space
- Divided into registers and dual ported RAM (DPRAM)
- First 4 kByte are reserved for registers
- DPRAM starts at 1000h
- DPRAM size depends on Slave Controller implementation (up to 60 kByte, 4kByte in actual FPGA implementation)
- Addressing of registers and DPRAM same
- Register Write is different – shadow Register for all Registers integrated
DPRAM write is not shadowed

Register of EtherCAT Slave Controller

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Configuration Tool

EtherCAT Master

Standards&Implementation

- First 1000h bytes (4 kBytes) of local address space
- Read access for both sides (EtherCAT and application)
- Write access from EtherCAT for most of the registers
 - Master has to configure the Slave Controller
 - No address settings needed
 - FMMU and Sync Manager configuration can be optimized for available bandwidth and cycle times
 - Exceptions that are writable from the application side:
 - AL Status Register, AL Status Code Register, AL Event Mask Register, Sync Manager Disable Registers, AL Identification Registers
- Process Data Interface (PDI) register initialized from Slave Information interface (Serial EEPROM)

- Registers might be monitored via configuration tool

Advanced Settings

- General
- Mailbox
- Distributed Clock
- ESC Access
 - E2PROM
 - FPGA
 - Memory

Start Offset: 0000

Length: 0400

Working Counter: 1

☐ Auto Reload
☐ Compact View
☐ Use Fixed Addr

Offs	Dec	Hex	Char
0004	SM/FMMU Cnt	1028	0404 ..
0006	Ports/DPRAM	4	0004 ..
0008	Features	4	0004 ..
0010	Phys Addr	1001	03e9 ..
0012	Phys Addr 2nd	0	0000 ..
0020	Register Protect	0	0000 ..
0030	Access Protect	0	0000 ..
0100	ESC Ctrl	1	0001 ..
0102	ESC CtrlEx	7	0007 ..
0108	Phys. RW Offset	0	0000 ..
0110	ESC Status	22035	5613 .V
0120	AL Ctrl	8	0008 ..
0130	AL Status	8	0008 ..
0134	AL Status Code	0	0000 ..
0140	PDI Ctrl	5	0005 ..

Bits	Name	Value	Enum
0-3	AI Status	8	OP
4	Error	0	

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Configuration Tool

EtherCAT Master

Standards&Implementation

- Register description for every ESC (FPGA/ ASIC)
 - DL Information, DL Control, DL Status, DL Address
 - AL Control, AL Status, AL Event
 - SyncManager + FMMU configuration
 - Distributed Clocks
 - Slave Information interface (Serial EEPROM)

Address	Length (Byte)	Description	EtherCAT Access	PDI Access
0x0100:0x0103	4	DL Control	r/w	r/-
0x0108:0x0109	2	Physical Read/Write Offset	r/w	r/-
0x0110:0x0111	2	DL Status	r/w	r/-
0x0120:0x0121	2	AL Control	r/w	r/-
0x0130:0x0131	2	AL Status	r/w	r/-
0x0134:0x0135	2	AL Status Code	r/-	r/w
0x0140:0x0141	2	PDI Control	r/w	r/w

Enable Disable Ports

Control of the device state machine

Status of the device state machine

Error Code

32 Bit I/O, SPI, µC Interface

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Standards&Implementation

- SyncManager protects a DPRAM section from simultaneous access → data consistency
- Up to 16 independent SyncManger channels possible
- The SyncManager configuration registers start at address 0x0800

SyncManager Types

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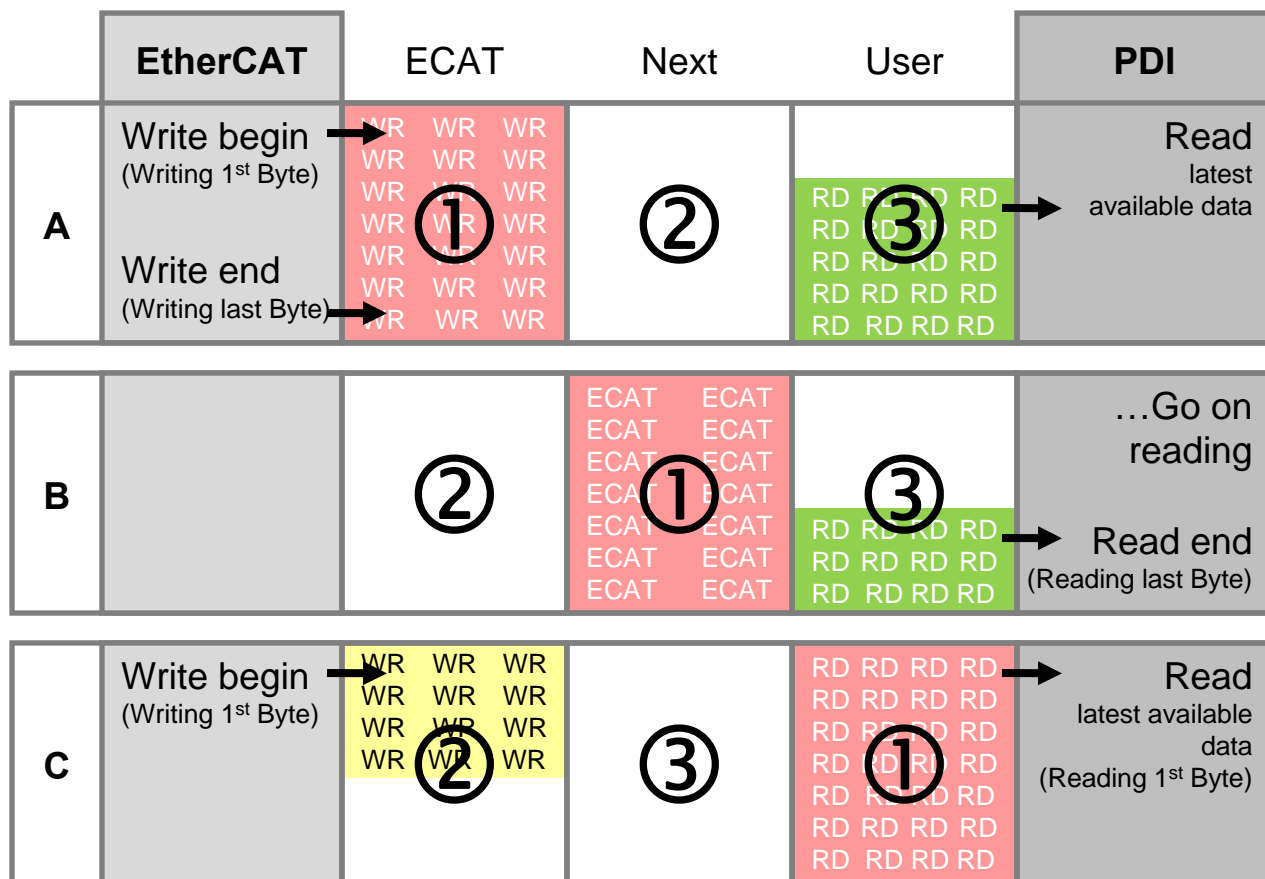
EtherCAT Master

Standards&Implementation

- Mailbox Type
 - 1 buffer SyncManager supports handshake
 - Data overflow protection
 - Writing side must write before reading side can read
 - Reading side must read before writing side can write again
- Buffered Type
 - 3 buffer SyncManager guarantees consistent data delivery and access to the newest data any time
 - Always a free buffer to write
 - Always a consistent buffer to read (except before the first writing)
 - Usually used for process data communication

Buffered Type (3 buffers) Write Example

- Characteristic: Data always available for both sides
- Requires 3 (consecutive) memory areas



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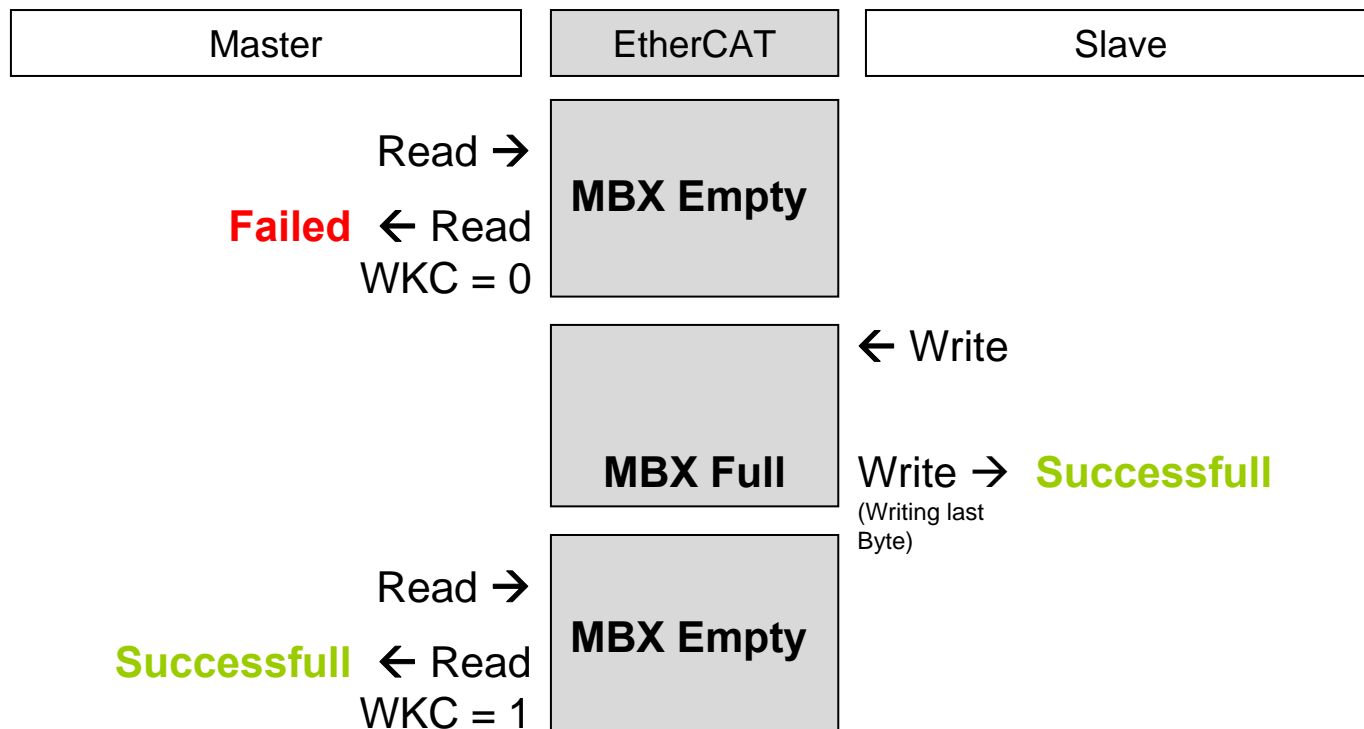
Configuration Tool

EtherCAT Master

Standards&Implementation

Mailbox Type (1 buffer) Read Example

- Allows handshake Communication
- Useful for non-Process Data
- Handshake mechanism – one side has control



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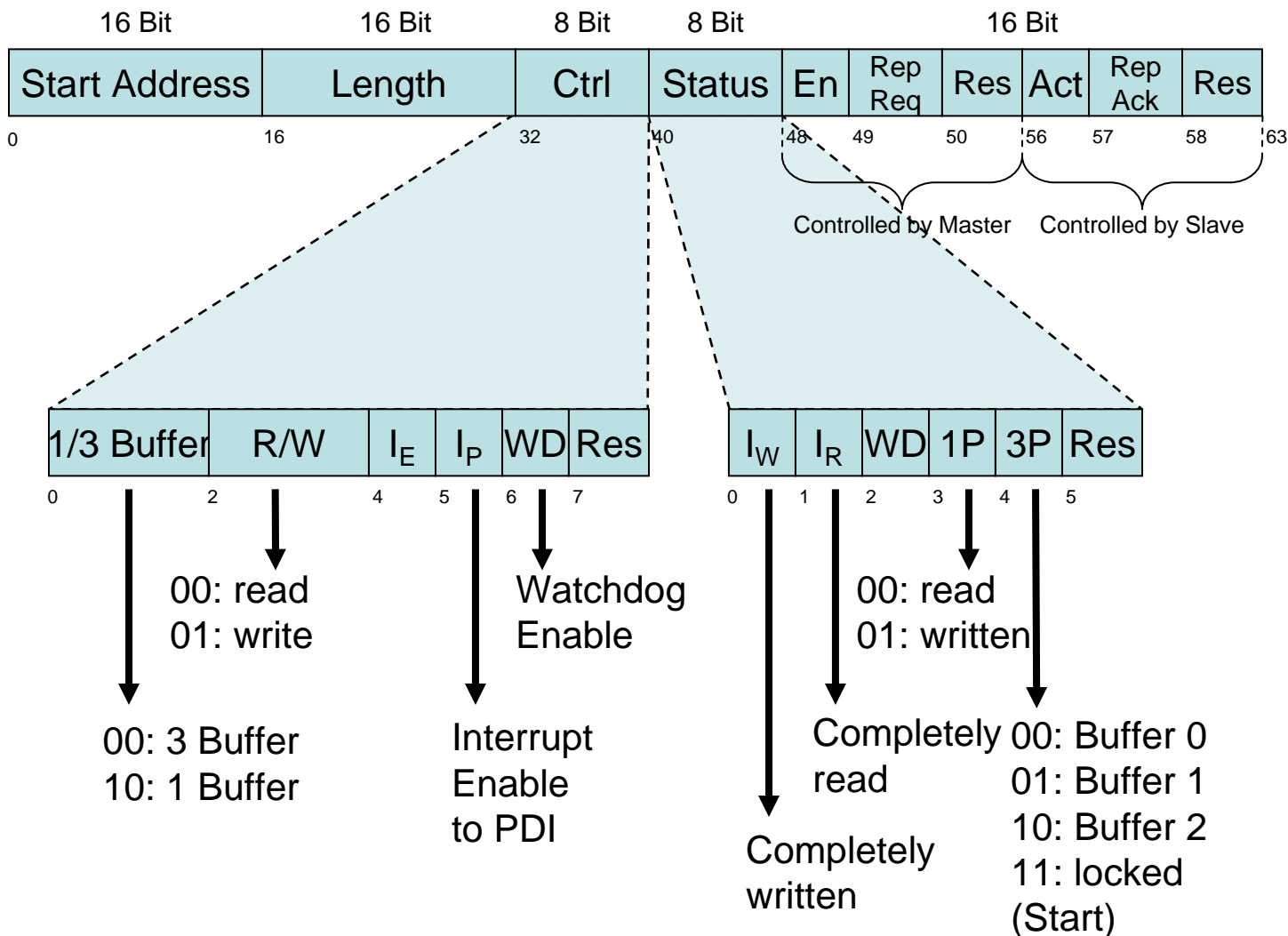
EtherCAT Master

Standards&Implementation

SyncManager channel configuration registers

SM register is from $0x0800+y*0x08$ to $0x0807+y*0x08$

y = SM index range 0..15



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Configuration Tool**EtherCAT Master****Standards&Implementation**

- Standard assignment
 - With mailbox support
 - SM0: Mailbox output
 - SM1: Mailbox input
 - SM2: Process data outputs
 - SM3: Process data inputs
 - Readable via CoE object 1C00h
 - Without mailbox support
 - SM0: Process data outputs
(or inputs if no outputs available)
 - SM1: Process data inputs
- Extended assignment
 - SM0: Mailbox output
 - SM1: Mailbox input
 - SM left are configurable via CoE object 1C00h
- One or more PDO always fits exactly into a SyncManager

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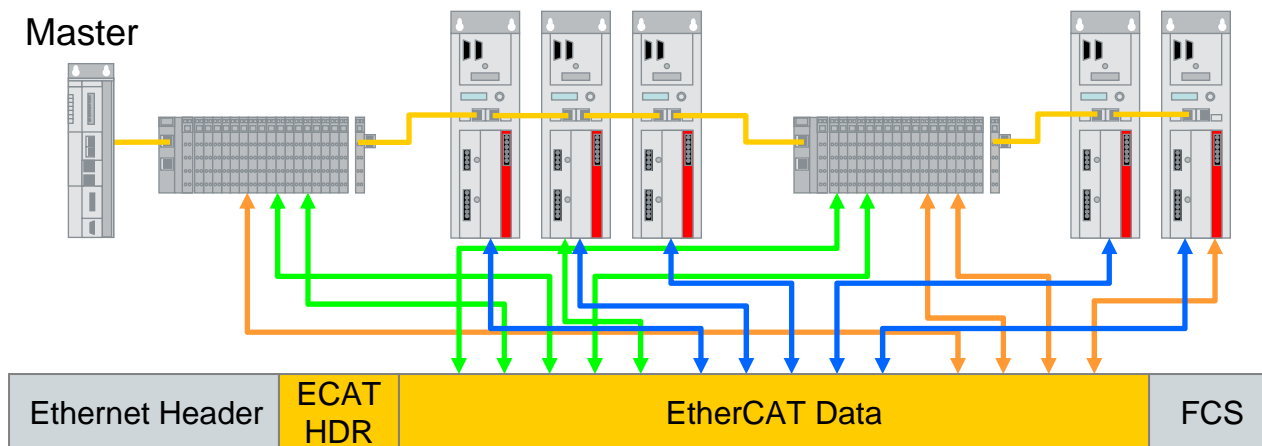
- Maps a section of the local address space into the global address space and vice versa
- Read and write access distinguishable
- Bitwise configuration of the memory section possible
- Up to 16 independent FMMU channels possible
 - FMMU configuration registers start at address 0x0600

Operation samples:

- Mapping of process data into the global address space
- Mapping of status bits from the register section
 - Access to device specific status information with a minimum overhead – e.g. fill status of a sync manager channel

FMMU Usage for Addressing

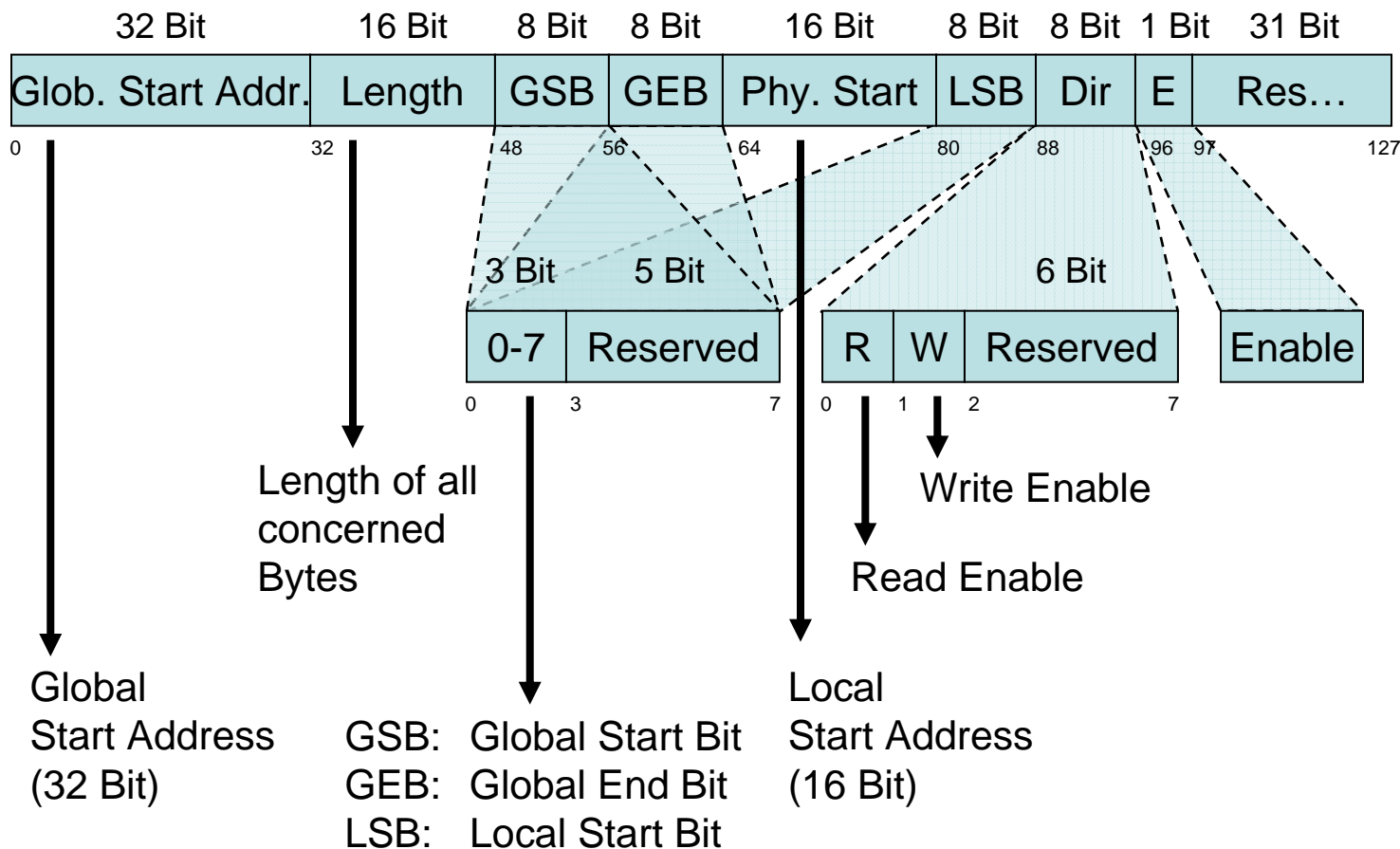
- Global address space
- 4 GByte address space
- Mapping to local addresses by
Fieldbus Memory Management Units (FMMU)
- All EtherCAT devices can map data in a single EtherCAT Datagram LRW – depending on the FMMU configuration



FMMU entity configuration registers

FMMU register is from $0x0600+y*0x10$ to $0x060F+y*0x10$

y = FMMU index range 0..15



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FMMU Register from 0x6000 to 0x06FF

Reg. Addr. Offset	Lenth in Byte	Description
+0x0:0x3	4	Logical Start Address
+0x4:0x5	2	Length
+0x6	1	Logical Start bit
+0x7	1	Logical Stop bit
+0x8:0x9	2	Physical Start Address
+0xA	1	Physical Start bit
+0xB	1	Type
+0xC	1	Activate
+0xD:0xF	3	Reserved

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1. Master reads hardware configuration including input and output data length of each slave
2. Master organizes mapping of process data
3. Master distributes information (start address etc.) for every slave about where process data in logical process image is provided
4. Process data communication starts

FMMU example

- Map 6 Bits form logical address 0x14711.3 to 0x14712.0 to the physical register bits 0x0F01.1 to 0x0F01.6.
 - The FMMU length is 2 Byte, since the mapped bits span 2 Bytes of the logical space.

FMMU config. register	FMMU reg. offset	Value
Logical Start Address	0x0:0x3	0x00014711
Length (Byte)	0x4:0x5	0x0002
Logical Start Bit	0x6	0x03
Logical Stop Bit	0x7	0x00
Physical Start Address	0x8:0x9	0x0F01
Physical Start Bit	0xA	0x01
Type	0xB	Rand and/or Write
Activate	0xC	1 (enable)

Note: FMMU configuration registers start at address 0x0600

Diagnosis at Data Link – Possible Errors

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Standards&Implementation

Error situation	Detection
Wrong Station	Check EEPROM, Vendor, Device, SerialNo
Transmission Errors	Check Transmission Error Counter of each station
Change between In and Out Cable	Check Link Status of last device (should have only a single port connected) (Only for 2 Port devices)
Link loss/ Station fault	Check working counter of a Broadcast read
Frame loss	Close loop in the middle Check errors again If no frame loss repeat this in the 2nd half of segment Otherwise repeat this in the first half of segment Repeat this until only a single station remains, which should be the station with problems

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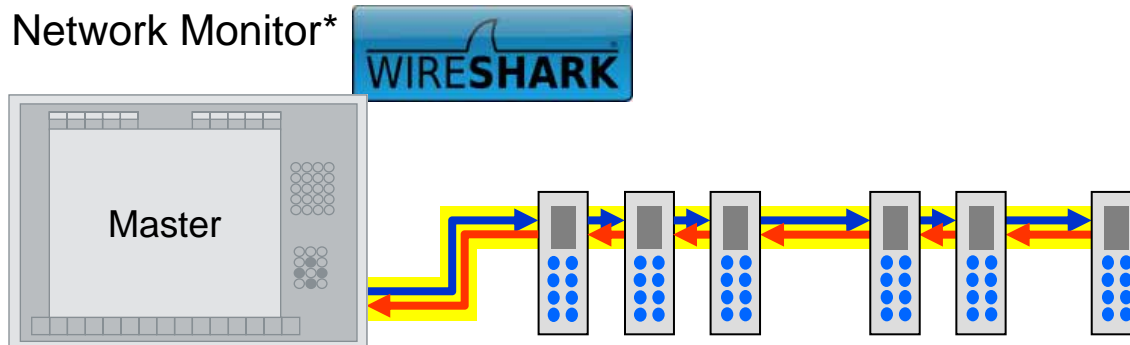
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EtherCAT Master

Standards&Implementation



- Masters sends an EtherCAT Frame (broadcast)
→ Monitor gets the first copy (unprocessed)
- Frame returns from EtherCAT Slave Devices
→ Monitor gets the second copy (processed)

*Attention: At low cycle times order of frames might be mixed because of timing restrictions within NDIS protocol driver.

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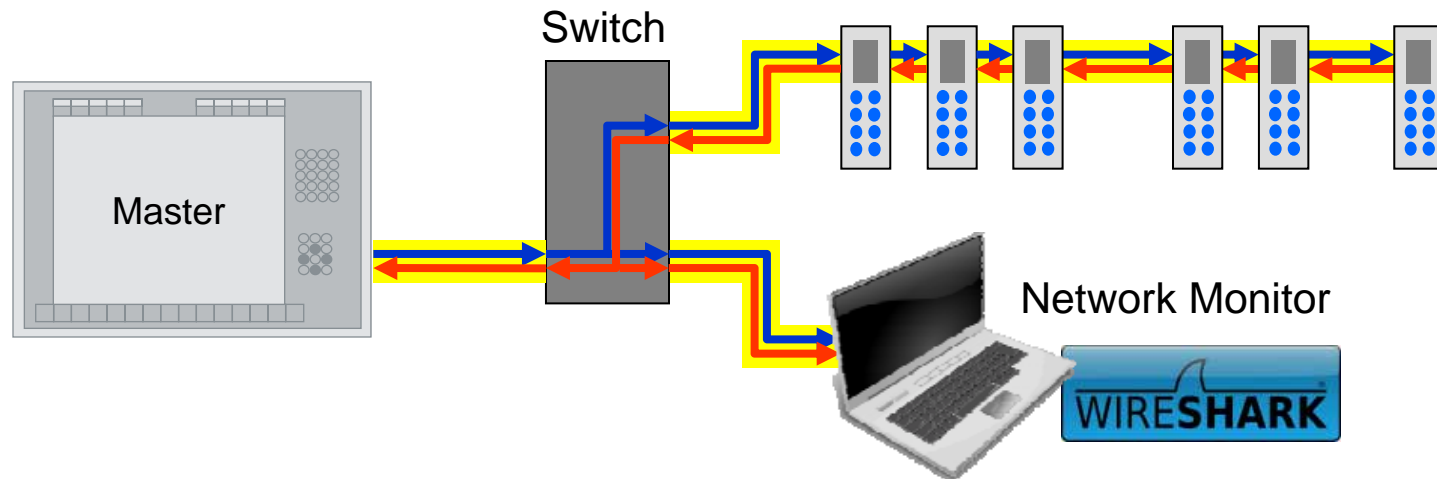
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Standards&Implementation



- Masters sends an EtherCAT Frame (broadcast)
→ Monitor gets the first copy (unprocessed)
- Frame returns from EtherCAT Slave Devices
→ Monitor gets the second copy (processed)

☒ Real-time performance is limited

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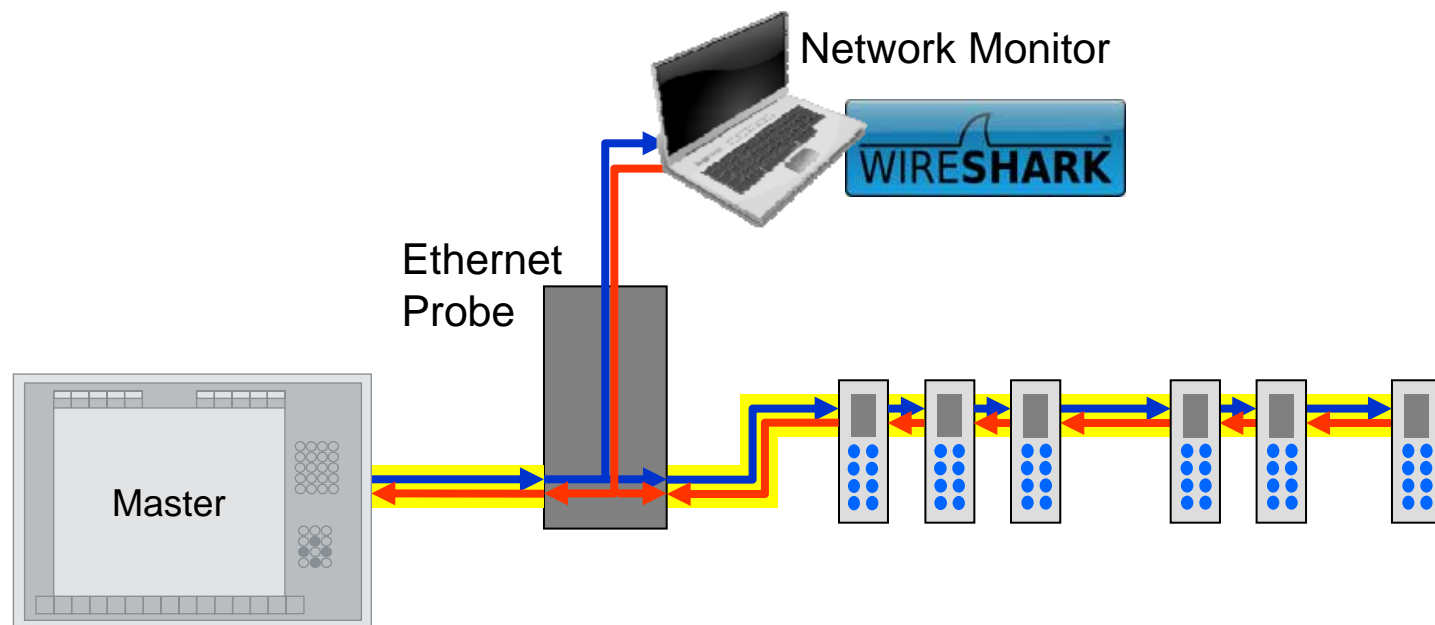
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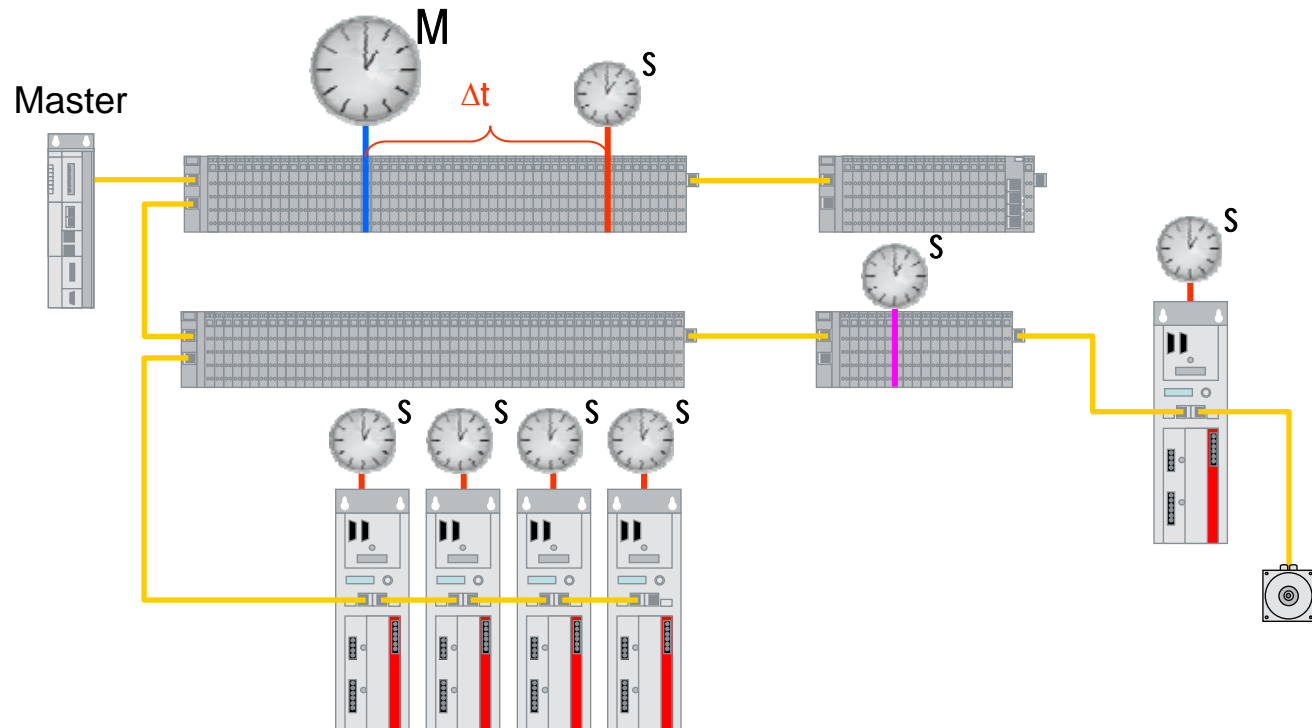


- Masters sends an EtherCAT Frame (broadcast)
→ Monitor gets the 1st copy (unprocessed) with Timestamp
- Frame returns from EtherCAT Slave Devices
→ Monitor gets the 2nd copy (processed) with Timestamp

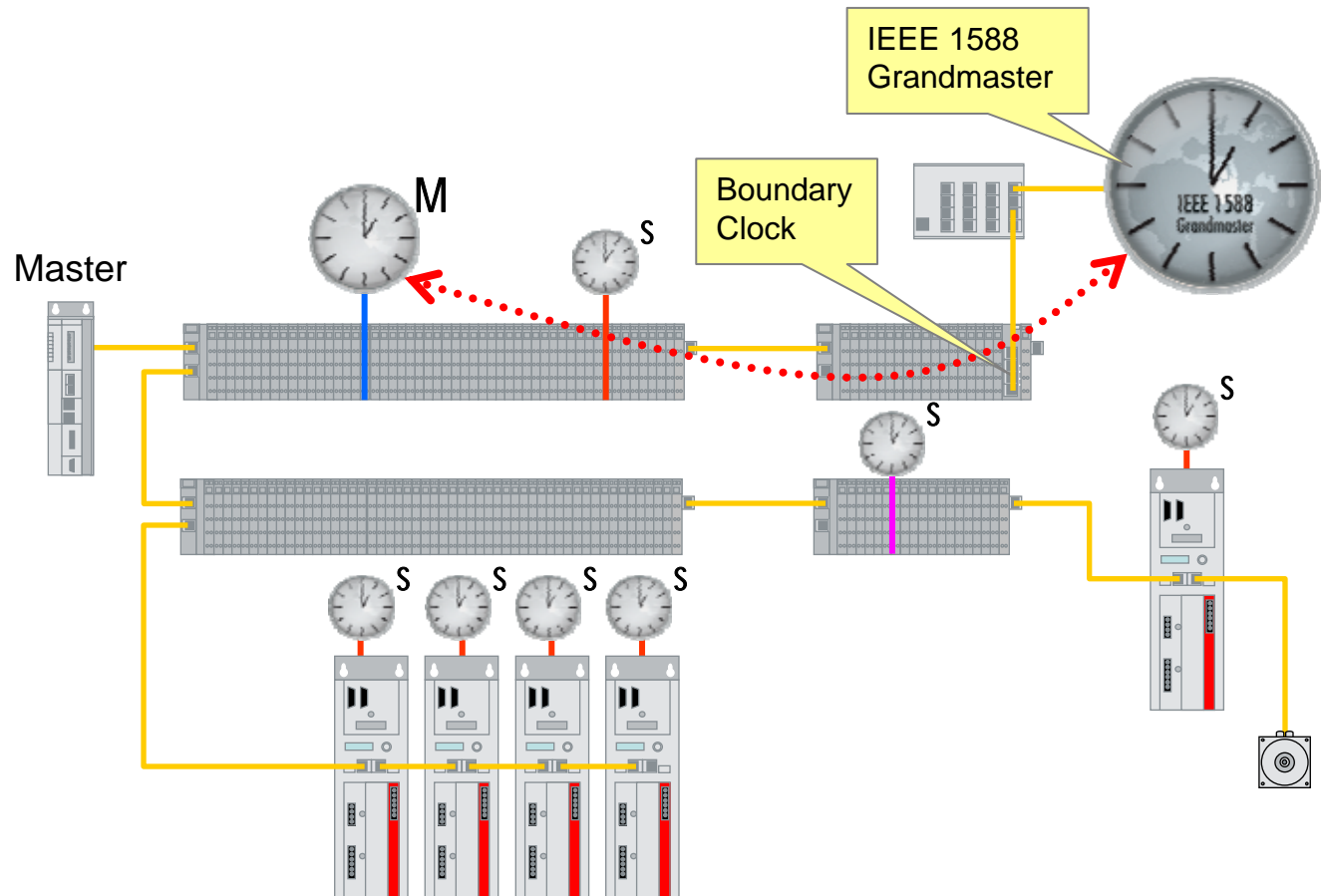
☑ Real-time performance is not affected (no jitter, no delay)

Purpose of Distributed Clocks (DC)

- Precise Synchronization ($\ll 1 \mu\text{s}$!) by exact adjustment of Distributed Clocks



- Switchport with integrated IEEE 1588 Boundary Clock



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Standards&Implementation

- Synchronization of EtherCAT devices
- Definition of a System Time
 - Beginning on January, 1st 2000 at 0:00h
 - Base unit is 1 ns
 - 64 bit value (enough for more than 500 years)
 - Lower 32 bits spans over 4.2 seconds
 - Normally enough for communication and time stamping
- Definition of a Reference Clock
 - One EtherCAT Slave will be used as a Reference Clock
 - Reference Clock distributes its Clock cyclically
 - Reference Clock adjustable from a “global” Reference Clock – IEEE 1588

Distributed Clocks Unit

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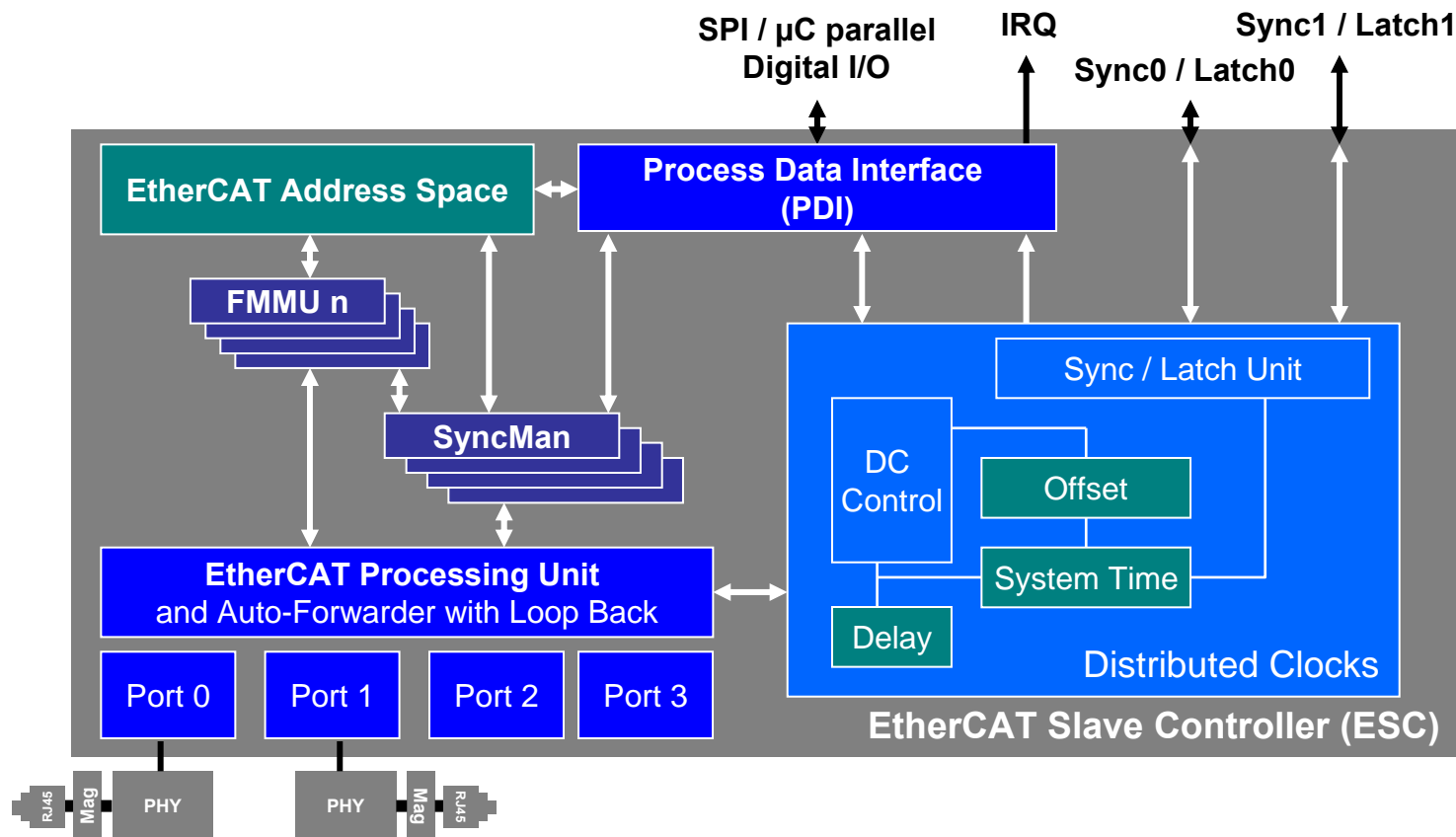
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Standards&Implementation

- Provider for local time signals
 - Generation of synchronous local output signals (SYNC0, SYNC1 Signals)
 - Generation of synchronous Interrupts
- Synchronous Digital Output updates and Input sampling
- Precise time stamping of input events (Latch unit)

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EtherCAT Master

Standards&Implementation

- **Propagation delay** measurement support
 - Each EtherCAT slave controller measures the delay between the two directions of a frame
 - Master calculates the propagation delays between all slaves
- **Offset compensation** to Reference Clock
 - Offset between local clock and Reference Clock
 - Same absolute system time in all devices
 - Simultaneousness (clear below 100ns difference) in all devices
- **Drift compensation** to Reference Clock
 - DC Control Unit

Register System Time

EtherCAT Basics

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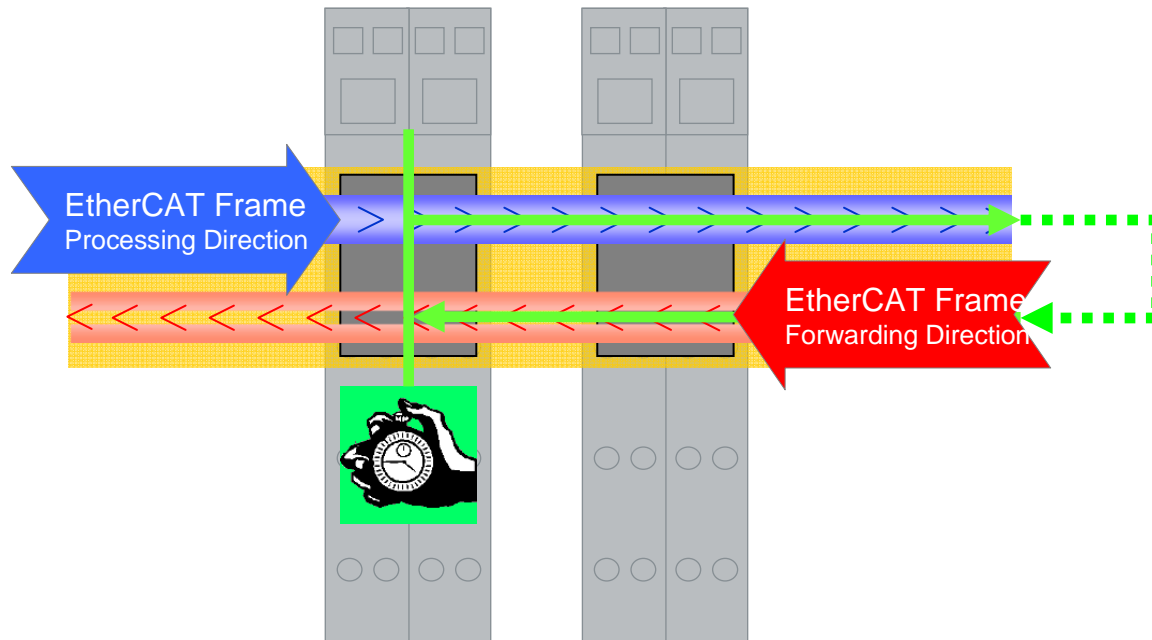
EtherCAT Master

Standards&Implementation

- Registers:
 - *System Time*
(0x0910:0x0917, small systems 0x0910:0x0913)
- *System Time*
 - Read access from both sides (EtherCAT and μ C)
 - Consistent access from μ C
(access to first byte saves an internal copy)
 - Consistent access from EtherCAT
(within a single frame, internally latched with SOF)
 - Write access from EtherCAT starts the DC control
 - ARMW command (auto increment read – multiple write)
allows to read *System Time* of the reference clock and write it to all slave clocks within a single frame

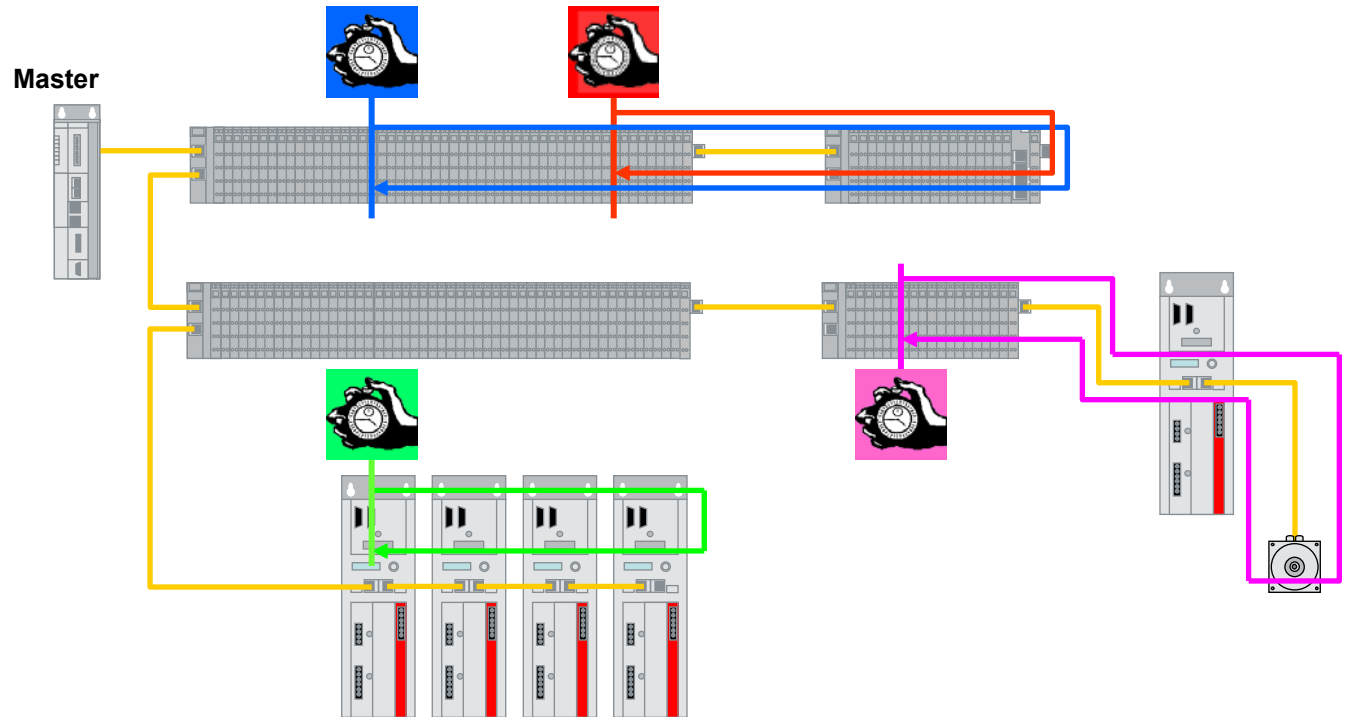
DC - Propagation Delay Measurement (1)

- EtherCAT Node measures time difference between leaving and returning frame



DC - Propagation Delay Measurement (2)

- EtherCAT Node measures time difference between leaving and returning frame



Propagation Delay Measurement

EtherCAT Basics

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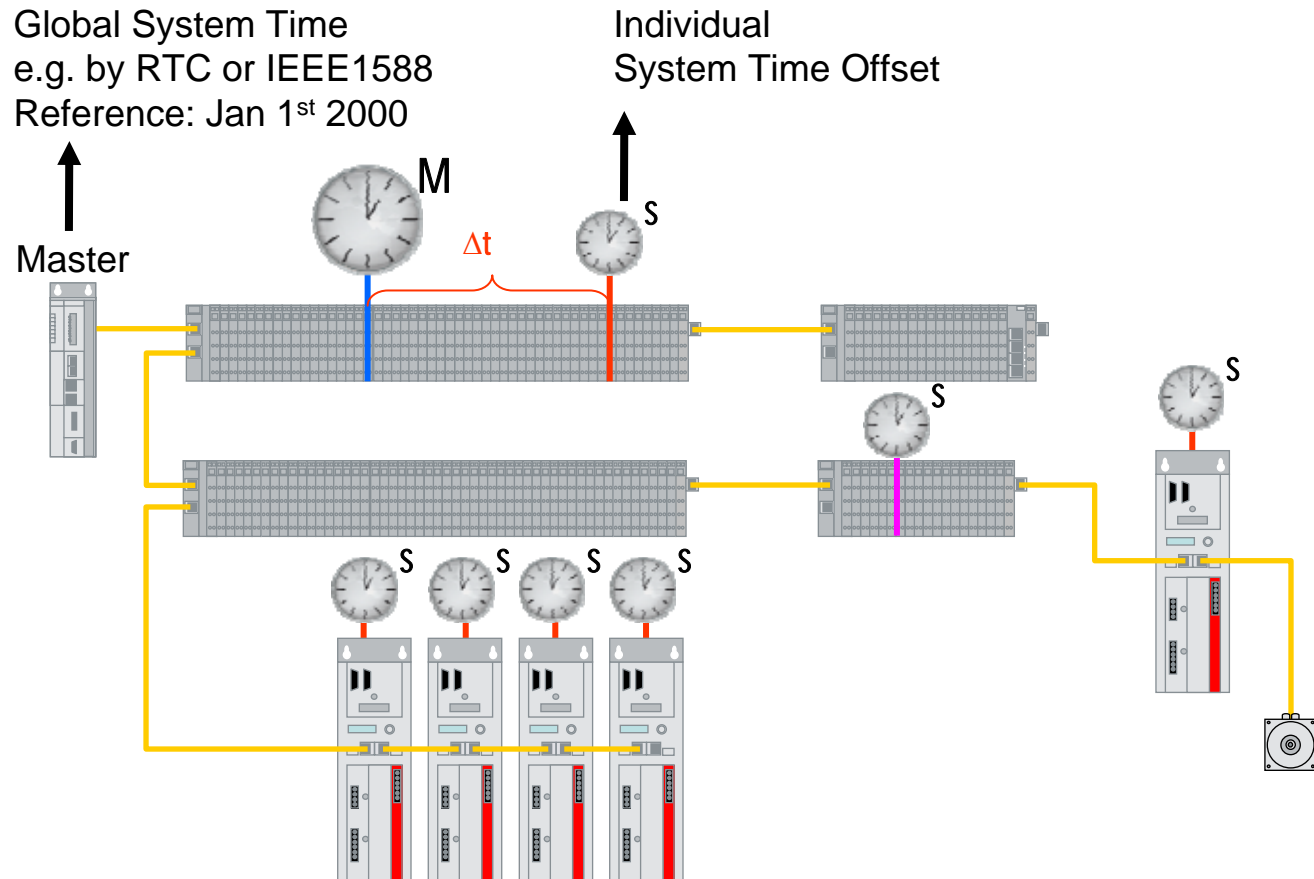
EtherCAT Master

Standards&Implementation

- Registers:
 - *Receive Time Port 0* (0x0900:0x0903)
 - *Receive Time Port 1* (0x0904:0x0907)
 - *Receive Time Port 2* (0x0908:0x090B)
 - *Receive Time Port 3* (0x090C:0x090F)
 - *System Time Delay* (0x0928:0x092B)
- Write access to *Receive Time Port 0* activates latch
 - Latch local time of SOF (Start of Frame)
 - At EOF (End of Frame) SOF time is copied to *Receive Time Port X*
- *Receive Time Port X* in local clock units (controlled)
- SOF time of all frames are latched on all ports internally
- Master reads all time stamps and calculates the delay times with respect to the topology.
- Individual delay time is written to register *System Time Delay*

Offset and Drift Compensation

- Same System Time in all devices



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Standards&Implementation

- Registers:
 - *System Time Offset*
(0x0920:0x927, small systems 0x0920:0x0923)
- Difference between the Reference Clock and every slave device's clock is calculated by the master.
- The offset time is written to register *System Time Offset*
- Each slave calculates its local copy of the System time using its local time and the local offset value:
 - $t_{\text{Local copy of System Time}} = t_{\text{Local time}} + t_{\text{Offset}}$

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Standards&Implementation

- Registers:
 - System Time
(0x0910:0x0917, small systems 0x0910:0x0913)
 - System Time Offset
(0x0920:0x0927, small systems 0x0920:0x0923)
- ARMW command (auto increment read – multiple write) allows to read *System Time* of the reference clock and write it to all slave clocks within a single frame
- DC Control
 - Write access to *System Time* compares received Time with local time
$$\Delta t = (t_{\text{Local time}} + t_{\text{Offset}} - t_{\text{PropagationDelay}}) - t_{\text{Received Time}}$$
 - If ($\Delta t > 0$) then decelerate local clock
else if ($\Delta t < 0$) accelerate local clock

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EtherCAT Master

Standards&Implementation

• Initialization with Propagation Delay compensation

1. Master reads the DL Status register of all slaves and calculates the network topology.
2. Master sends a broadcast write to Receive Time Port 0 register (at least first byte). All slaves latch the local time of the first preamble bit of this frame at all ports and at the ECAT Processing Unit. Some ESCs need the EtherCAT network to be free of frames before the broadcast write is sent.
3. Master waits until the broadcast write frame has returned.
4. Master reads all Receive Time Port 0-3 registers (depending on the topology and the Receive Time ECAT Processing Unit register (0x0918:0x091F) which contains the upper 32 bits of the receive times.
5. Master calculates individual propagation delays and writes them to System Time Delay registers of the slaves. Possible overruns of the 32 bit Receive Times have to be checked and taken into account.
6. Master sets System Time Offset register of the Reference Clock so that the Reference Clock is bound to the master time. The offset for the Reference Clock is master time minus Receive Time ECAT Processing Unit (local time) of the Reference Clock.
7. Master calculates System Time offsets for all DC slaves and writes them to the System Time Offset registers. The offset of each slave is Receive Time ECAT Processing Unit from Reference Clock minus Receive Time ECAT Processing Unit from each DC slave.
8. For static drift compensation, the master sends many separate ARMW or FRMW drift compensation frames (e.g., 15,000 frames) to distribute the System Time of the Reference Clock to all DC slaves.
9. For dynamic drift compensation, the master sends ARMW or FRMW commands periodically to distribute the System Time of the Reference Clock to all DC slaves. The rate of the drift compensation commands depends on the acceptable maximum deviation.

SyncSignal Generation

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Standards&Implementation

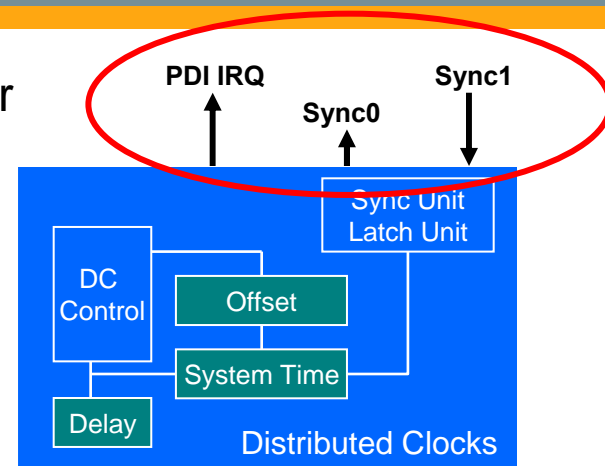
- Output of the Sync unit can be used for
 - Interrupt generation
 - PDI Digital Output Update events

- Can be mapped to
 - *AL Event Request* Register for PDI IRQ
 - SYNC0 and SYNC1

- SyncSignals can be generated at a specific System Time
- Four Operation modes are supported:

- Cyclic generation
- Single shot
- Cyclic acknowledge
- Single shot acknowledge

- The second SyncSignal (SYNC1) depends on SYNC0, it can be generated with a predefined delay after SYNC0 pulses
- Initiated alternatively from the EtherCAT master or slave application side



Registers for SyncSignal Generation

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Standards&Implementation

Register Address	Name	Description
0x0140[11:10]	PDI Control	Enable/Disable DC Units (power saving)
0x0151	Sync/Latch PDI Configuration	Configuration of SYNC/LATCH[1:0] pins
0x0980.0	Cyclic Unit Control	Assignment of cyclic function to EtherCAT or PDI
0x0981	Activation	Activation of cyclic function and SYNC pins
0x0982:0x0983	Pulse Length of SYNC signals	Length of SYNC impulse length
0x098E	SYNC0 Status	Status of SYNC0 signal
0x098F	SYNC1Status	Status of SYNC1 signal
0x0990:0y0997	SYNC0 Start Time	Start Time of cyclic operation
0x0998:0x099F	NEXT SYNC1 Pulse	Next Sync1 Pulse
0x09A:0x09A3	SYNC0 Cycle Time	Cycle Time of SYNC0
0x09A4:0x09A7	SYNC1 Cycle Time	Cycle Time of SYNC1

SYNC0 Signal Generation modes

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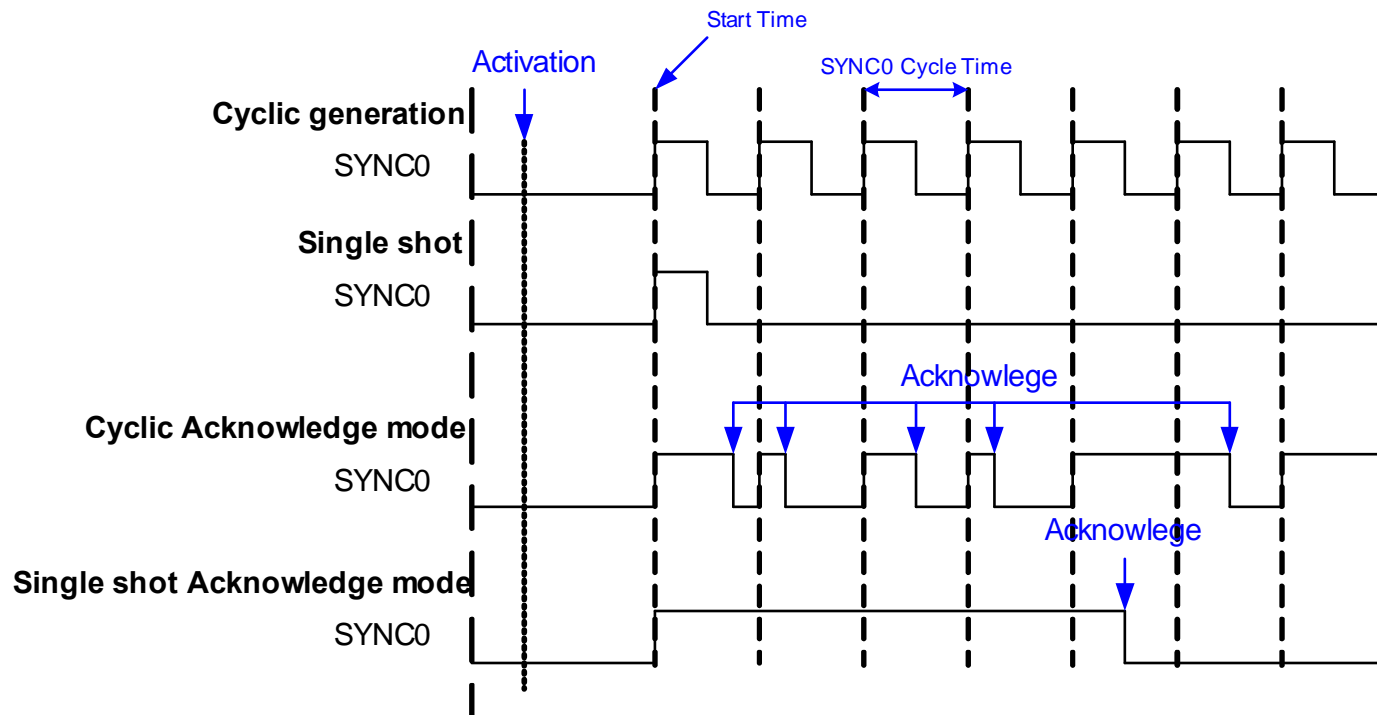
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Pulse Length of SYNC Signals (0x0982:0x0983)	SYNC0 Cycle Time (0x09A0:0x09A3)	
	> 0	= 0
> 0	Cyclic generation	Single Shot
= 0	Cyclic Acknowledge*	Single Shot Acknowledge*

* Acknowledge by reading SYNC status register (0x098E, 0x098F)

SYNC1 Signal Generation modes

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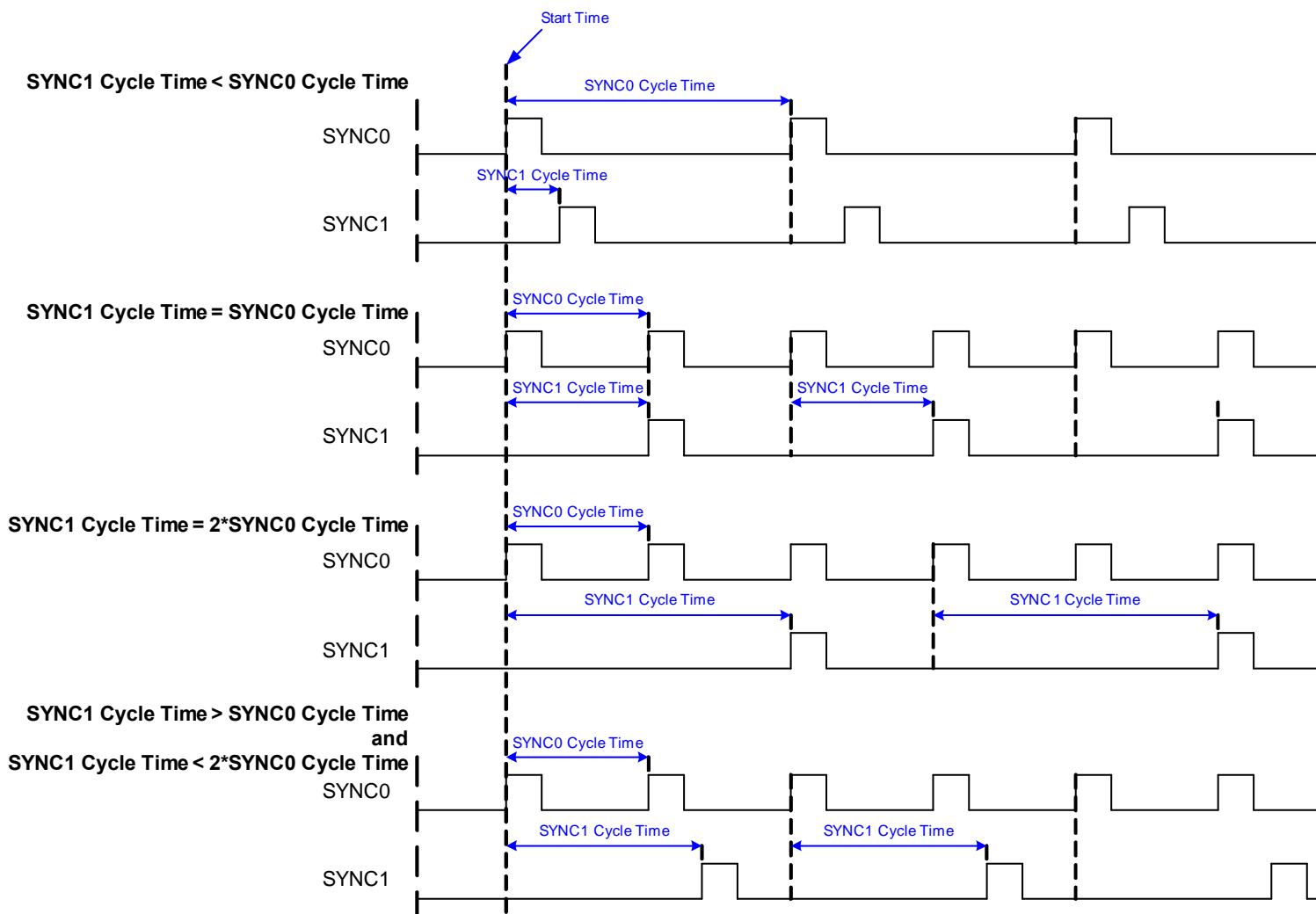
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Standards&Implementation

- The Latch Event unit supports time stamping of the system time with two independent input signals
 - LATCH0 and LATCH1 input signals are used
(can be the same pins as for SYNC0 and SYNC1, ESC dependents)
 - Time Stamping of SyncManager events is possible
- Latch on positive and/or negative edge
- Single or continuous latch configurable
- The *Latch Time* register (0x09B0:0x09CF) contain the time stamps
 - Acknowledge by reading the *Latch Time* register.

Registers for Latch Input Events

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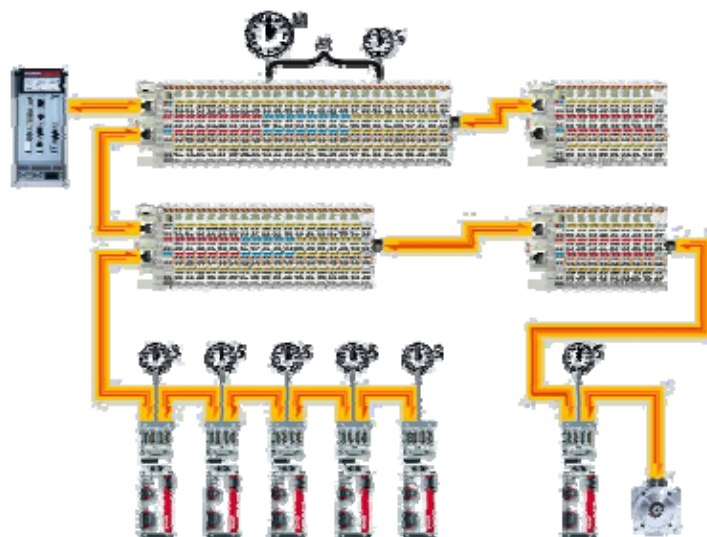
EtherCAT Master

Standards&Implementation

Register Address	Name	Description
0x0140[11:10]	PDI Control	Enable/Disable DC Units (power saving)
0x0151	Sync/Latch PDI Configuration	Configuration of SYNC/LATCH[1:0] pins
0x0980[5:4]	Cyclic Unit Control	Assignment of cyclic function to EtherCAT or PDI
0x09A8	Latch0 Control	Latch unit configuration for Latch0
0x09A9	Latch1 Control	Latch unit configuration for Latch1
0x09AE	Latch0 Status	Latch status of Latch0
0x09AF	Latch1 Status	Latch status Latch1
0x09B0:0x09B7	Latch0 Time Positive Edge	Time stamp positive edge Latch0
0x09B8:0x09BF	Latch0 Time Negative Edge	Time stamp negative edge Latch0
0x09C0:0x09C7	Latch1 Time Positive Edge	Time stamp positive edge Latch1
0x09C8:0x09CF	Latch1 Time Negative Edge	Time stamp negative edge Latch1
0x09F0:0x09F3	EtherCAT Buffer Change Event Time	Time stamp for ECAT SyncManager buffer change event
0x09F8:0x09FB	PDI Buffer Start Event Time	Time stamp for PDI SyncManager buffer start event
0x09FC:0x09FF	PDI Buffer Change Event Time	Time stamp for PDI SyncManager buffer change event

Synchronization Modes

- Definition of a unique behavior of EtherCAT devices (Master and Slaves) for synchronized applications
- Synchronization modes
 - Free Run
 - Synchronization with SyncManager Event, i.e. receipt of the telegram
 - Synchronization with Distributed Clocks



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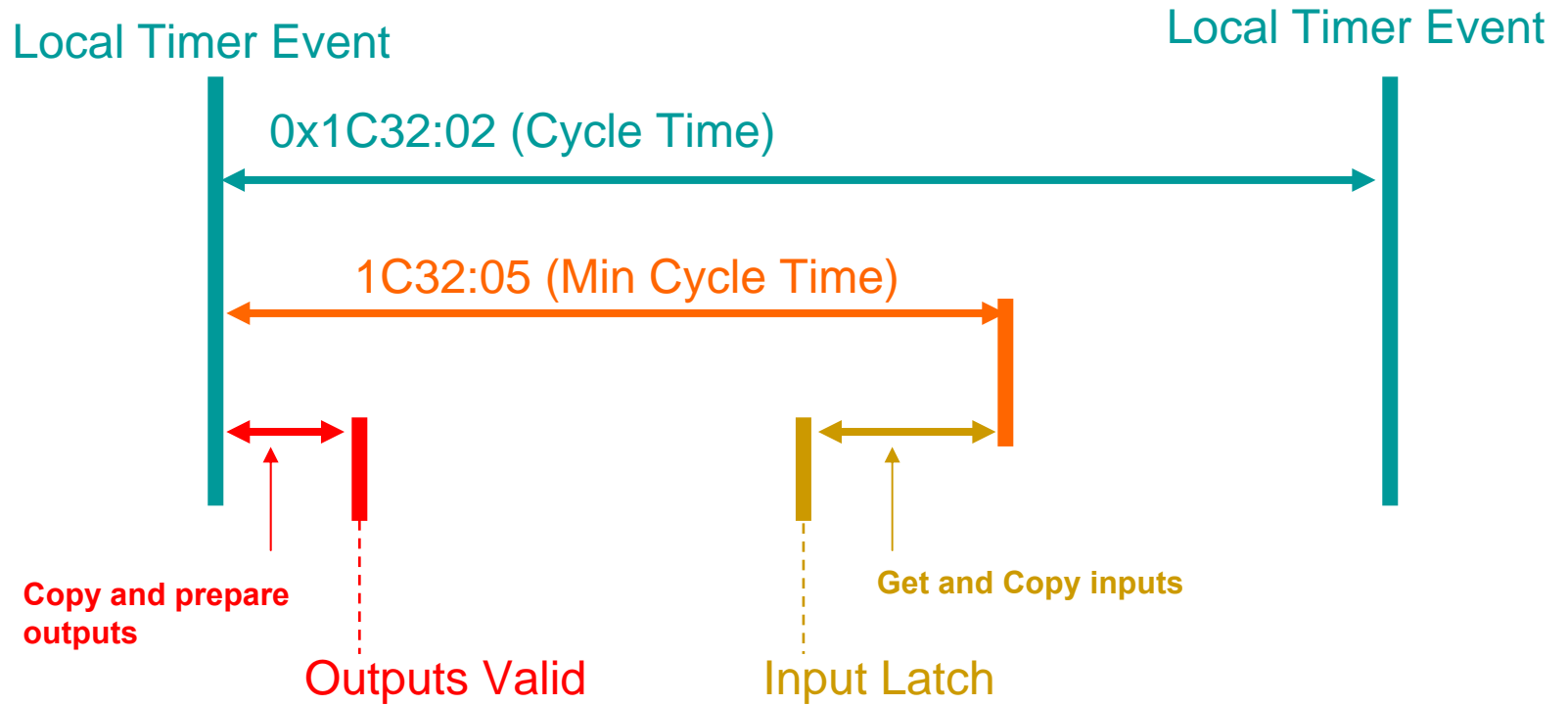
EtherCAT Master

Standards&Implementation

1. Free Run
2. Synchronous to SM2/3 (with Shift)
3. DC Mode 1 (Sync0 Event)
4. DC Mode 1 (Sync0 Event with Shift)
5. DC Mode 2 (Sync0, Sync1, with Shift)
6. DC Mode 3 (SM2 Event, Sync0)
7. DC Mode 4 (SM2 Event, Sync0, Sync1)
8. DC Mode with subordinated cycles

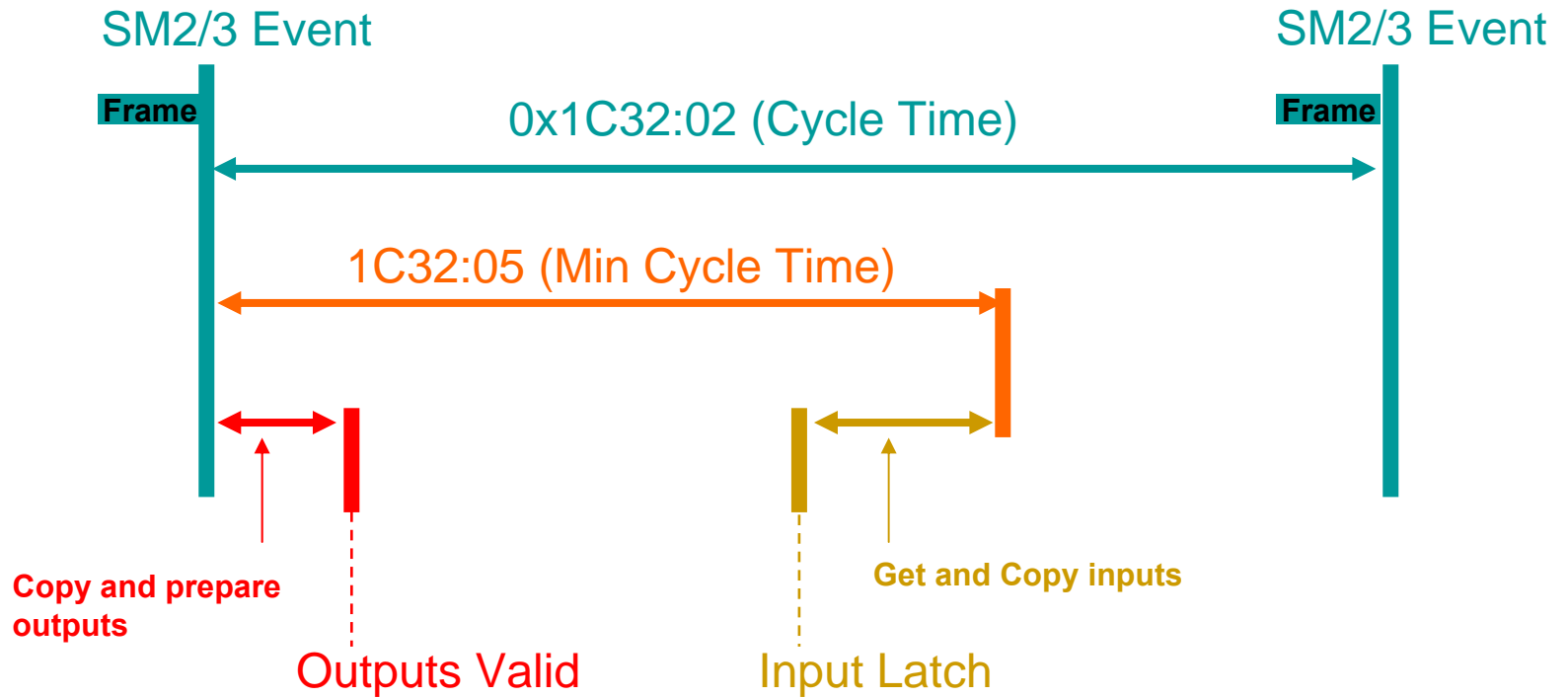
Free Run

Local Timer



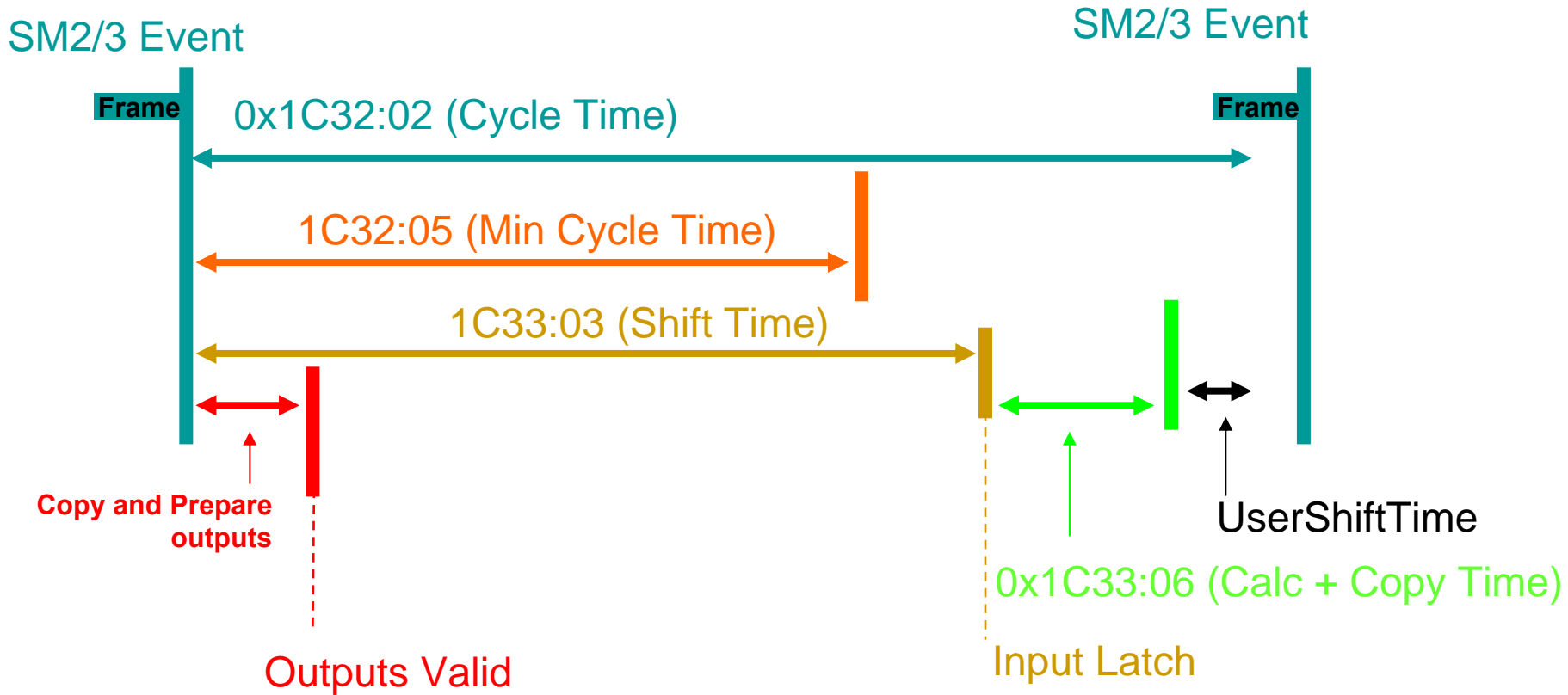
Synchronous with SM2/3

SM-Event



Synchronous with SM2/3

SM Event (Shift of Input Latch)



Output Calc+Copy Time (RO):

- Minimum time between trigger event (SM or SYNC0) and sync event (SYNC0 or SYNC1)

Output Delay Time (RO):

- Time between sync event (SYNC0 or SYNC1) and Outputs Valid

Output Shift Time (RO/RW):

- Time between SYNC0 event and Outputs Valid
- Outputs Valid could be delayed by writing this time

Input Delay Time (RO, if Input Latch is triggered by SYNC0 or SYNC1):

- Time between sync event (SYNC0 or SYNC1) and Input Latch

Input Calc+Copy Time (RO):

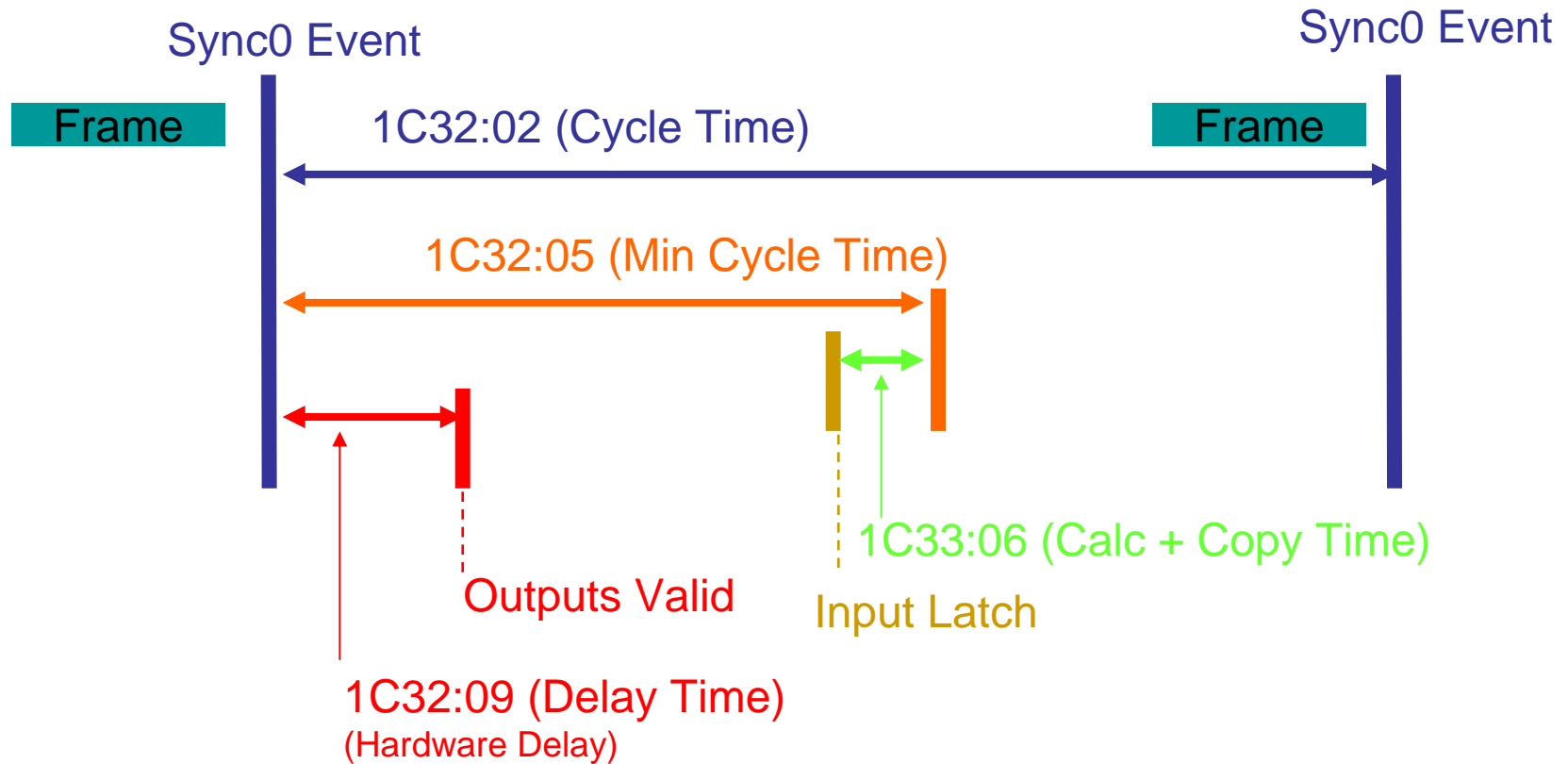
- Time between input latch and data availability for the master

Input Shift Time (RO/RW):

- Time between SYNC0 event and Input Latch
- Input Latch could be delayed by writing this time

DC Mode 1

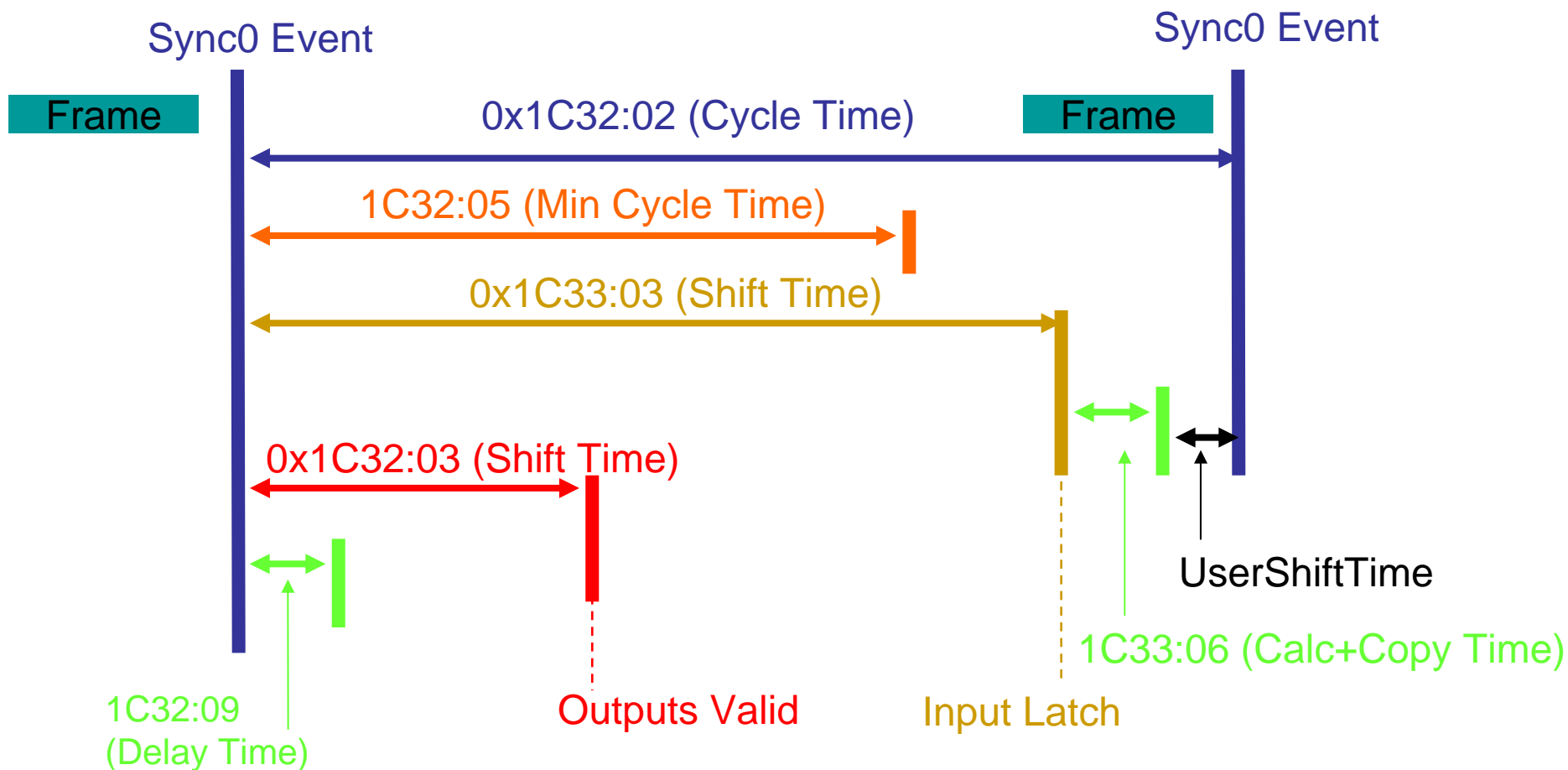
Sync0 Event



- Frame has to be received before Sync0 Event is generated
- Sync0 Cycle Time has to be greater than the value of 0x1C32:05
- 0x1C32:01 = 0x1C33:01 = 2, 0x1C32:06 = 0, 0x1C33:09 = 0

DC Mode 1

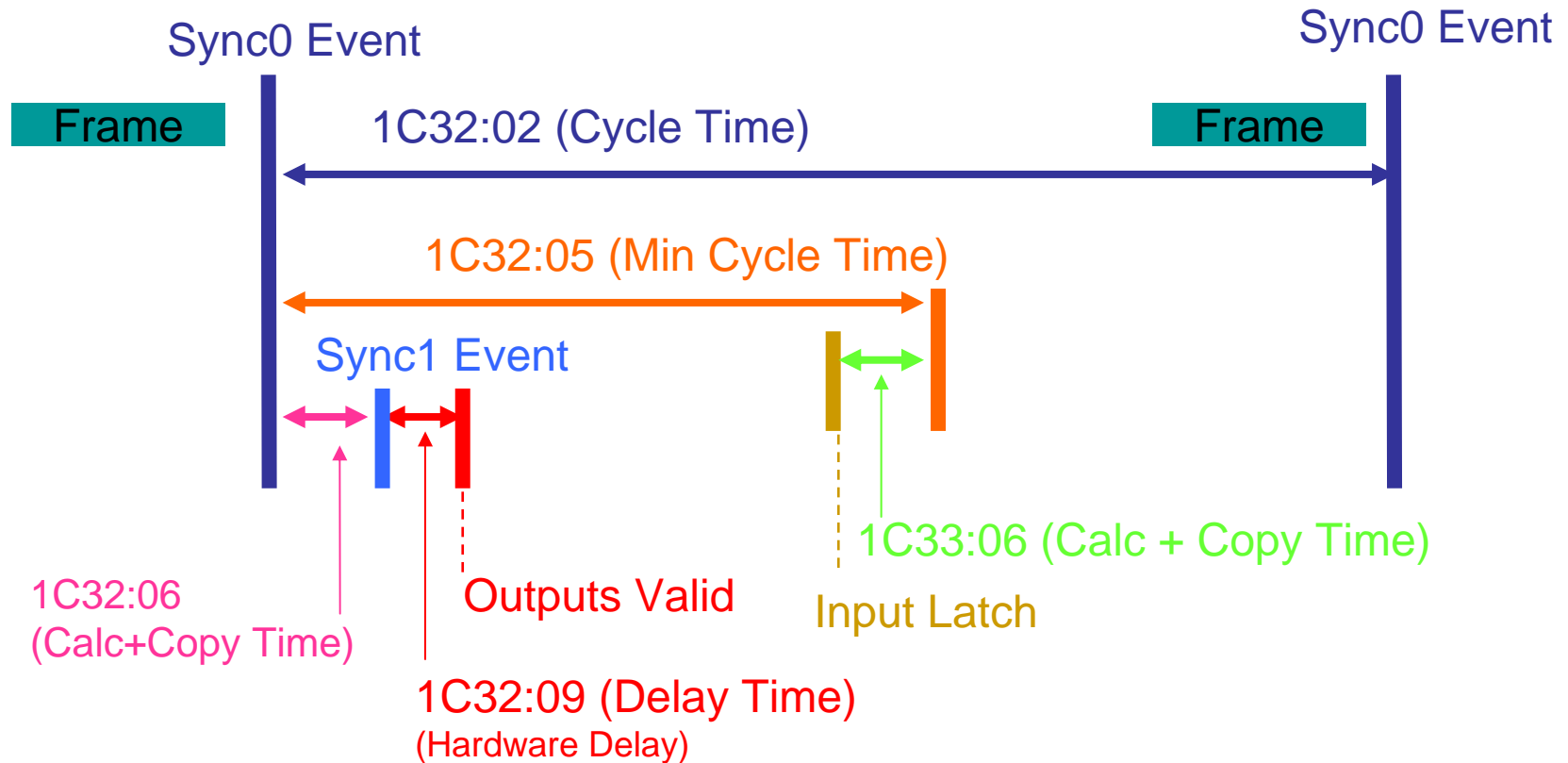
Sync0 Event (Shift of Outputs Valid and/or Input Latch)



- Output Shift Time 0x1C32:03 has to be greater than the value of 0x1C32:06
- Input Shift Time 0x1C33:03 has to be greater than 0x1C32:05-0x1C33:06
- Input Shift Time 0x1C33:03 has to be smaller than Sync0 Cycle Time-UserShiftTime-0x1C33:06

DC Mode 2

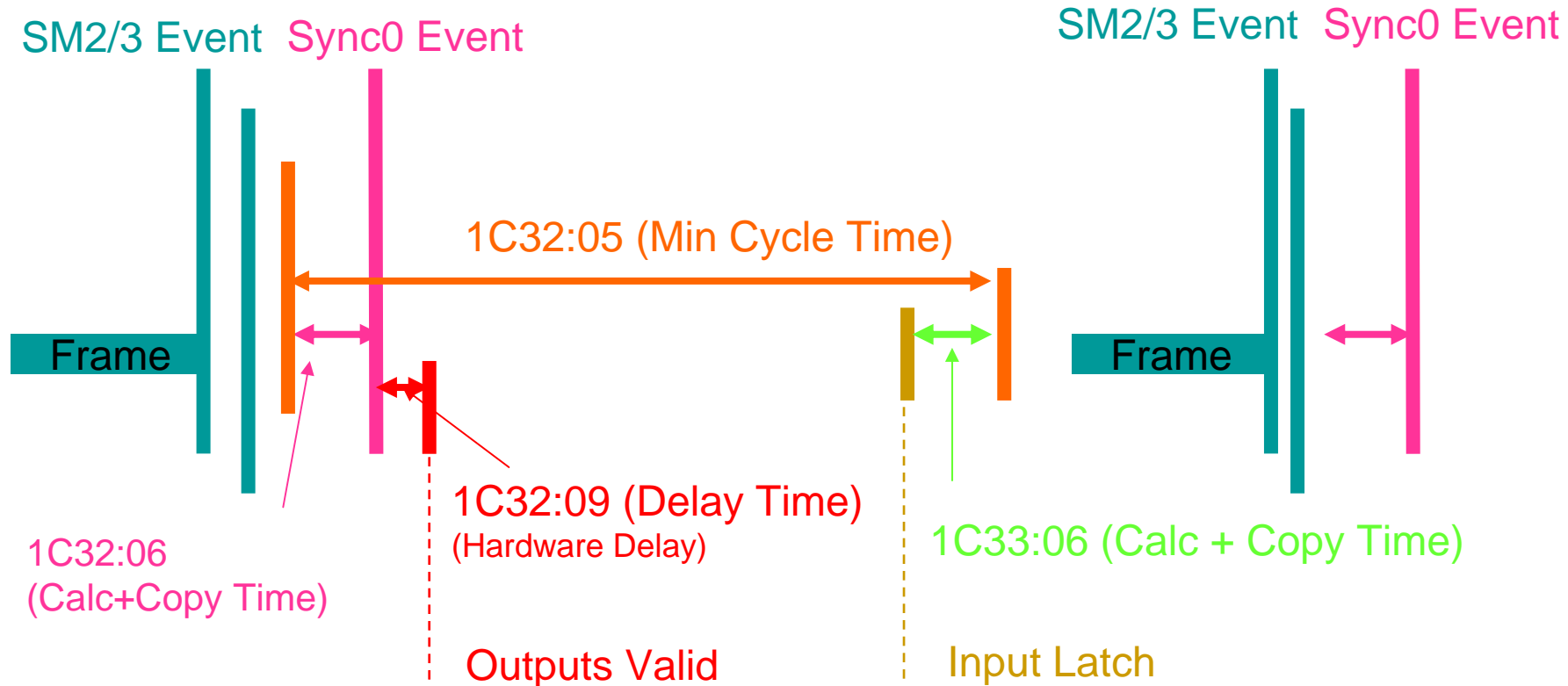
Sync0 Event, Sync1 Event



- Frame has to be received before Sync0 Event is generated
- Sync0 Cycle Time has to be greater than the value of 0x1C32:05
- Shift between SYNC0 and SYNC1 event has to be at least the value of 0x1C32:06
- 0x1C32:01 = 0x1C33:01 = 3, 0x1C33:09 = 0

DC Mode 3

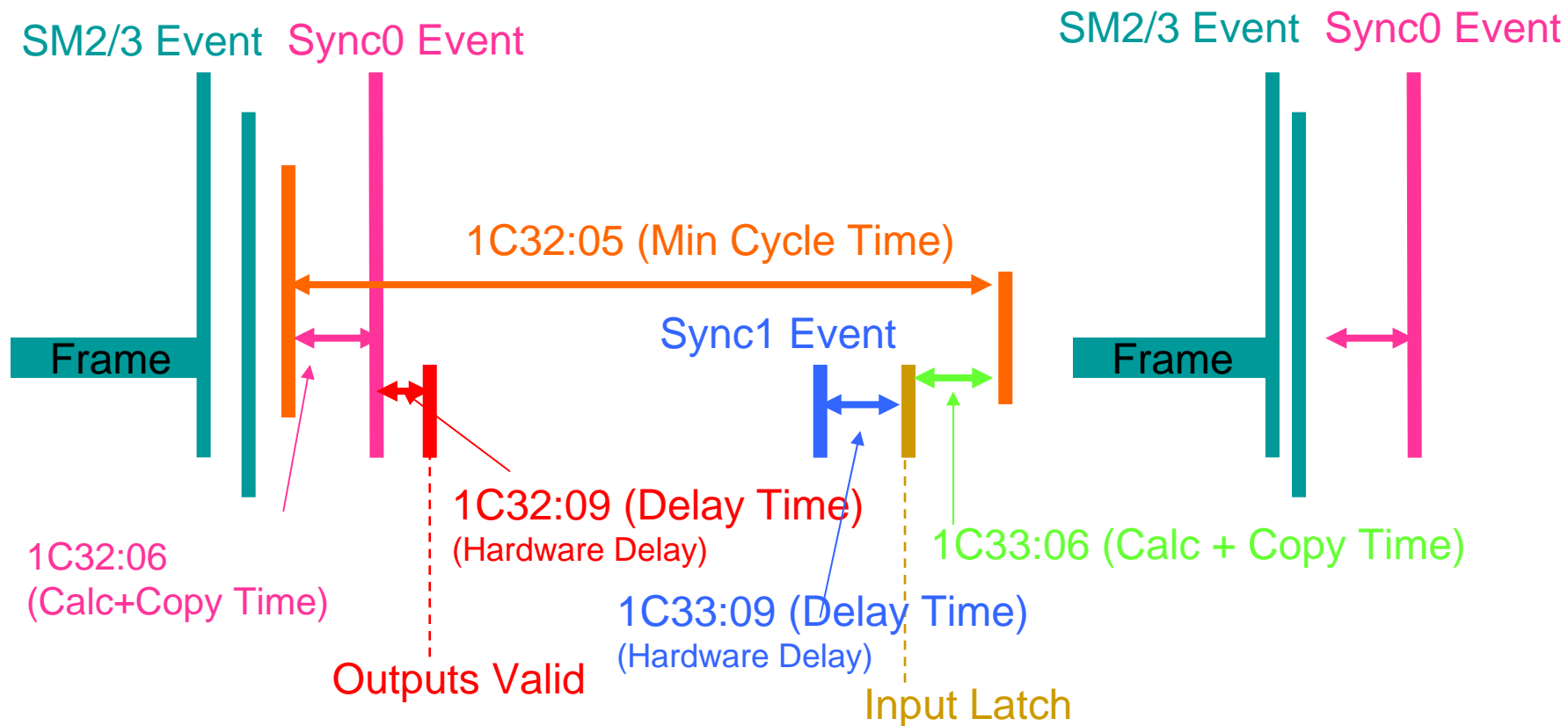
SM-Event, Sync0 Event



- Frame has to be received at least the value of 0x1C32:06 before Sync0 Event is generated
- Sync0 Cycle Time has to be greater than the value of 0x1C32:05
- 0x1C32:01 = 0x1C33:01 = 2, 0x1C33:09 = 0

DC Mode 4

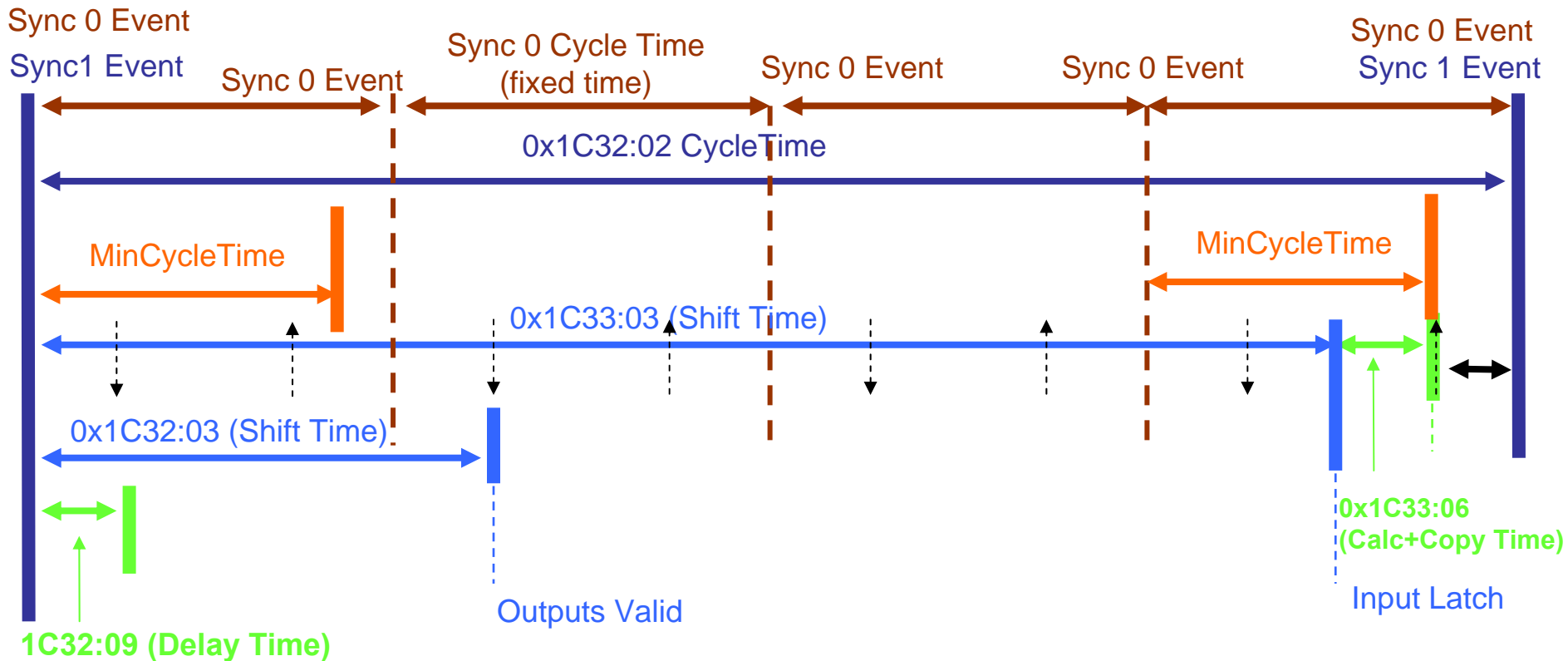
SM-Event, Sync0 Event, Sync1 Event



- Frame has to be received at least the value of 0x1C32:06 before Sync0 Event is generated
- Sync0 Cycle Time has to be greater than the value of 0x1C32:05
- Shift between SYNC0 and SYNC1 has to be at least 0x1C32:05-0x1C32:06-0x1C33:06-0x1C33:09
- 0x1C32:01 = 2, 0x1C33:01 = 3

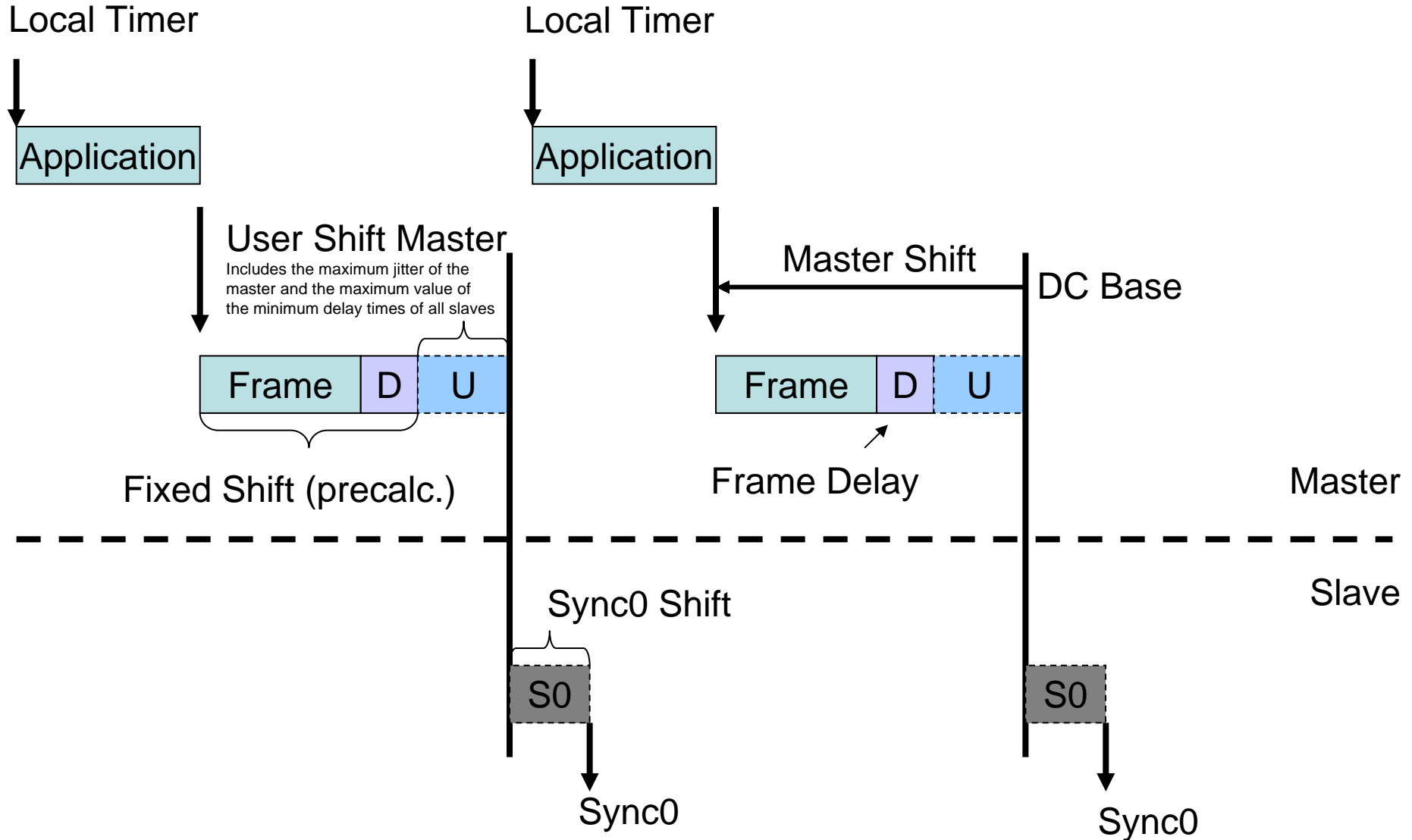
DC Mode subordinated cycles

Sync0 Event, Sync1 Event

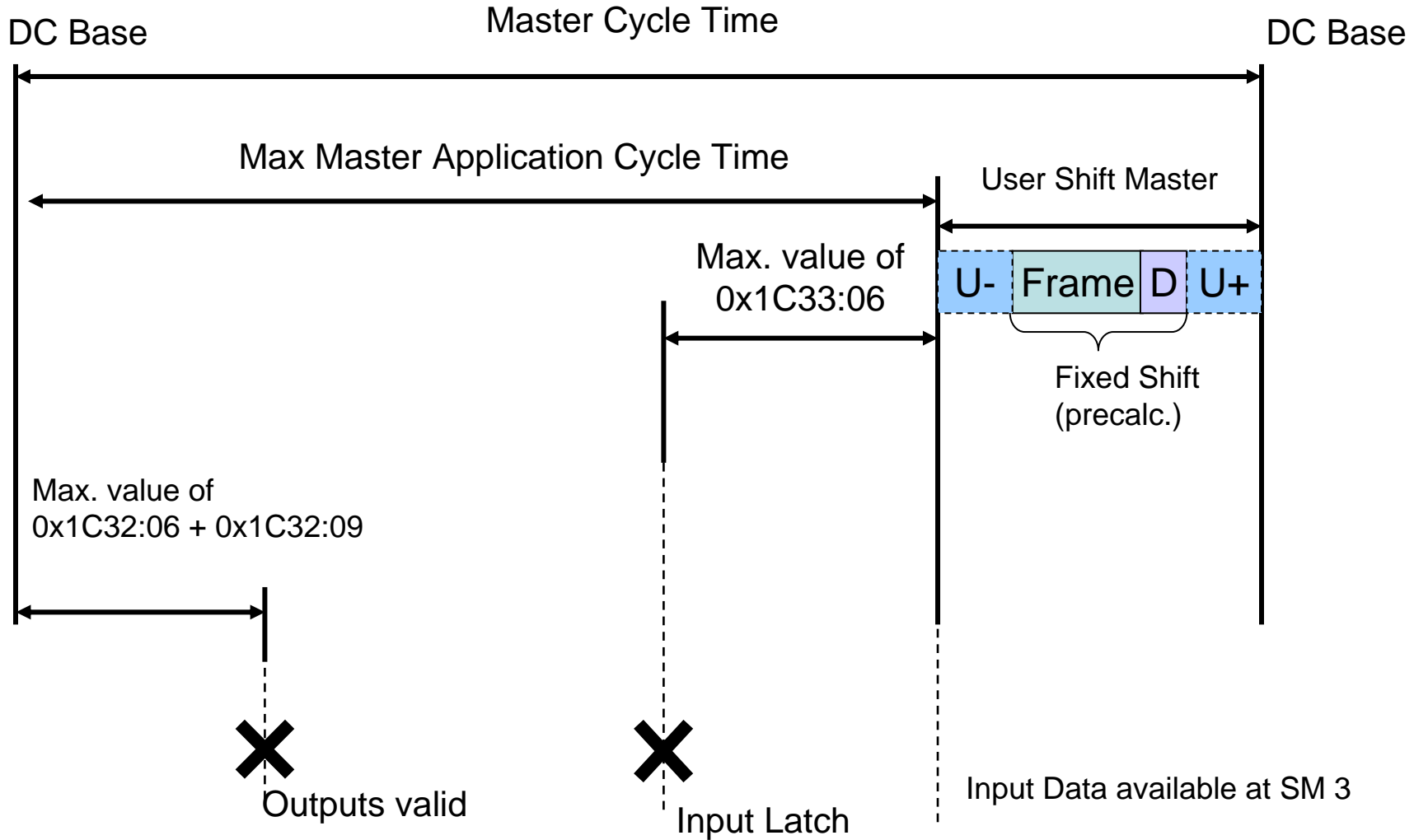


↓ Possible Output Valid /
Input Latch

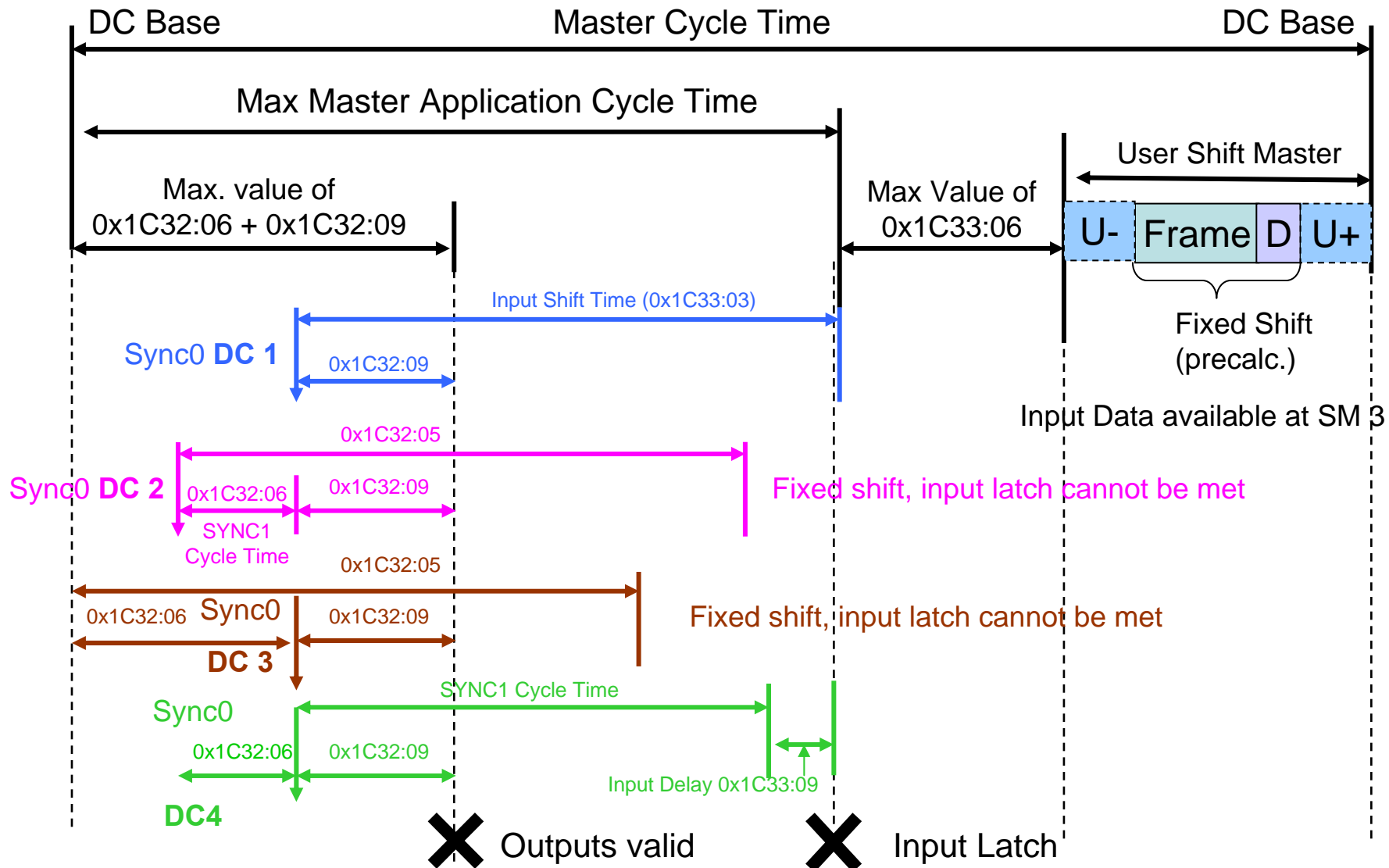
Distributed Clocks in TwinCAT

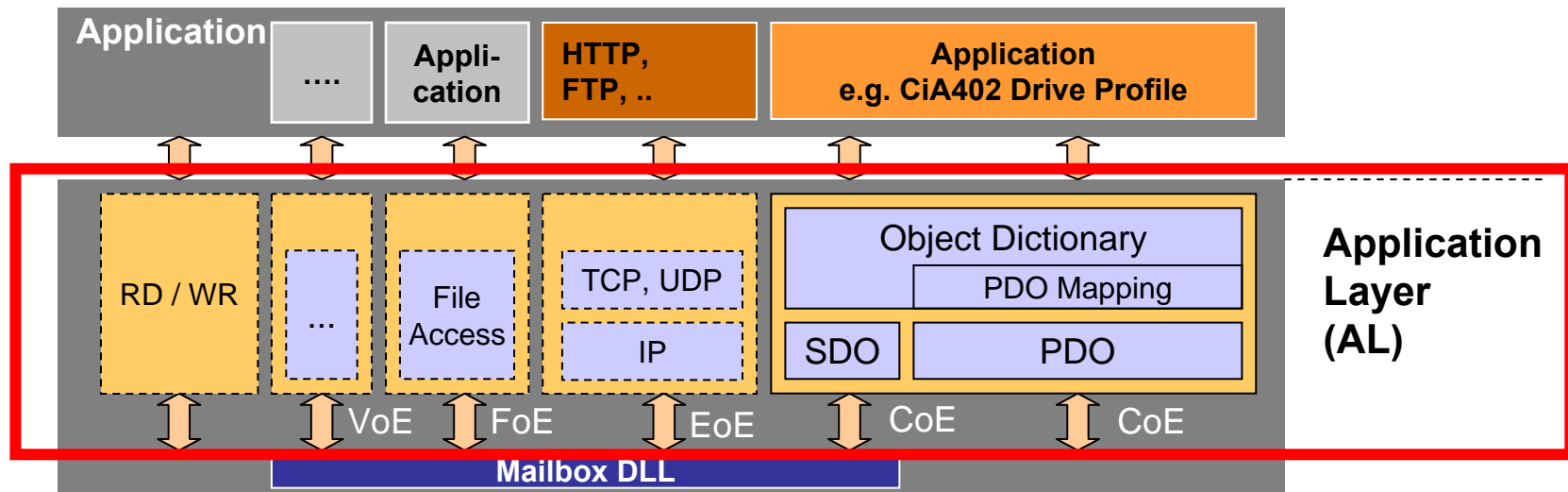


Master Settings for use of DC

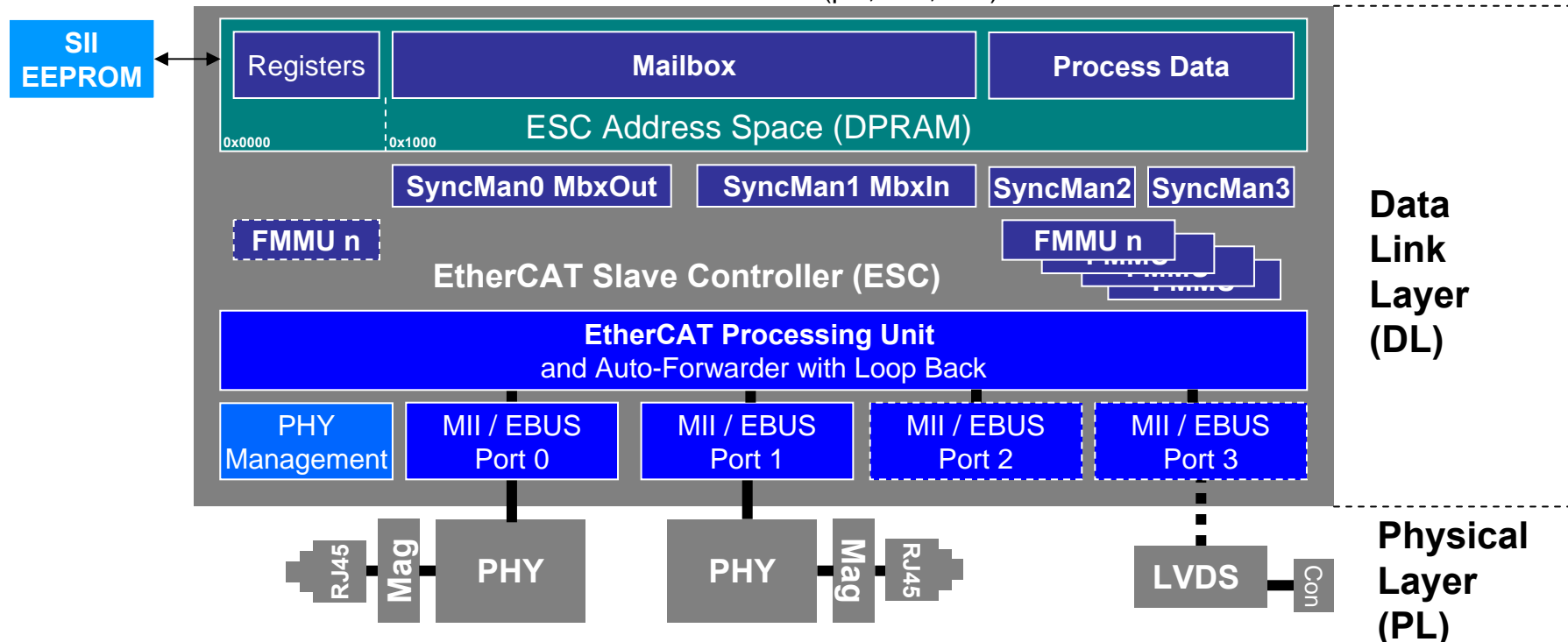


Master Settings for use of DC – Slave related settings





Process Data Interface (μC, SSI, I/O)



Purpose of Application Layer (AL)

EtherCAT Basics

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CoE CANopen

FoE File Access

SoE Servo Drive

ESI: Device Description

SII: Slave Information /IF

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EtherCAT Master

Standards&Implementation

- EtherCAT State Machine
 - Boot-up of device and network
- Mailbox Interfaces and Protocols
 - Access Parameter of a device
 - Asynchronous transfer
- Protocols
 - **E**thernet **o**ver **E**therCAT (EoE)
 - **C**AN application protocol **o**ver **E**therCAT (CoE)
 - **F**ile transfer **o**ver **E**therCAT (FoE)
 - **S**ervo Drive **o**ver **E**therCAT (SoE)
- Slave Information Interface (SII)
 - Information about the device's features and configuration

Purpose of EtherCAT State Machine

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EtherCAT Master

Standards&Implementation

- Defines general communication states of EtherCAT slave devices
- Specifies the initialization and error handling of EtherCAT slave devices → Boot-up of the network
- States correspond to the communication relationship between master and slave
- Requested and current state of a slave device are reflected in the AL Control and AL Status registers
- Five states are defined:
 - ‘Init’, ‘Pre-Operational’, ‘Safe-Operational’, ‘Operational’
 - ‘Bootstrap’ optional state for firmware updates

EtherCAT State Machine

EtherCAT Basics

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- Frame Structure
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- SyncManager
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Distributed Clocks

Application Layer

- State Machine

Mailbox

- Mailbox Interface
- EoE Ethernet
- CoE CANopen
- FoE File Access
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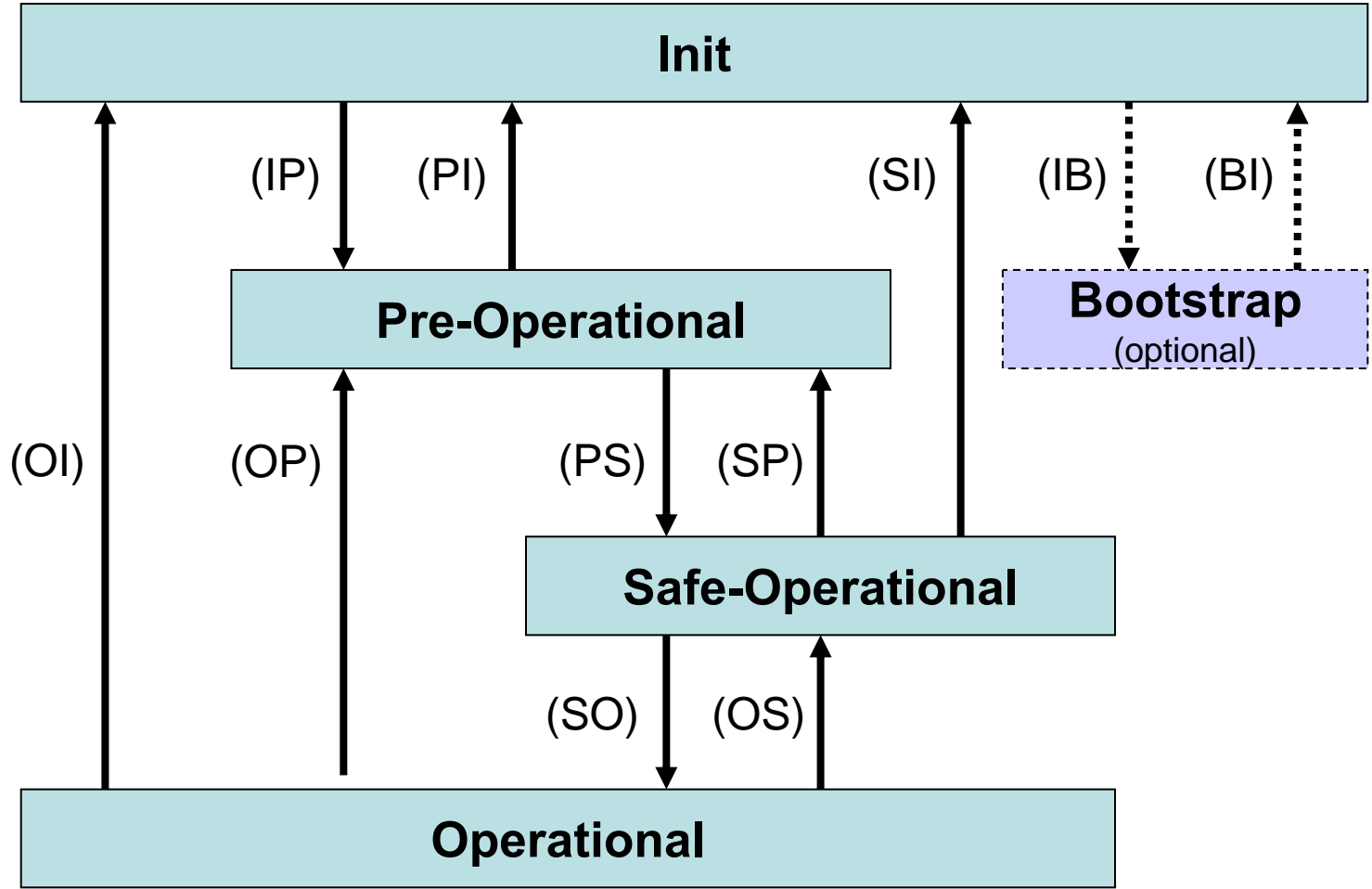
Device Profiles

- Modular Devices
- Drives

Configuration Tool

EtherCAT Master

Standards&Implementation



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EtherCAT Master

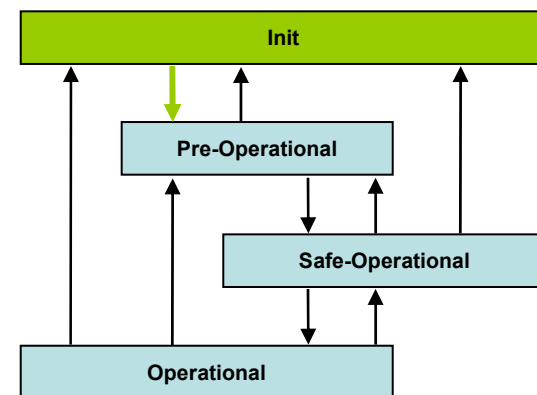
Standards&Implementation

• ‘Init’ State

- No communication on the Application Layer
- Master has access to the DL-Information registers

• Transition to ‘Pre-Operational’

- Master configures register, at least:
 - DL Address register
 - Sync Manager channels for Mailbox communication
- Master requested ‘Pre-Operational’ state
 - sets AL Control register
 - wait for AL Status register confirmation



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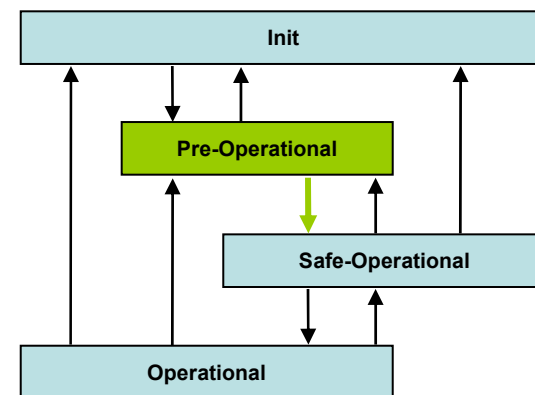
Standards&Implementation

• ‘Pre-Operational’ State

- Mailbox communication on the Application Layer
- No Process Data communication

• Transition to ‘Safe-Operational’

- Master configures parameter using the Mailbox
 - e.g.: Process Data Mapping
- Master configures DL Register
 - SyncManager channels for Process Data communication
 - FMMU channels
- Master requested ‘Safe-Operational’ state



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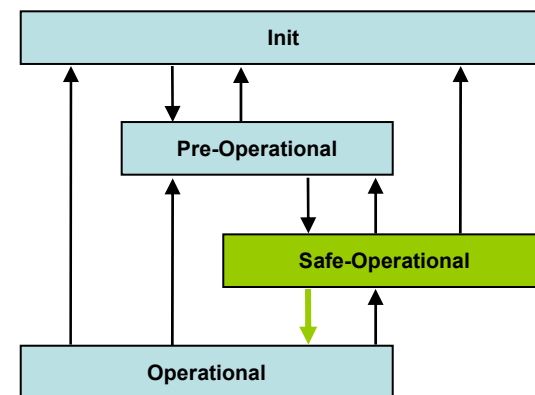
Standards&Implementation

• ‘Safe-Operational’ State

- Mailbox communication on the Application Layer
- Process Data communication, but only Inputs are evaluated – Outputs in ‘Safe’ state

• Transition to ‘Operational’

- Master sends valid Outputs
- Master requested ‘Operational’ state (AL Control/Status)



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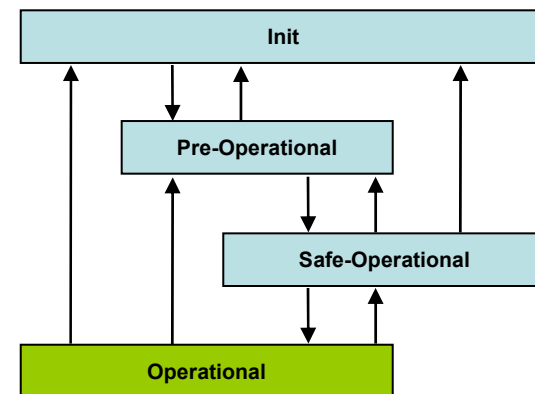
Drives

Configuration Tool

EtherCAT Master

Standards&Implementation

- **‘Operational’ State**
 - Inputs and Outputs are valid



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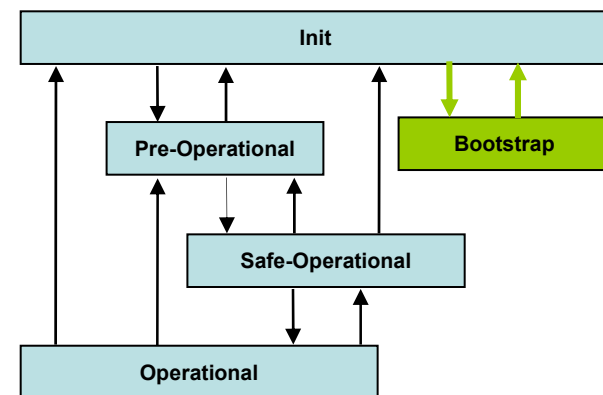
Configuration Tool

EtherCAT Master

Standards&Implementation

• ‘Bootstrap’ State

- ‘Bootstrap’ State is optional – but recommended if firmware updates necessary
- State changes only from and to ‘Init’
- No Process Data communication
- Communication via Mailbox on Application Layer
- Special mailbox configuration possible, e.g. larger mailbox size
- Only FoE protocol available (possibly limited “file” range)



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Standards&Implementation

- Requested and current state of a slave device are reflected in the AL Control and AL Status registers
 - AL Control (0x0120)
Initiate State Transition of Device State Machine
 - AL Status (0x0130)
Actual State of Device State Machine
 - AL Status Code (0x0134)
Reason of error or other status code

- **AL Status Code (0x0134)**
 - Error Codes (extract)

Code	Description
0x0000	No Error
0x0011	Invalid requested state change
0x0015	Invalid mailbox configuration
0x0018	No valid inputs available
0x0019	No valid outputs
0x001A	Synchronization error
0x001B	Sync manager watchdog

- Further Status Codes (extract)

Code	Description
0x0021	Slave needs INIT
0x0022	Slave need PREOP
0x0030	Invalid DC Sync Configuration
0x0031	Invalid DC Latch Configuration

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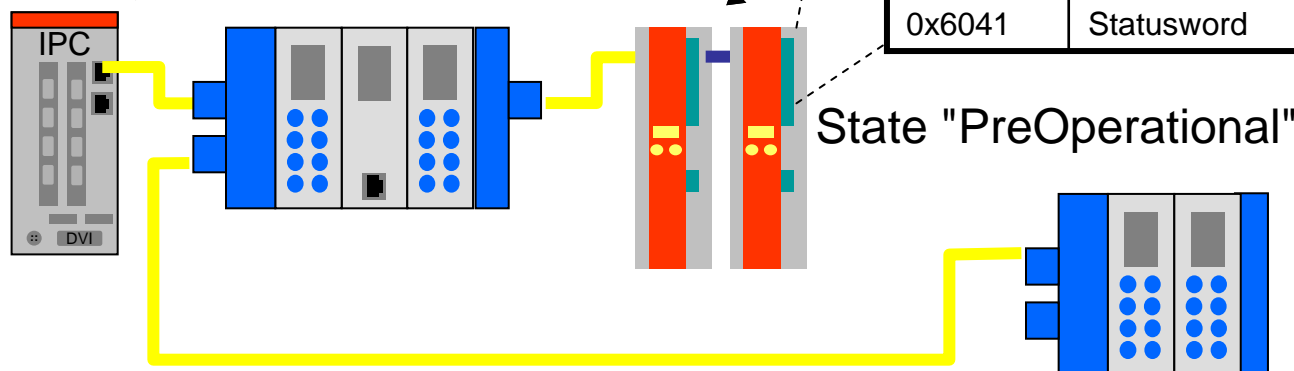
Configuration Tool

EtherCAT Master

Standards&Implementation

Mailbox Transfer for Parameter Data

Full duplex capable



Device Parameter (example)

Index	Value
0x1600	RxPDO Mapping
0x1A00	TxPDO Mapping
0x2000	Current control
0x2010	Velocity control
0x2040	Motor parameter
0x2070	Actual values
0x6040	Controlword
0x6041	Statusword

Simple IO-Device
No Parameter

→ No Mailbox necessary

Purpose of Mailbox Transfer

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EtherCAT Master

Standards&Implementation

- Standard way to exchange Parameter Data
- The Mailbox Interface is optional – but recommended
- Needed if Process Data configurable or any other non cyclic services
- Full duplex capable
(Slave can initiate a communication)
- 2 Sync Manager channels reserved
 - Sync Manager 0 : Master to Slave
 - Sync Manager 1 : Slave to Master
- Available at early stage of communication
(State Pre-Operational)
- Multi protocol capable

EtherCAT Basics**Slave Structure****Device Model (ISO/OSI)****Physical Layer****Data Link Layer**

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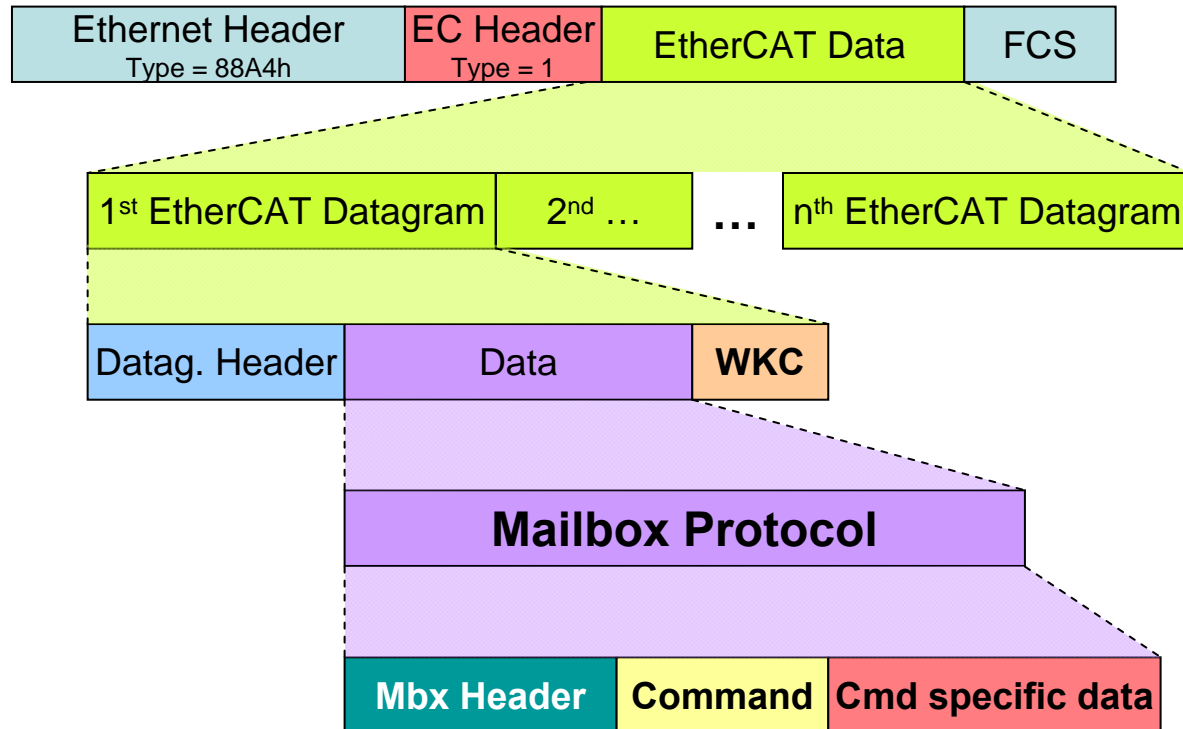
Modular Devices

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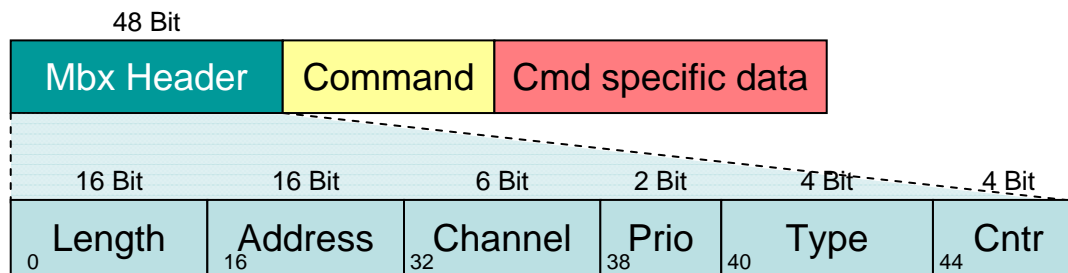
Configuration Tool**EtherCAT Master****Standards&Implementation**

- Ethernet over EtherCAT (EoE)
 - Tunnels standard Ethernet Frames over EtherCAT
- CAN application protocol over EtherCAT (CoE)
 - Access of a CANopen* object dictionary and its objects
 - CANopen* Emergency and optional event driven PDO messages
- File Access over EtherCAT (FoE)
 - Download and upload firmware and other 'files'
- Servo Drive over EtherCAT (SoE)
 - Access the Servo Profile Identifier (IDN)
- Vendor specific Profile over EtherCAT (VoE)
 - First DWORD contains the Vendor ID, the next WORD contains a Vendor Type, the rest is vendor specific

- Datagram within an EtherCAT Frame



Mailbox Header



Length	Length of following data
Address	Station Address of originator
Channel	reserved for future use
Priority	reserved for future use
Type	Mailbox Type, Protocol identifier for following data 0 Mailbox Error 2 EoE (Ethernet over EtherCAT) 3 CoE (CAN application protocol over EtherCAT) 4 FoE (File Access over EtherCAT) 5 SoE (Servo Drive over EtherCAT) 15 VoE (Vendor specific profile over EtherCAT)
Counter	Sequence number for duplicate detection Increments with every new mailbox service (only the values 1-7 will be used to be compatible with older versions).

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Mailbox Error Handling Procedure

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EtherCAT Master

Standards&Implementation

- Reliable way of mailbox data exchange
- Mailbox control procedure
- Recover from lost frames
- No additional frames if no error
- Additional receive buffer required
- Extra counter in mailbox header needed
- HW/SW solution
 - SyncManager configuration register with toggle flags
 - SW-Mailbox-DL Layer for checking toggle bits

Mailbox Error Handling – Sync Manager

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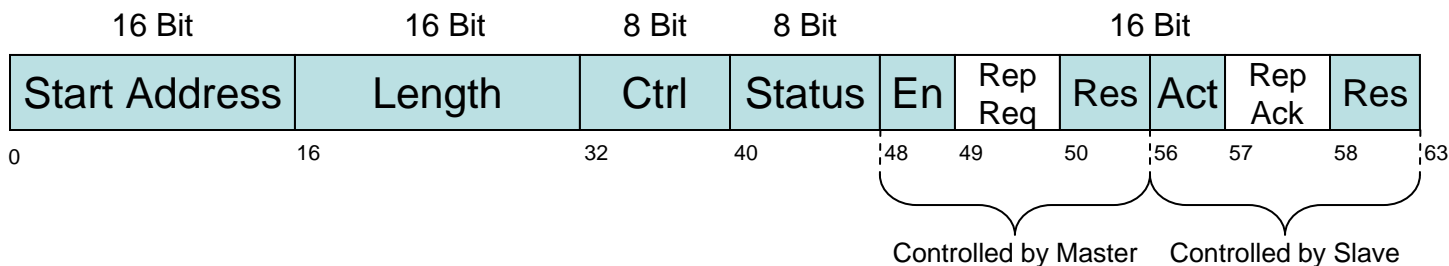
Drives

Configuration Tool

EtherCAT Master

Standards&Implementation

- Sync Manager channel configuration registers



Rep Req.....Repeat Request
Rep AckRepeat Acknowledge

- Following:
 - Mailbox Error Handling - Write Example
 - Mailbox Error Handling - Read Example

Mailbox Error Handling – Mailbox Write

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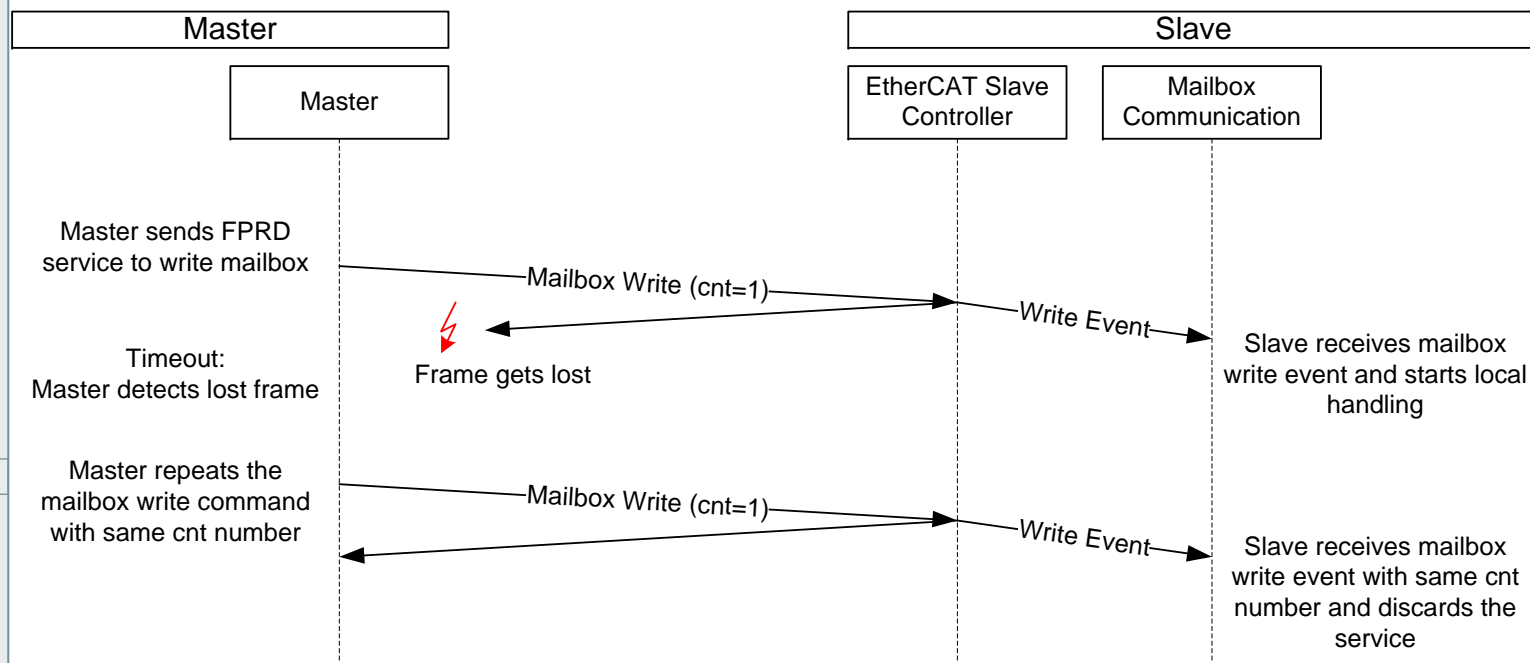
Modular Devices

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Configuration Tool

EtherCAT Master

Standards&Implementation



Mailbox Error Handling – Mailbox Read

EtherCAT Basics

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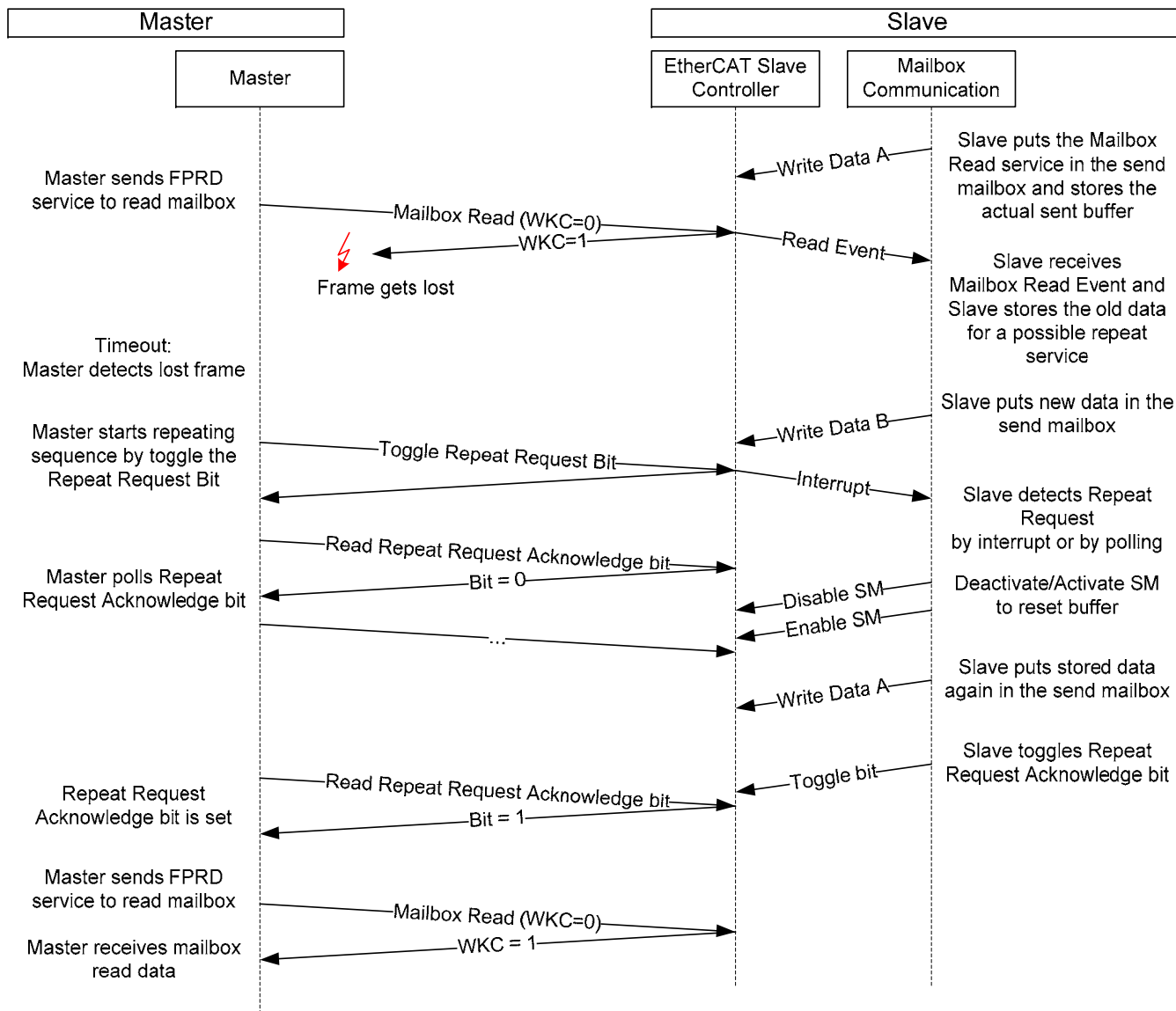
Modular Devices

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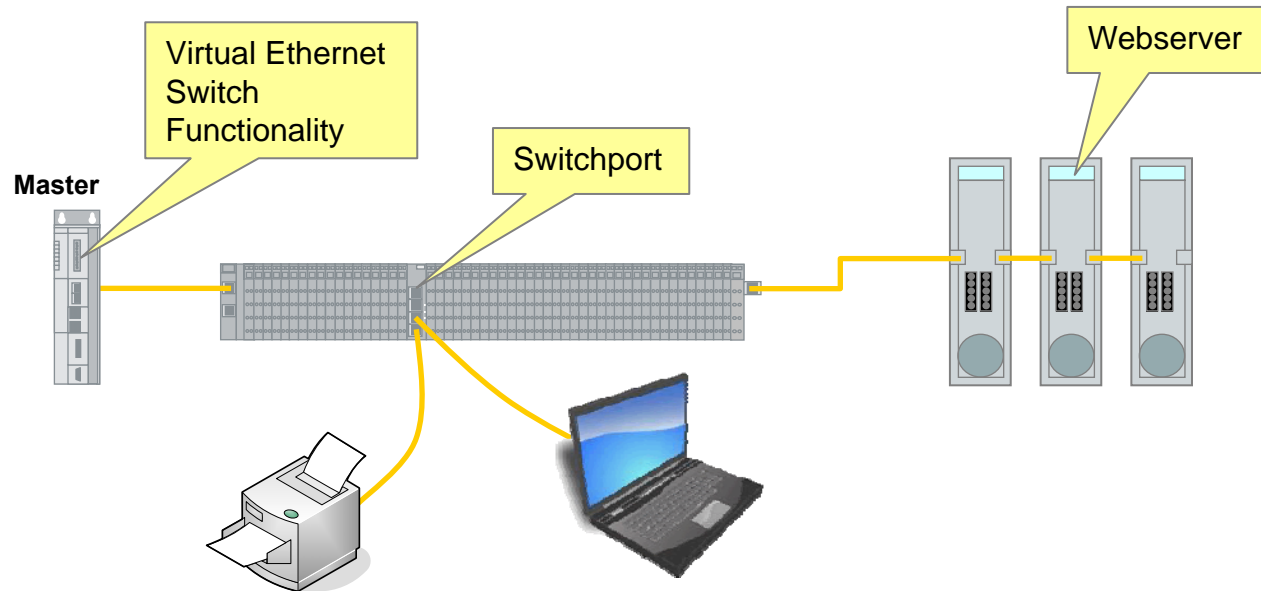
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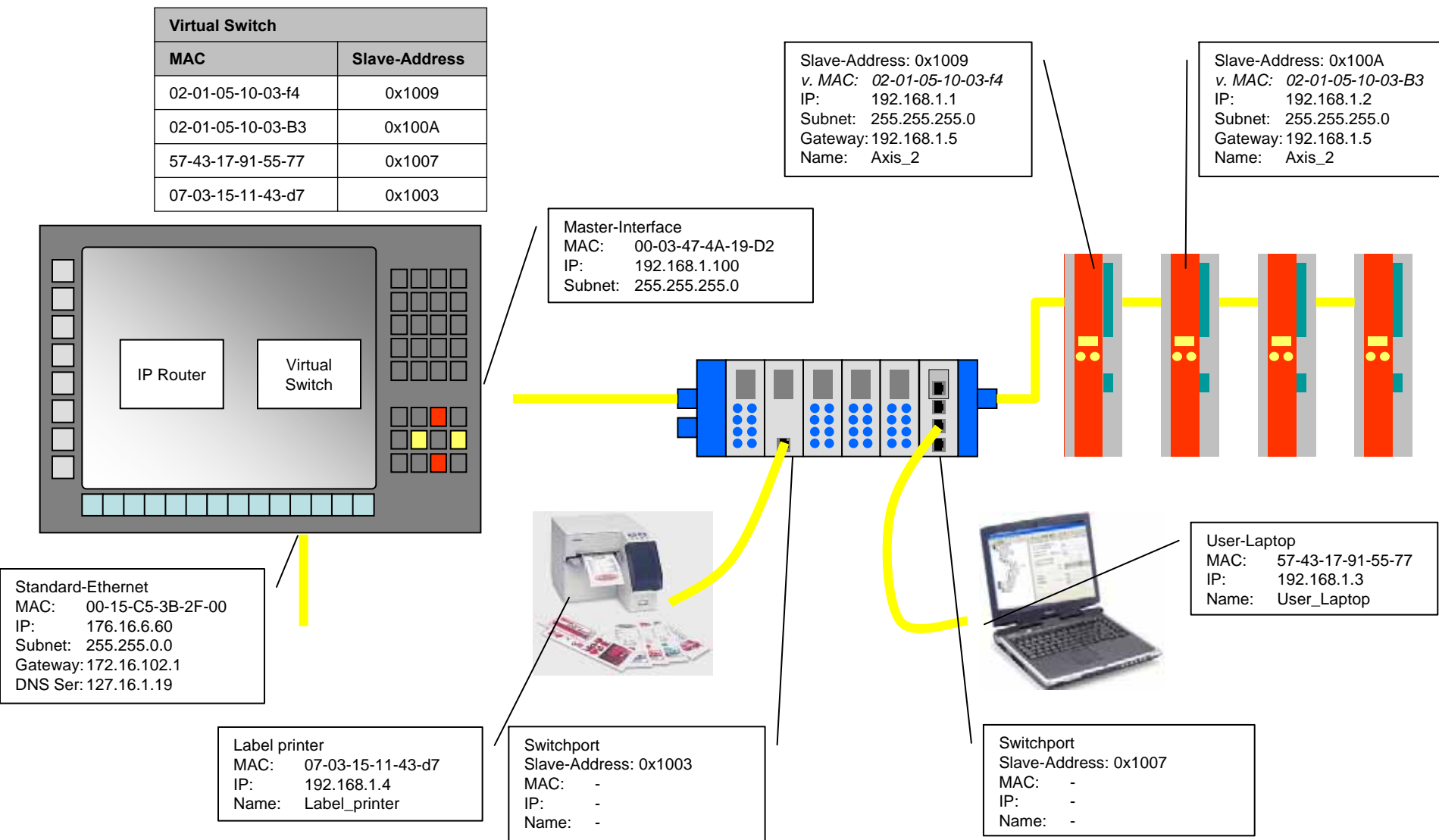
Configuration Tool

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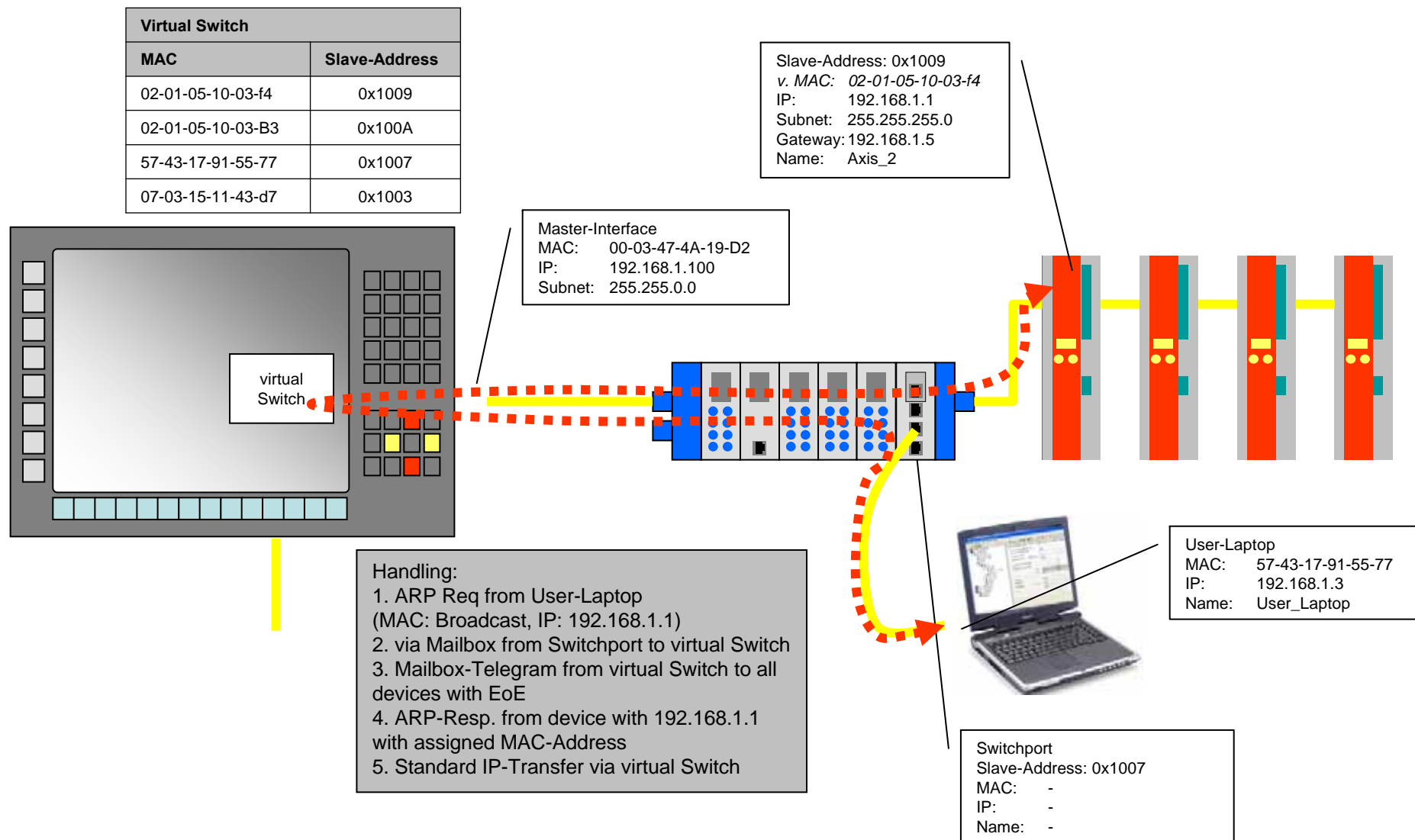
Standards&Implementation



Ethernet over EtherCAT (EoE)



Ethernet over EtherCAT (EoE)

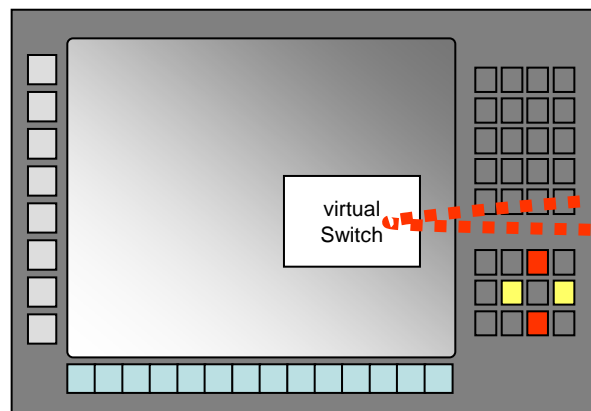


Ethernet over EtherCAT (EoE)

Virtual Switch	
MAC	Slave-Address
02-01-05-10-03-f4	0x1009
02-01-05-10-03-B3	0x100A
57-43-17-91-55-77	0x1007
07-03-15-11-43-d7	0x1003

Handling:

1. User-Laptop gets MAC-ID of printer via ARP
2. IP-Telegram from User-Laptop to printer
Dest-MAC: 07-03-15-11-43-d7,
→ virtual Switch forwards to EtherCAT Slave 0x1003



Master-Interface
MAC: 00-03-47-4A-19-D2
IP: 192.168.1.100
Subnet: 255.255.0.0



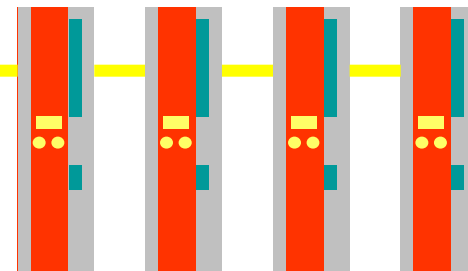
Label printer
MAC: 07-03-15-11-43-d7
IP: 192.168.1.4
Name: Label_printer

Switchport
Slave-Address: 0x1003
MAC: -
IP: -
Name: -



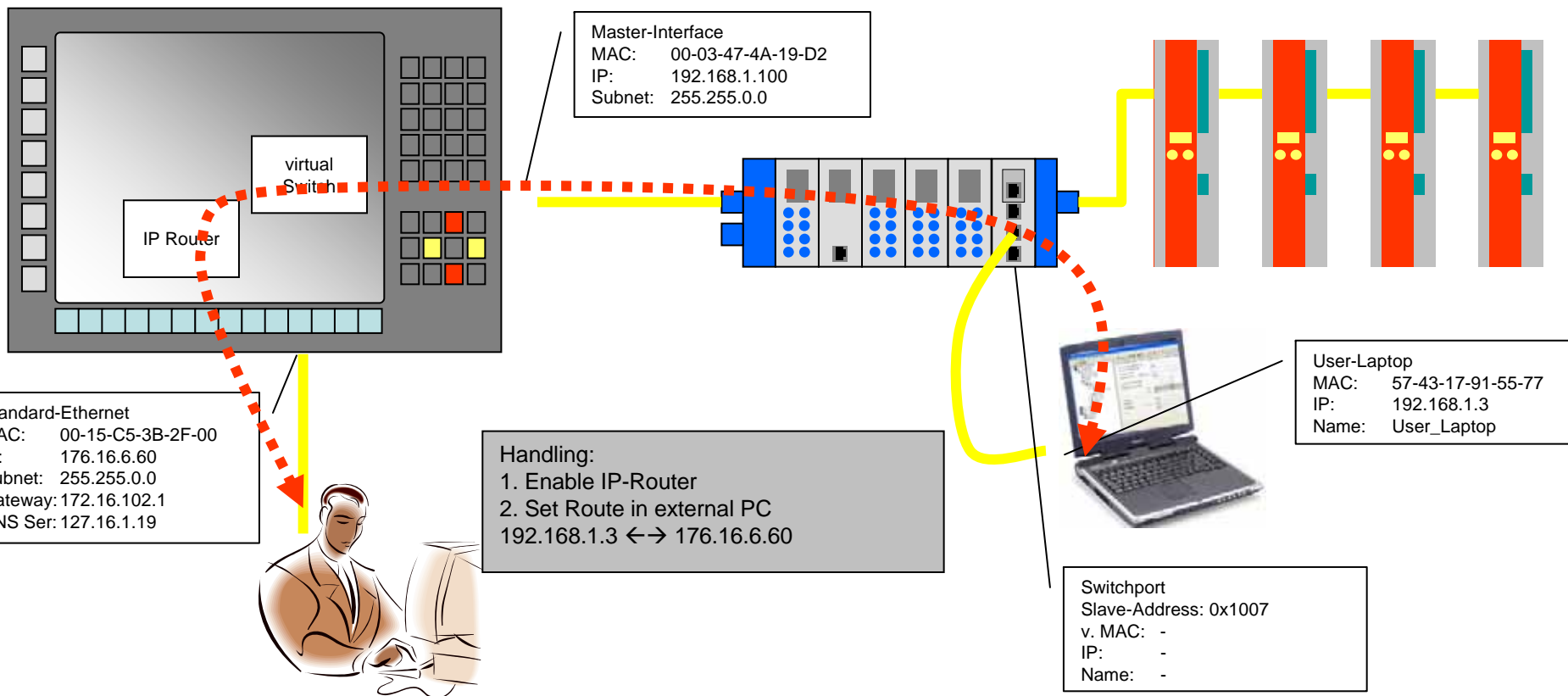
User-Laptop
MAC: 57-43-17-91-55-77
IP: 192.168.1.3
Name: User_Laptop

Switchport
Slave-Address: 0x1007
MAC: -
IP: -
Name: -



Ethernet over EtherCAT (EoE)

Virtual Switch	
MAC	Slave-Address
02-01-05-10-03-f4	0x1009
02-01-05-10-03-B3	0x100A
57-43-17-91-55-77	0x1007
07-03-15-11-43-d7	0x1003



EoE Route Configuration

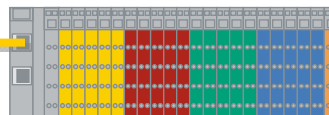
Diagnosis / Configuration

EtherCAT Master

EtherCAT Slave
with WebServer



Ethernet over EtherCAT (EoE)



IP: 172.16.8.31
Mask: 255.255.0.0

```
route
ADD 192.168.1.0
MASK 255.255.255.0
172.16.4.205
```

IP: 172.16.4.205
Mask: 255.255.0.0

IP Routing must be
enabled

IP: 192.168.1.100
Mask: 255.255.255.0

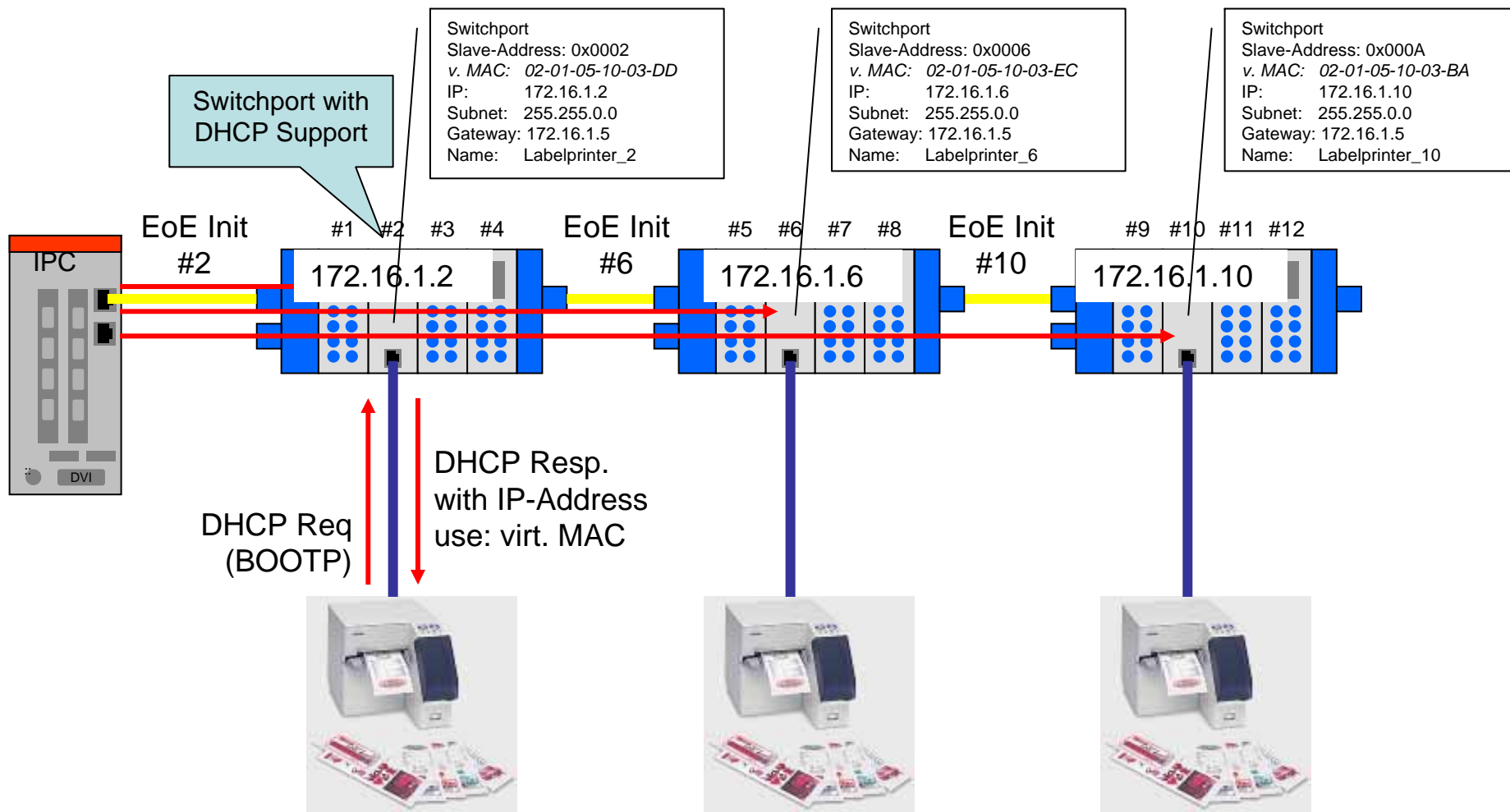
IP: 192.168.1.11
Mask: 255.255.255.0

```
route
ADD 172.16.0.0
MASK 255.255.0.0
192.168.1.100
```

Or

Default Gateway:
192.168.1.100

EoE – Support of DHCP, BOOTP



Purpose of Ethernet over EtherCAT (EoE)

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Device Model (ISO/OSI)

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CoE CANopen

FoE File Access

SoE Servo Drive

ESI: Device Description

SII: Slave Information /IF

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Configuration Tool

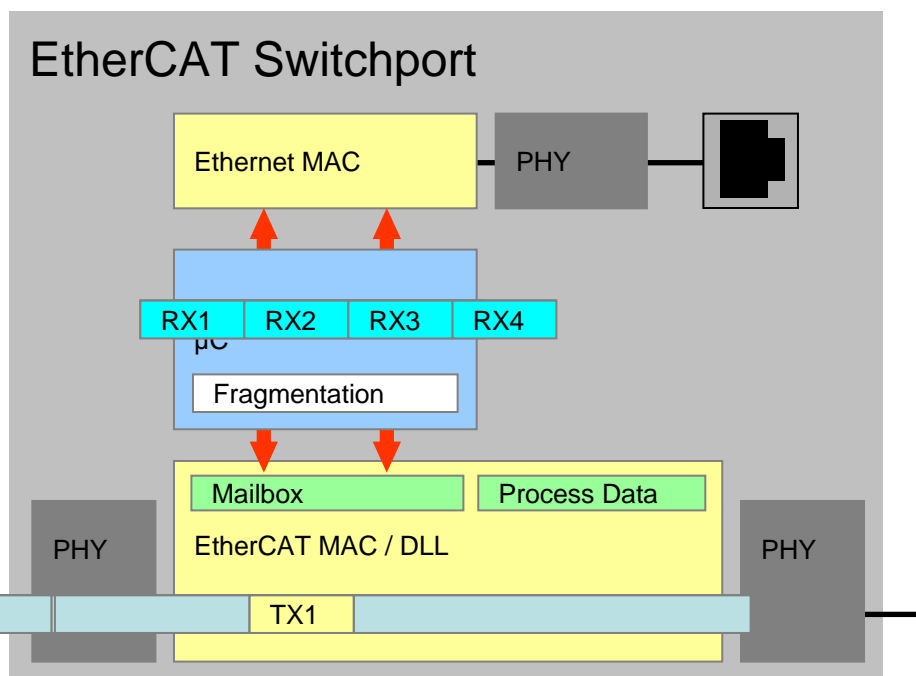
EtherCAT Master

Standards&Implementation

- Tunnels transparently Ethernet Frames over EtherCAT
- Tunneling reduces the cycle times without restrictions and to optimized available bandwidth
- Used for devices with TCP/IP stacks (e.g. Web Server) and for infrastructure devices like Switch Terminals
- Allows to access corresponding devices in the normal IP network in combination with a 'Virtual Ethernet Switch' (Layer 2) on the master side

EoE – Switchport: Any Ethernet Protocol

- Interface to any Ethernet Device or Network
- Ethernet Frames are inserted into EtherCAT Protocol:
 - ‘Ethernet over EtherCAT’



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EoE – Frame Header

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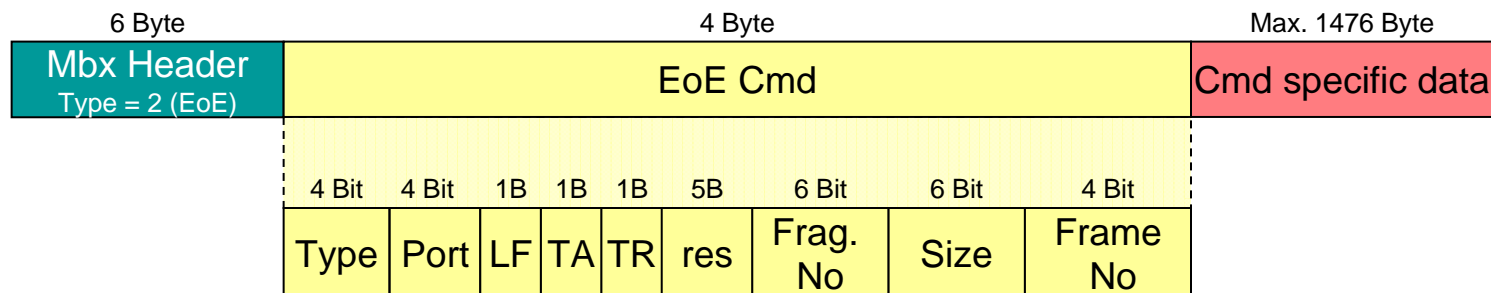
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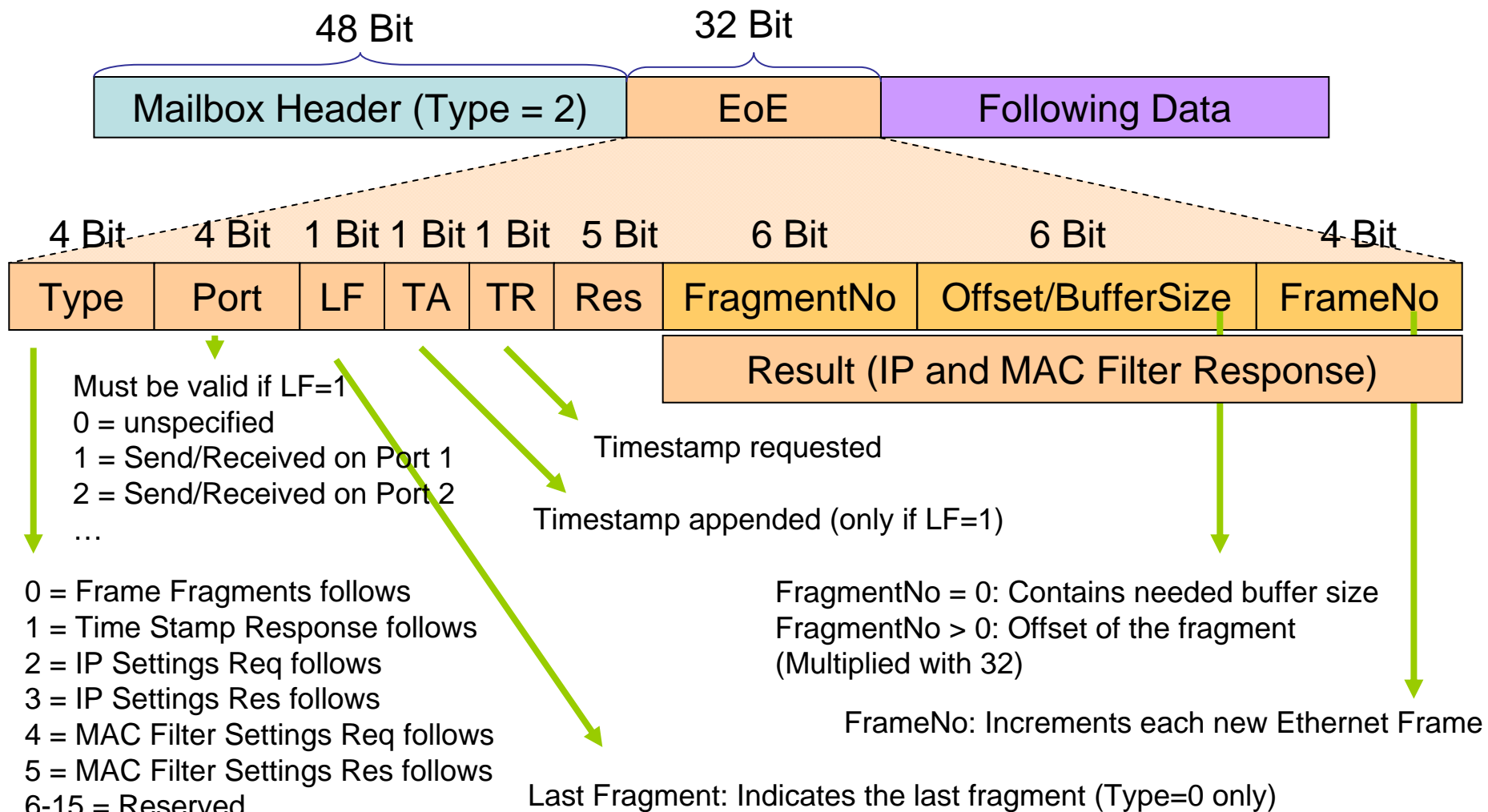
EtherCAT Master

Standards&Implementation

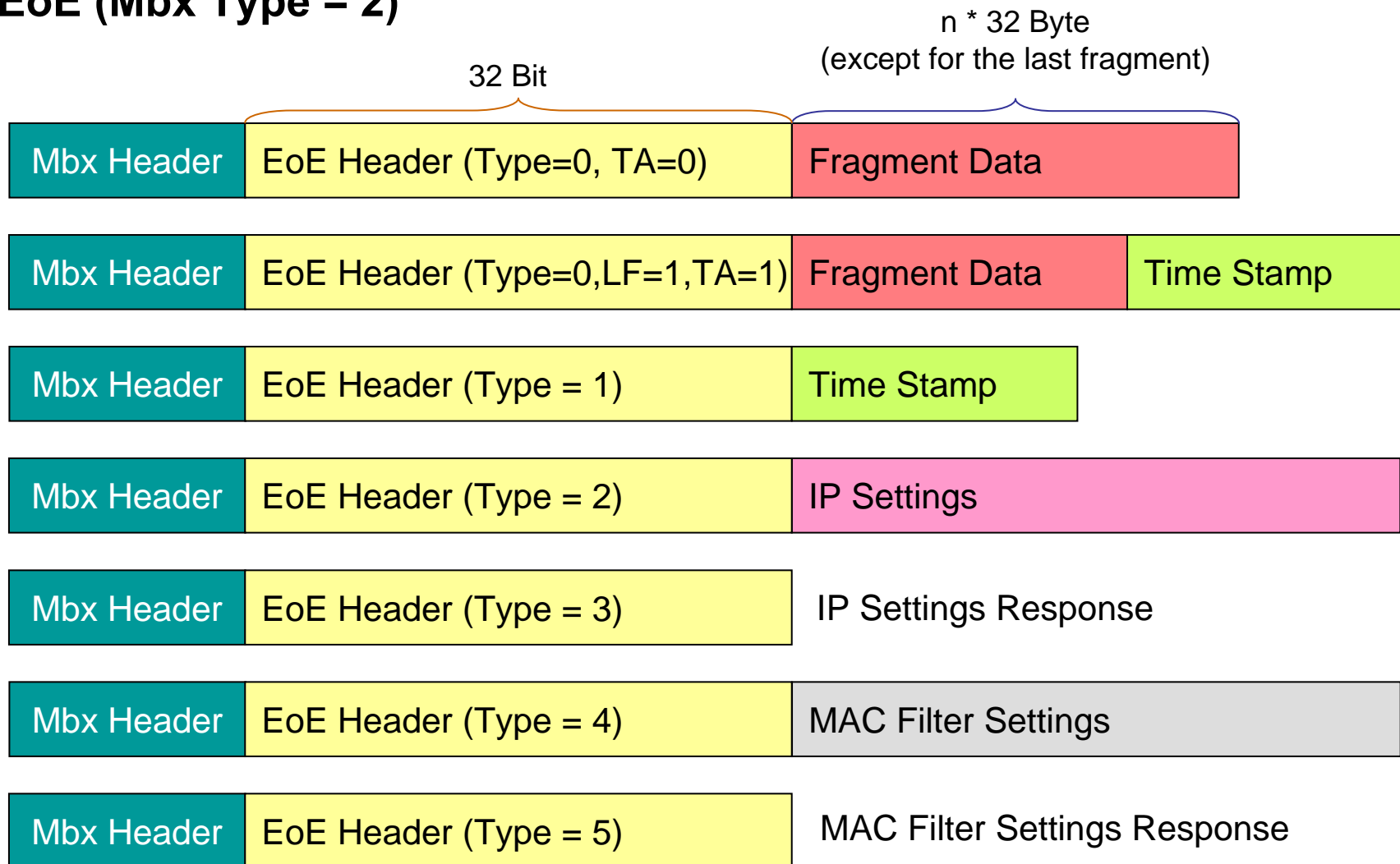


Type	EoE Frame Type 0x00 EoE Fragment Request 0x01 Initiate EoE Request 0x02 IP Parameter Request 0x03 IP Parameter Response 0x04 Set MAC Address Filter Request 0x05 Set MAC Address Filter Response
Port	Selected Port
LF	Last fragment
TA	Time stamp appended (only if LF=1)
TR	Time stamp request
Fragment No	Fragment Number of the Ethernet Frame fragment
Size	Complete size of Ethernet Frame
Frame No	Number of the Ethernet Frame

EoE Header



EoE (Mbx Type = 2)



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Standards&Implementation

- Set MAC and IP address if EtherCAT device has IP stack
- MAC Filter Settings Data Structure:
Used if EtherCAT device acts as bridge (switch)
- Result Values (IP and MAC Filter Settings only):
 - 0x0000 No Error
 - 0x0001 Unspecified Error
 - 0x0002 Unsupported Type
 - 0x0201 No IP Support
 - 0x0401 No MAC Filter Mask Support
 - ...

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Standards&Implementation

- Time Stamp
 - 32 Bit Timing information with 1 ns resolution
 - DC System Time if available (Register 910h)
 - Time stamp trigger is the beginning of Destination Address (DA) in the Ethernet Frame
- Time stamp appended (TA = 1)
 - Slave to Master:
Time stamp contains exact receive time
 - Master to Slave:
Time Stamp contains desired send time
 - Time stamp extends frame data by 32 Bit
 - TA is allowed in last fragment only (LF = 1).
If necessary, add additional fragment
 - fill the “last” fragment with parts of the Time Stamp (LF=0, TA=0) and send a very last fragment with the rest of the Time Stamp (LF=1, TA=1)
 - Slave should always append a Time Stamp if it has this feature

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Standards&Implementation

- Time Stamp requested (TR = 1)
 - Response with the exact send time and the same FrameNo requested
 - Response should be send as soon as possible
 - Can be used for segment to segment synchronization

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Standards&Implementation

- No special Error Handling required!
- EoE is a Layer 2 Protocol – logical like an Ethernet Switch
- If a fragment is missing, the whole frame will be discarded
- Frame loss is recognized at higher levels

Purpose of CAN application protocol over EtherCAT (CoE)

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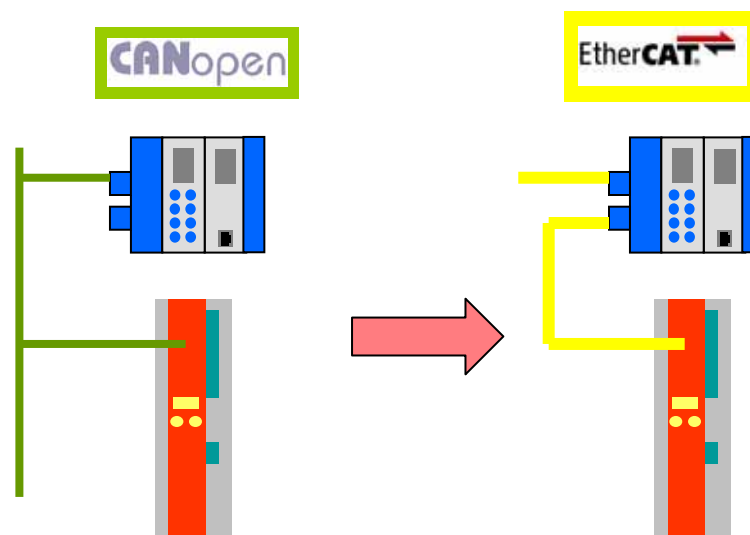
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EtherCAT Master

Standards&Implementation

- Recommended protocol for Service Data Access
 - Configuration of communication parameter
 - Configuration of device specific parameter
- Easy migration path from CANopen* Devices to EtherCAT CoE Devices
 - Protocol Stacks can be re-used



CAN application protocol over EtherCAT Device Architecture

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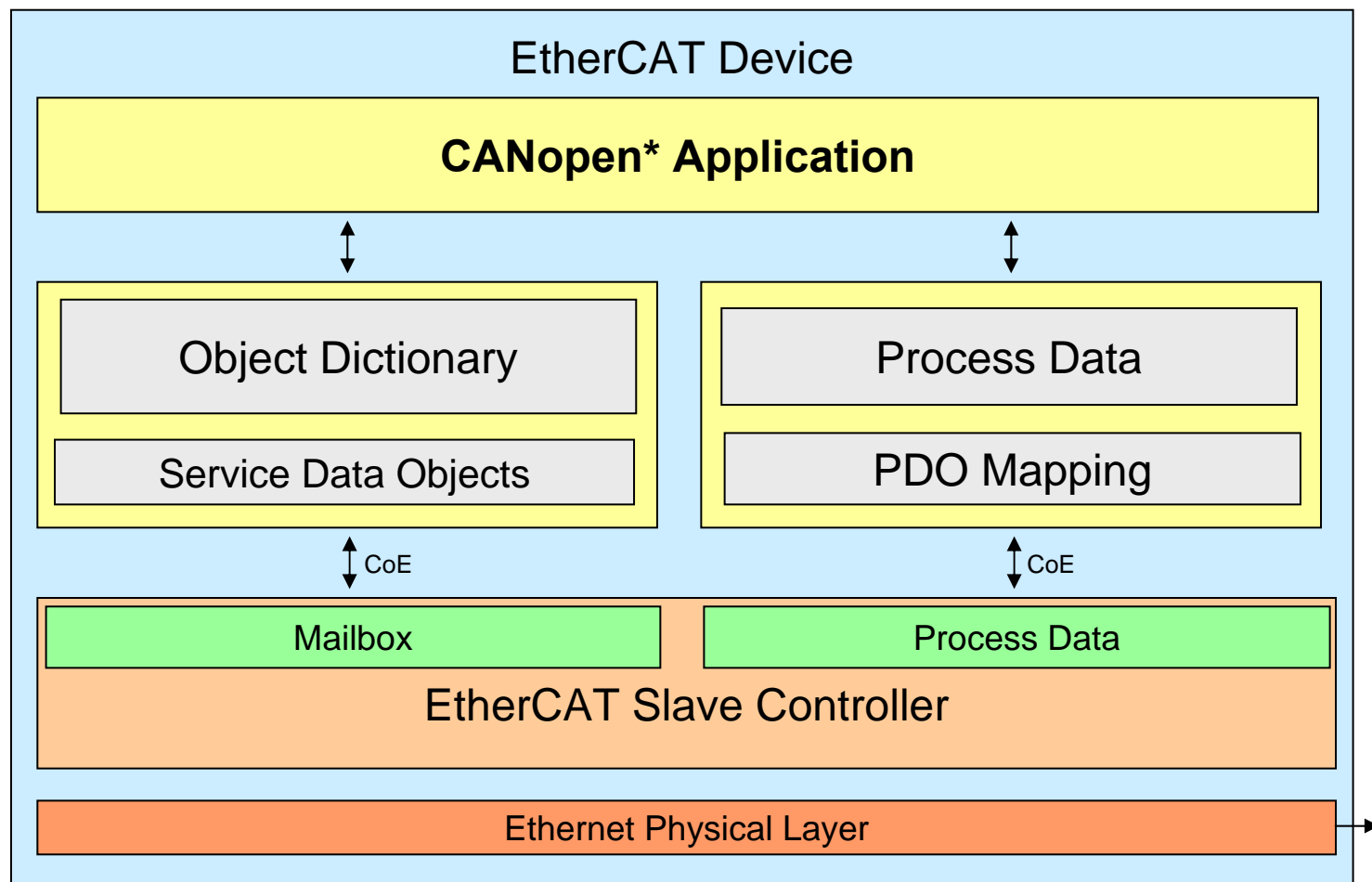
Device Profiles

- Modular Devices
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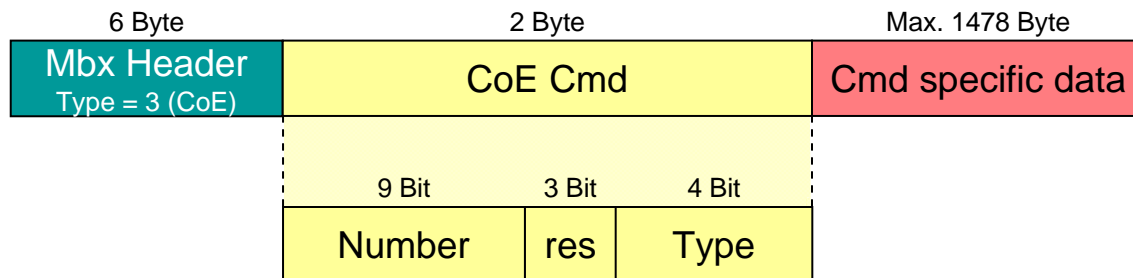
Configuration Tool

EtherCAT Master

Standards&Implementation



CoE –Frame Header



Number	PDO Number (PDO transfer only)
Type	Message Type
	0 reserved
	1 Emergency Message
	2 SDO Request
	3 SDO Response
	4 TxPDO
	5 RxPDO
	6 Remote transmission request of TxPDO
	7 Remote transmission request of RxPDO
	8 SDO information
	9-15 reserved for future use

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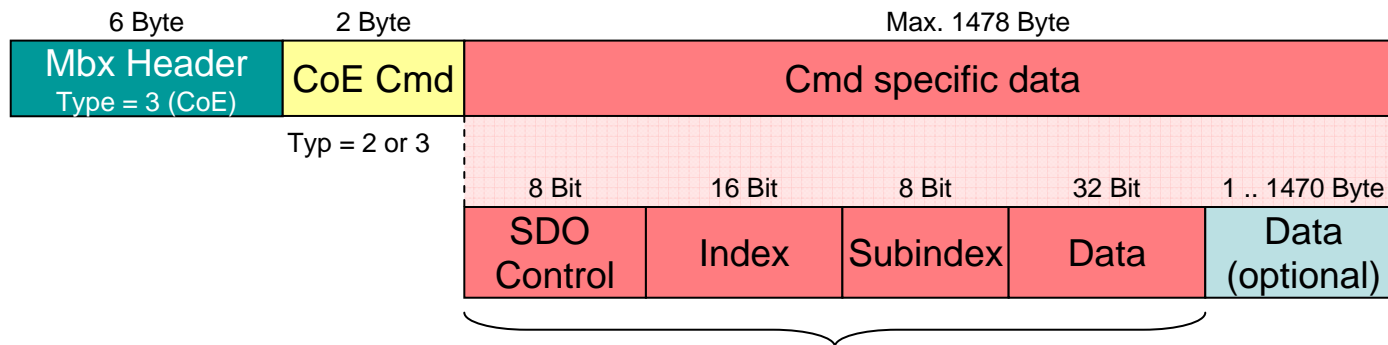
Standards&Implementation

- **SDO: Access to a CANopen* object dictionary ✓**
 - Download and Upload of parameters
 - Standard Process Data Mapping (PDO Mapping)
 - Full Access to CANopen* Profiles
- **PDO: Process Data Objects (over CoE!) ✓**
 - Direct PDO transfer
 - Remote Transmission Requests of PDOs
- **Emergency Messages ✓ (not used)**

Standard CANopen Features*

- **+ Object Dictionary Information (SDO Information)**
 - Upload of object dictionary (identifier lists)
 - Upload of object description
 - Upload of entry descriptions

CoE – Standard CANopen* Frames



Standard CANopen* Frame

SDO Control	Standard CANopen* SDO Services
Index	Object Addressing by Index ...
Subindex	... and Subindex
Data	Data for the SDO-Service
Data (optional)	Optionally more than 4 bytes of data can be sent with one frame. Full mailbox size usable

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Object Dictionary – Example CoE Drive

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EtherCAT

Read: "Modes of operation display"

Parameter list

Name	Index	Type	Subindex (SI)	
Controlword	0x6040	Variable	No Subindex available	
Statusword	0x6041			
Target position	0x607A			
Target velocity	0x60FF			
Position actual value	0x6064	Array	Number of SIs	0x607B SI0
Position Range limit	0x607B		Min Pos. Range limit	0x607B SI1
Modes of operation	0x6060		Max Pos. Range limit	0x607B SI2
Modes of operation display	0x6061			
Following error actual value	0x60F4			
Following error window	0x6065			
Gear Ratio	0x6091			
Device Type	0x1000	Record	Number of SIs	0x1018 SI0
Identity object	0x1018		Vendor ID	0x1018 SI1
			Product Code	0x1018 SI2
			Revision Number	0x1018 SI3
			Serial Number	0x1018 SI4

Object Dictionary – Example CoE Drive

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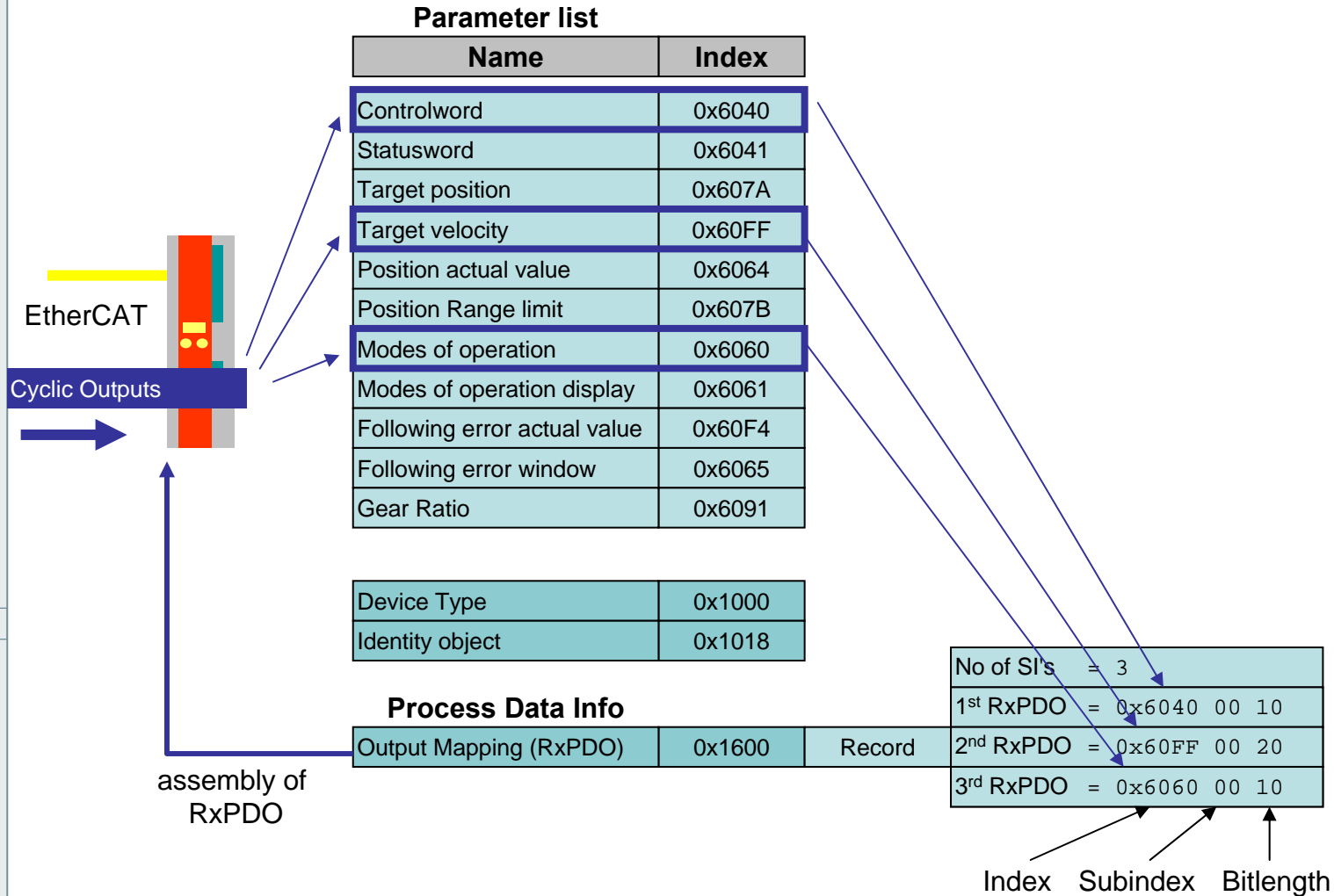
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Object Dictionary – Example CoE Drive

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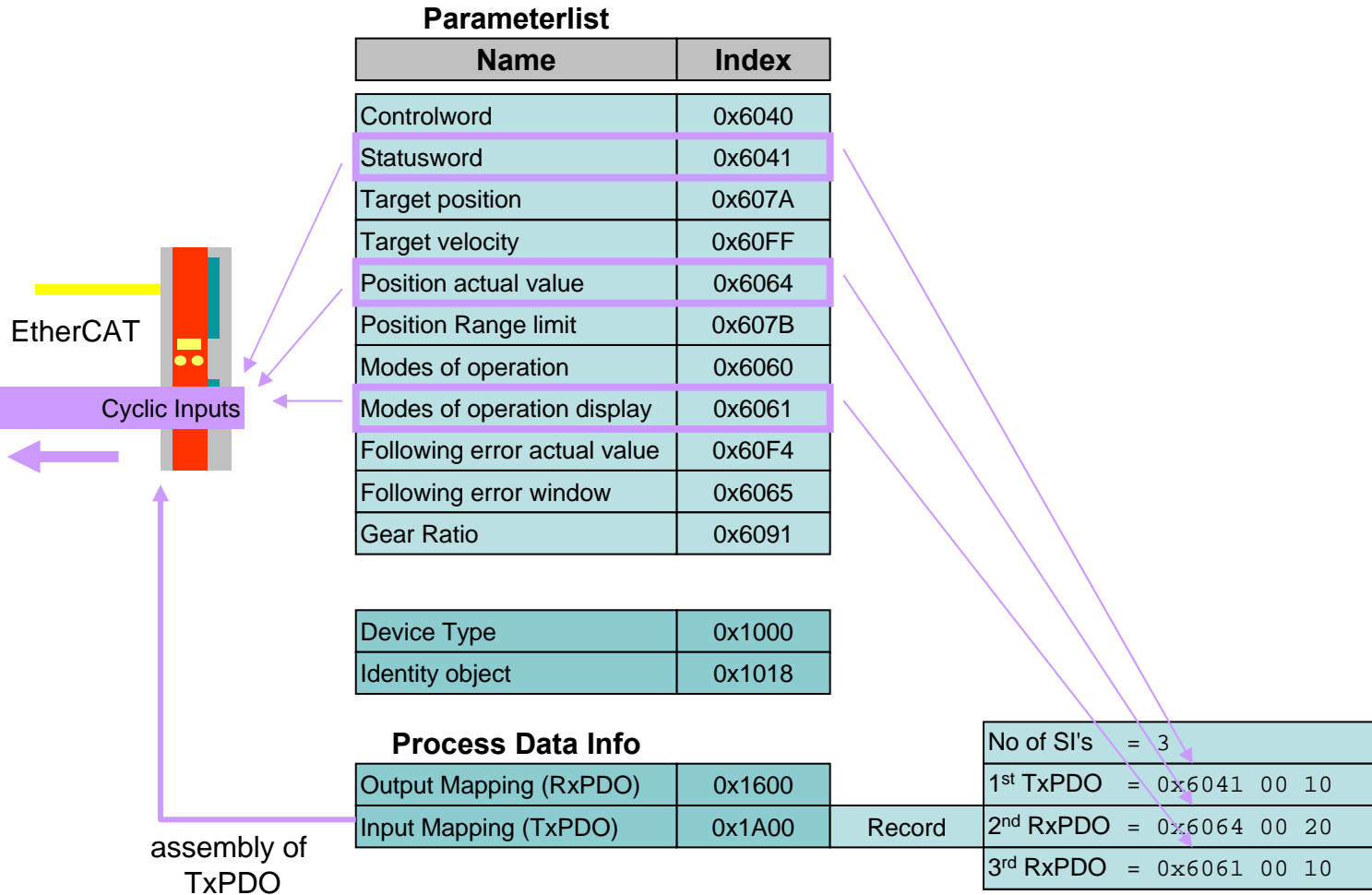
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Addressing through DL and AL

SDO Access: Download Request

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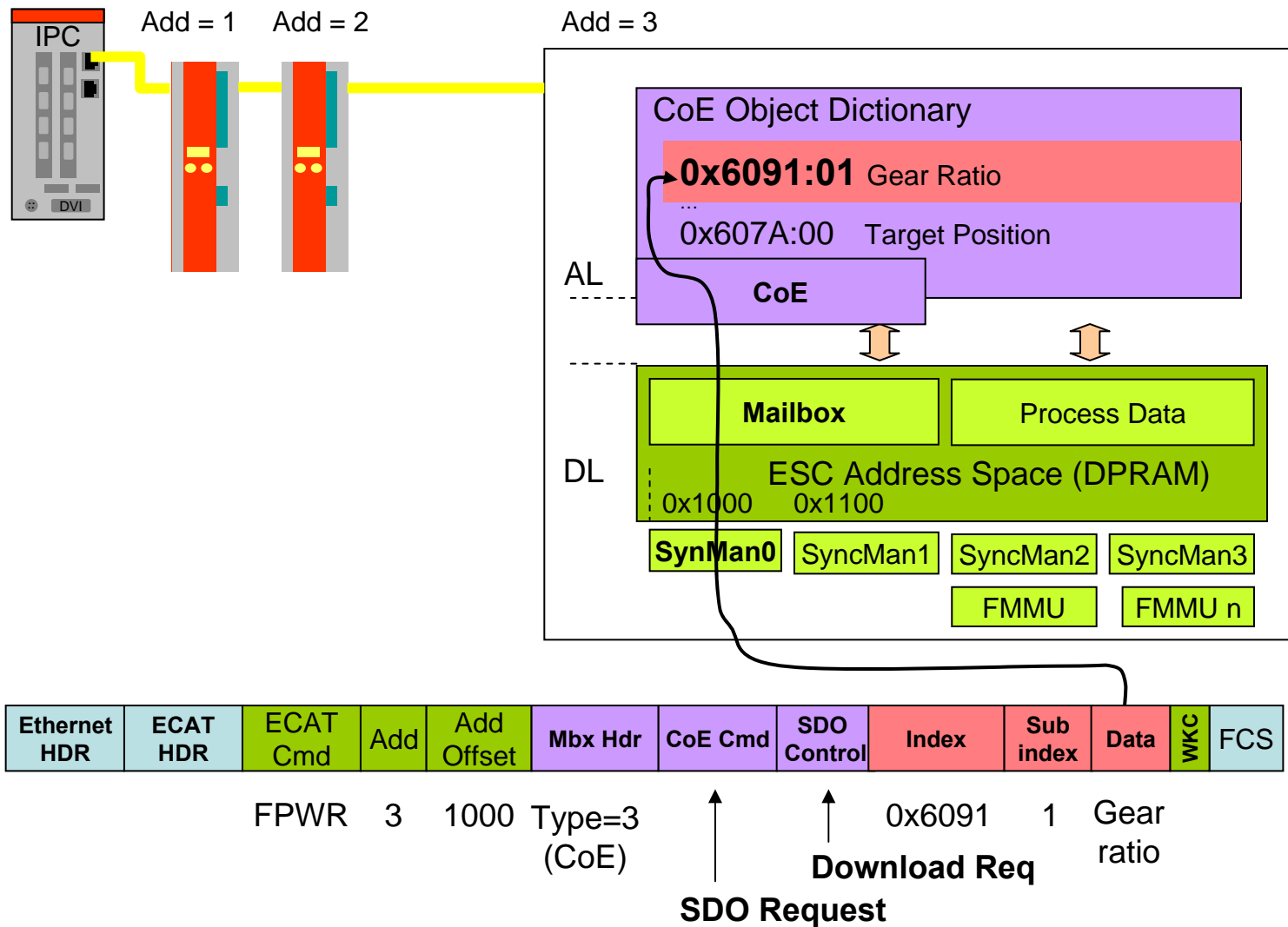
Device Profiles

- Modular Devices
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Configuration Tool

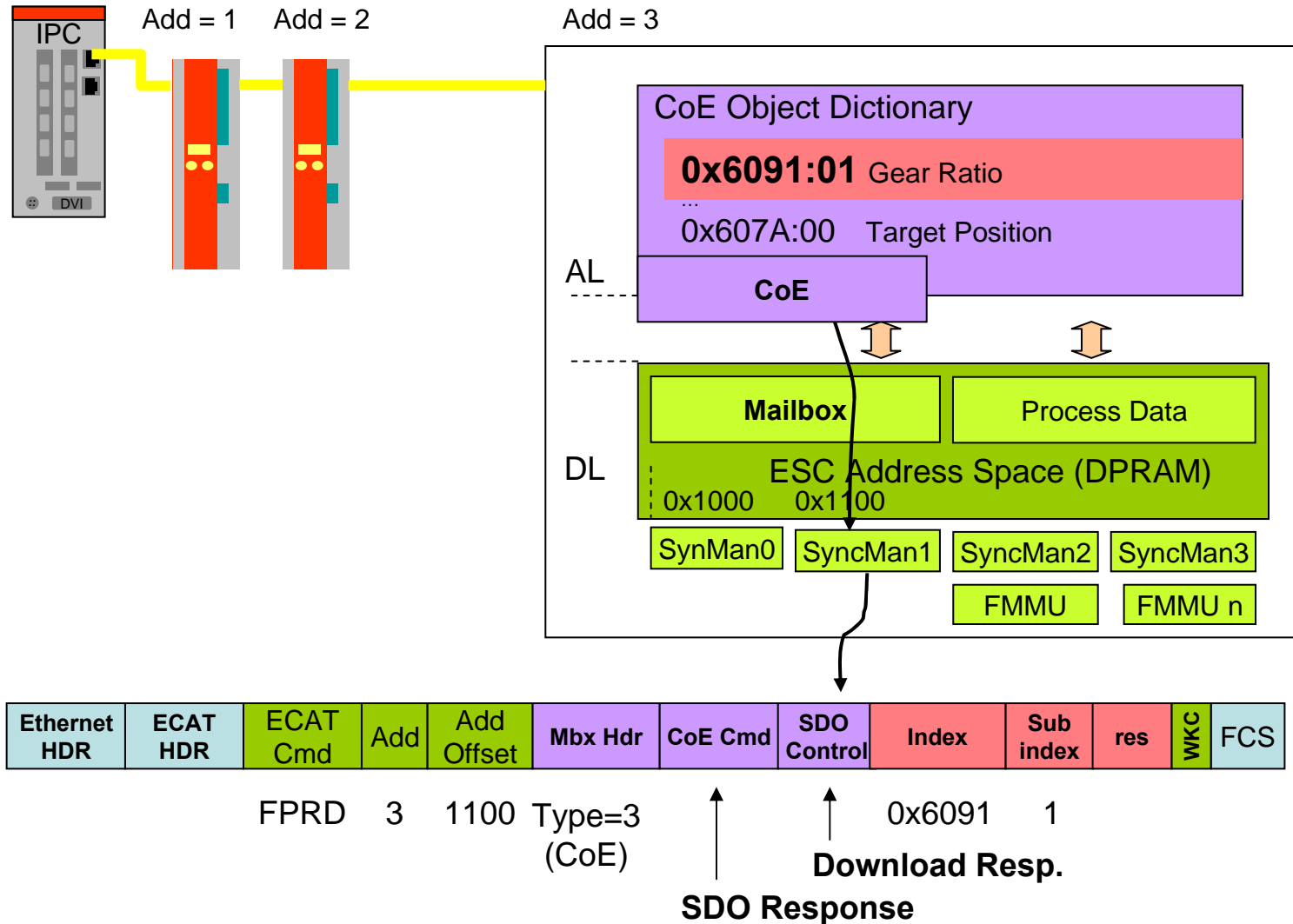
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Addressing through DL and AL

SDO Access: Download Response



Addressing through DL and AL (PDO Access)

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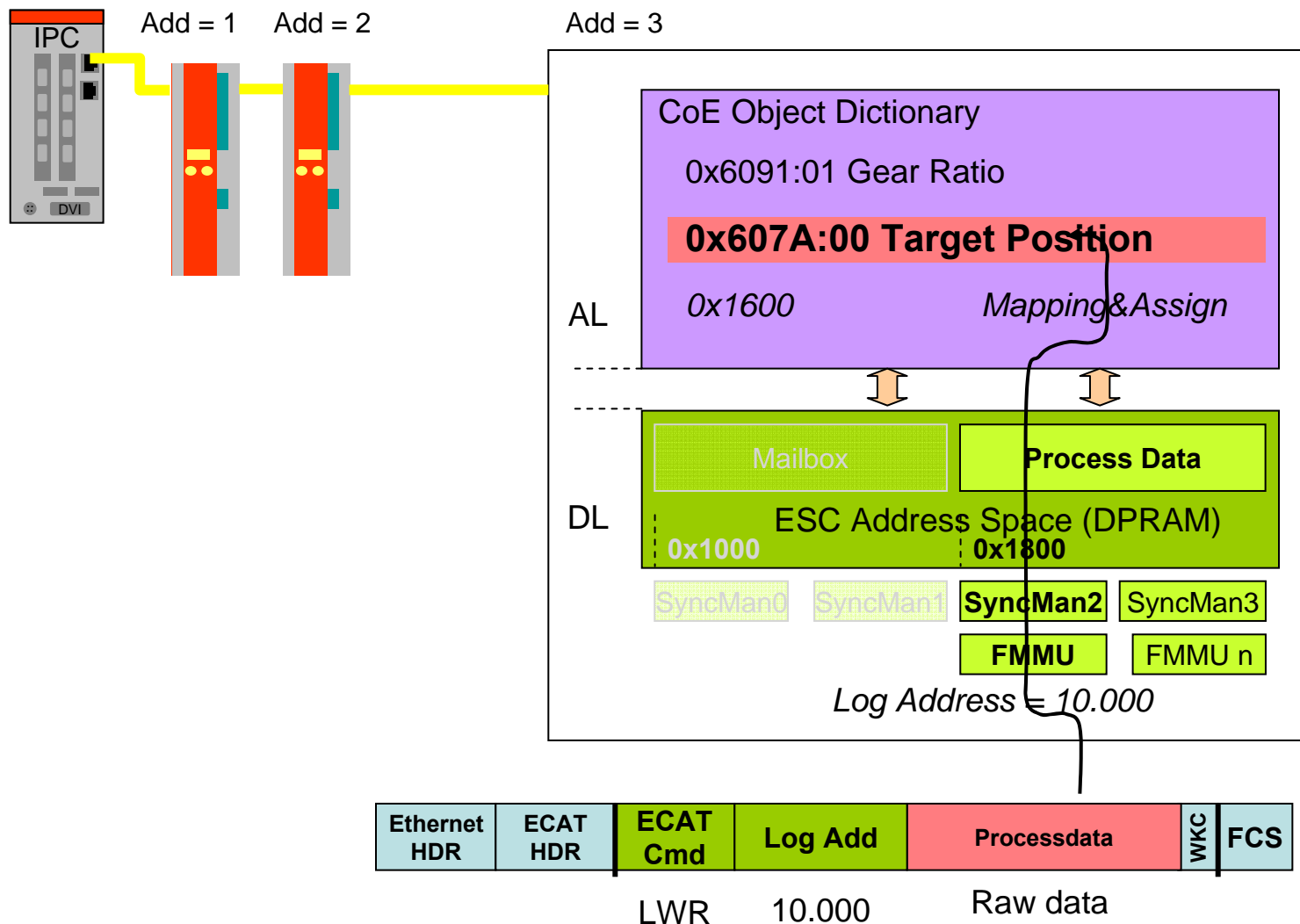
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Example: 'Initiate SDO Download' Request

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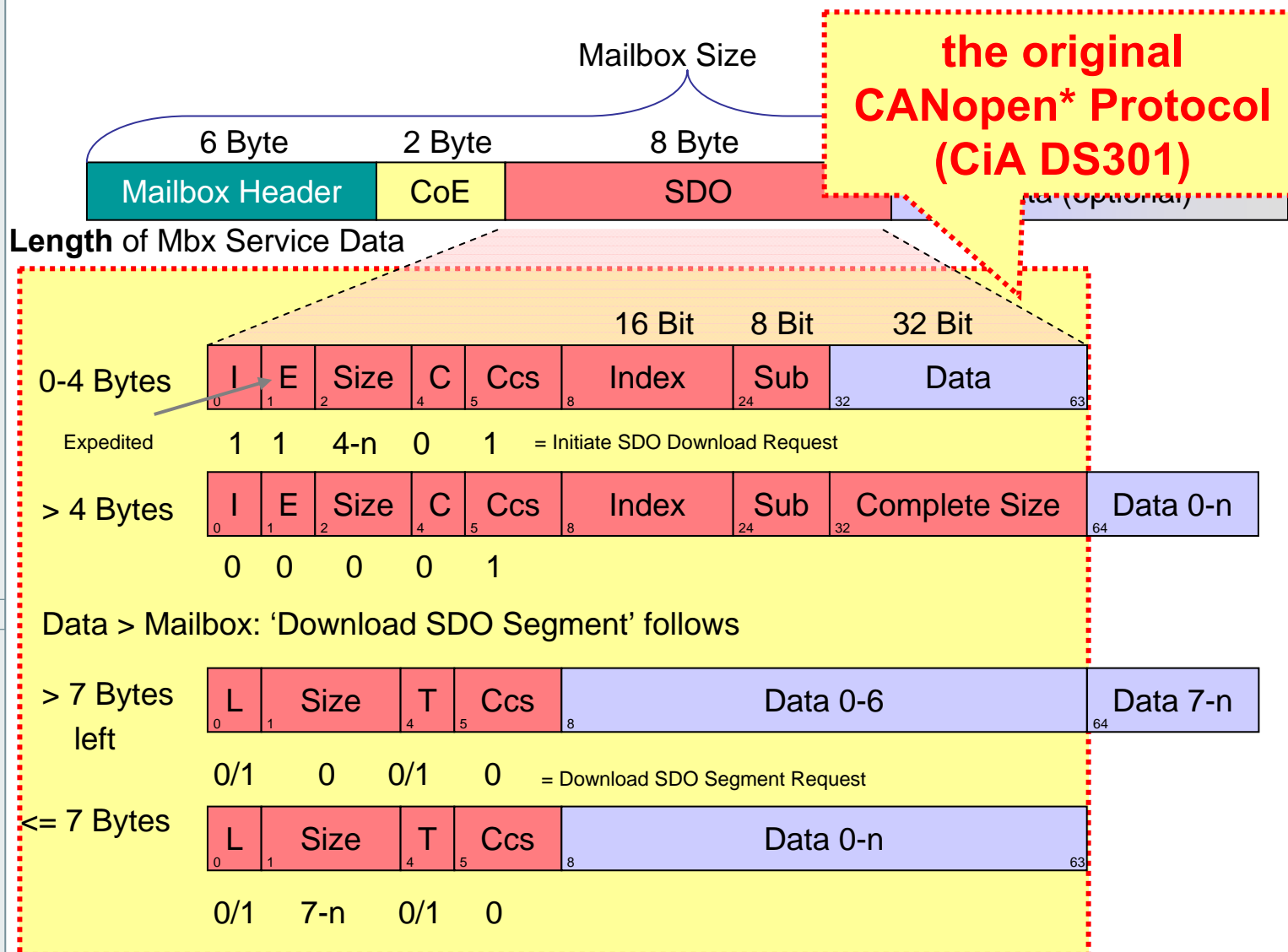
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SDO Download Services differences

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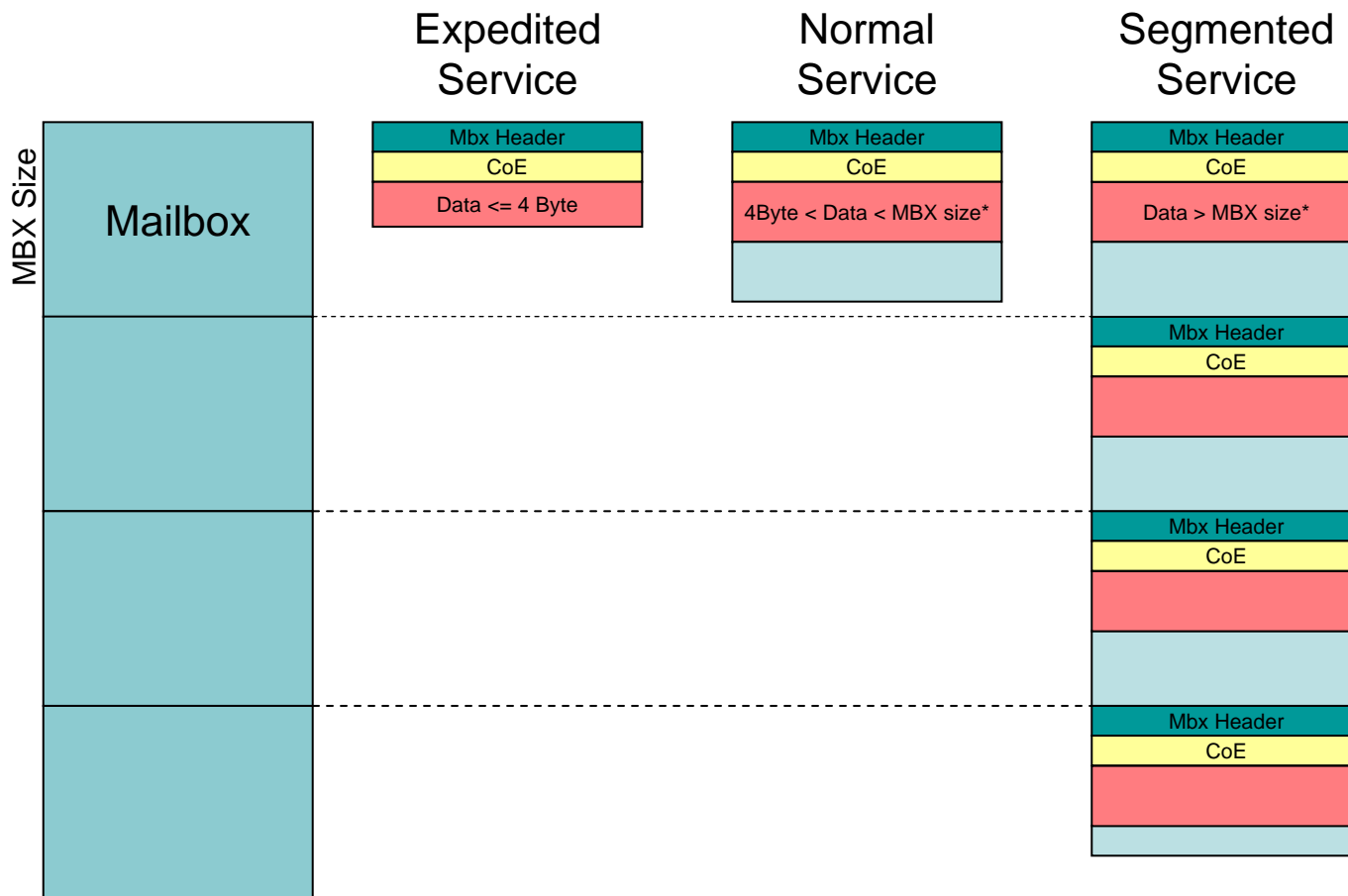
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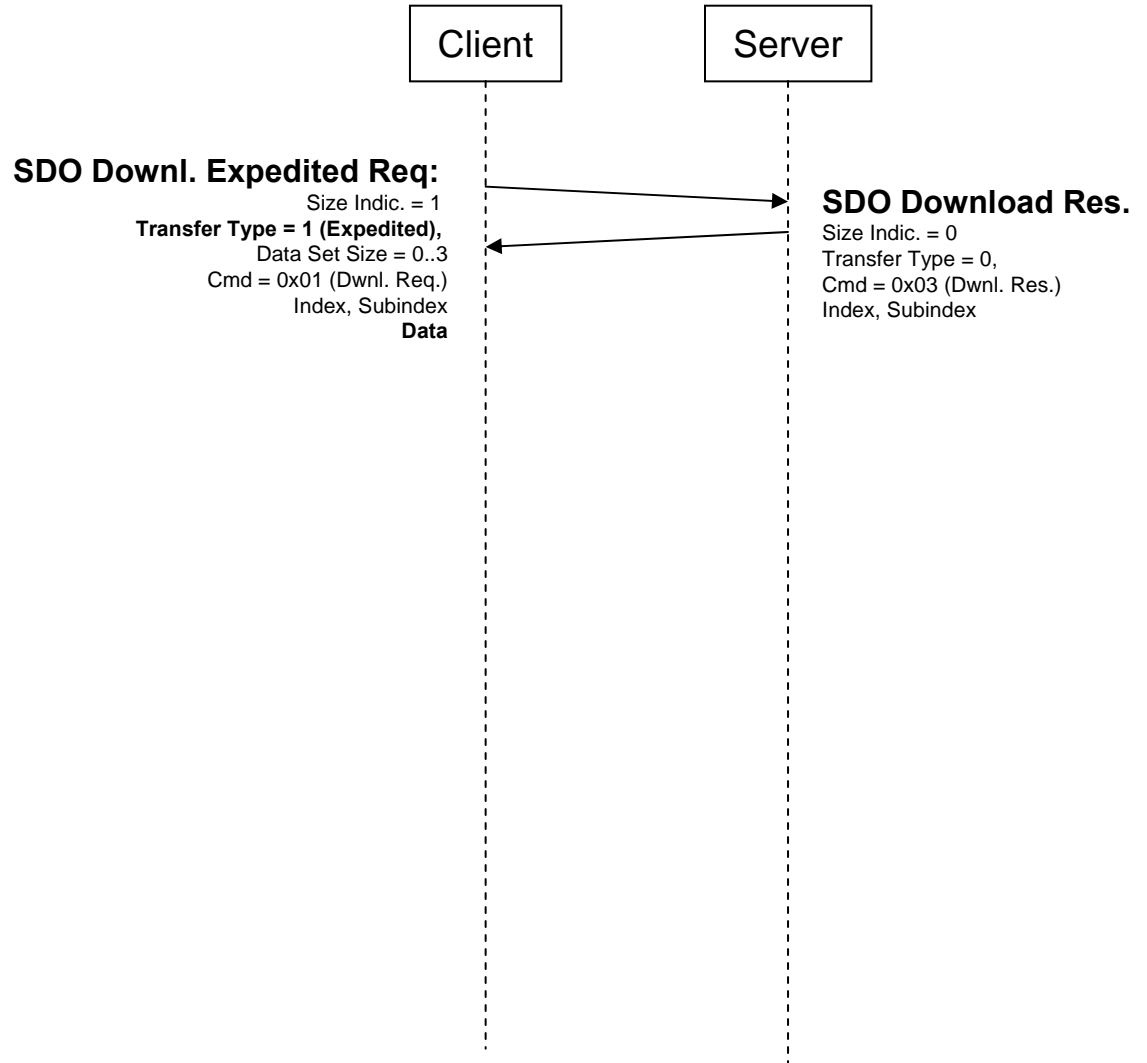
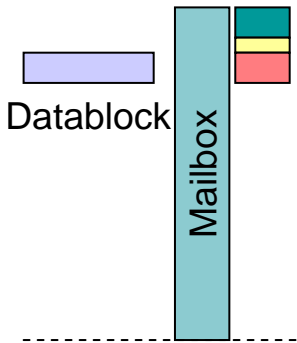
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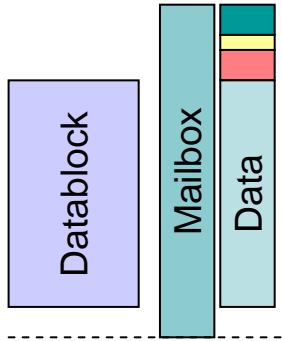


*MBX size = Mbx size – Mbx-Header – CoE-cmd – SDO-Cntrl – Index - Subindex

Example: Expedited Download



Example: Normal Download



SDO Downl. Normal Req:

Size Indic. = 1
Transfer Type = 0 (Normal)
Data Set Size = 0
Cmd = 0x01 (Dwnl. Req.)
Index, Subindex
Complete Size
Data

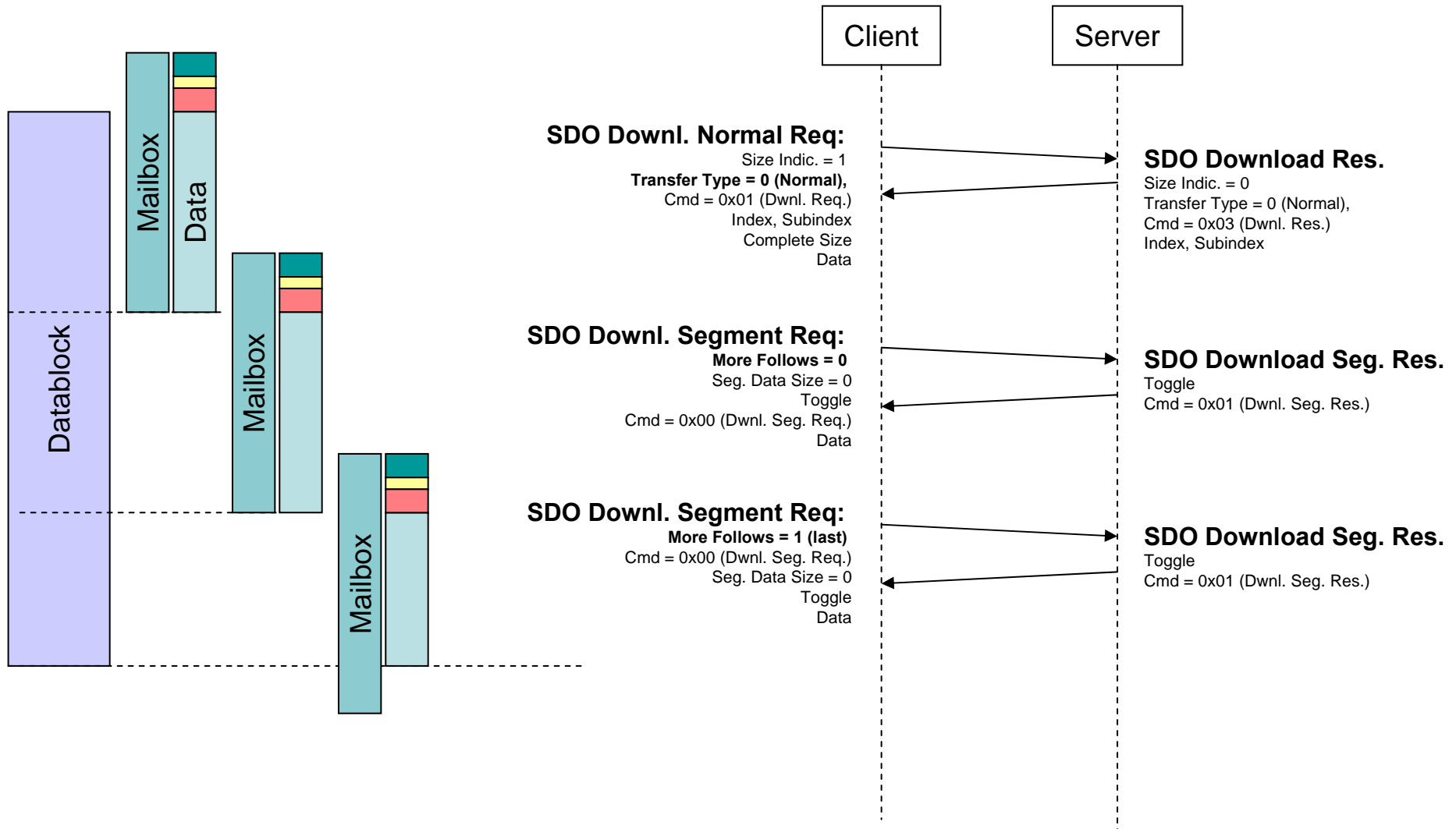
Client

Server

SDO Download Res.

Size Indic. = 0
Transfer Type = 0,
Cmd = 0x03 (Dwnl. Res.)
Index, Subindex

Example: Segmented Download



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Standards&Implementation

- Breaking of the 8 byte border
 - Full mailbox size usable
→ Block transfer unnecessary
 - ‘Initiate SDO Download’ request /
‘SDO Upload’ response can contain data after SDO header
 - ‘Download SDO Segment’ request /
‘Upload SDO Segment’ response can contain more than 7 bytes of data
- Downloading and Uploading all Subindices at once
 - Bit 4 of the Initiate ‘SDO Download / Upload’ request header indicates a ‘Complete Access’ to an Index
 - Sub Index field contains the start Subindex
 - 0: Complete Index with all Subindices
 - 1: Complete Index without Subindex 0

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- Access to object lists
 - list of all supported object identifiers
 - list of all object identifiers that can be mapped in PDOs
 - list of all object identifiers that should be included in a backup
- Access to object descriptions
 - Descriptions of objects – as defined in DS 301
- Access to entry descriptions
 - Descriptions of object entries (sub index) – as defined in DS 301

- Devices with an EtherCAT-CoE and a CANopen* interface are possible with the same object dictionary

Index Range	Meaning
0x0000 – 0x0FFF	Data Type Description
0x1000 – 0x1FFF	Communication objects <ul style="list-style-type: none"> • Device Type, Identity, PDO Mapping – like defined in DS 301 • Objects defined in DS 301 not needed are reserved for EtherCAT • Additional objects (Sync Manager Communication Type, Sync Manager PDO Assignment) located in unused areas of DS 301
0x2000 – 0x5FFF	Manufacturer specific
0x6000 – 0x9FFF	Profile specific
0xA000 – 0xFFFF	reserved

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Allgemein BK1120 EtherCAT Startup CoE - Online Online				
Update List		<input type="checkbox"/> Auto Update		
Erweitert..		All Objects		
Index	Name	Flags	Wert	
1018:02	Product Code	RO	0x04602C22 (73411618)	
1018:03	Revision Number	RO	0x00000000 (0)	
1018:04	Serial Number	RO	0x00000000 (0)	
1600:0	RxPDO Mapping Box 001	RW	> 2 <	
1600:01	Output Mapping Area 001	RW	0x7000:01, 8	
1600:02	Output Mapping Area 002	RW	0x7000:02, 40	
1602:0	RxPDO Mapping Box 003	RW	> 4 <	
1603:0	RxPDO Mapping Box 004	RW	> 3 <	
16FF:0		RW	> 1 <	
1A00:0	TxPDO Mapping Box 001	RW	> 2 <	
1A00:01	Input Mapping Area 001	RW	0x6000:01, 8	
1A00:02	Input Mapping Area 002	RW	0x6000:02, 40	
1A01:0	TxPDO Mapping Box 002	RW	> 4 <	
1A03:0	TxPDO Mapping Box 004	RW	> 3 <	
1AFF:0		RW	> 1 <	
1C00:0	Sync Manager Type	RO	> 4 <	
1C00:01	SubIndex 001	RO	0x01 (1)	
1C00:02	SubIndex 002	RO	0x02 (2)	
1C00:03	SubIndex 003	RO	0x03 (3)	
1C00:04	SubIndex 004	RO	0x04 (4)	
1C12:0	SM 002 RxPDO Assign	RW	> 4 <	
1C12:01	SubIndex 001	RW	0x16FF (5887)	
1C12:02	SubIndex 002	RW	0x1600 (5632)	
1C12:03	SubIndex 003	RW	0x1603 (5635)	
1C12:04	SubIndex 004	RW	0x1602 (5634)	
1C13:0	SM 003 TxPDO Assign	RW	> 4 <	
4000:0	Coupler Table 0 [LO]	RW	> 128 <	
4001:0	Coupler Table 0 [HI]	RW	> 128 <	
4012:0	Coupler Table 9 [LO]	RO	> 5 <	

EtherCAT Basics

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EtherCAT Master

Standards&Implementation

Index	Meaning
0x1C00	SyncManager Communication Type
0x1C10 – 0x1C2F	SyncManager PDO Assign
0x1C30 – 0x1C4F	SyncManager Parameter

- SyncManager Communication Type
 - Subindex (1-32) defines communication type of the corresponding Sync Manager channel
 - Mailbox Out (= 1 buffer write)
 - Mailbox In (= 1 buffer read)
 - Process Data Out (= 3 buffer write)
 - Process Data In (= 3 buffer read)
- SyncManager PDO Assign
 - Contains a list of assigned PDOs for each Sync Manager channel (Index of PDO mapping objects)
 - Assigned PDO in Subindex 1 to n describe the process data parts of the Sync Manager channel
- SyncManager Parameter
 - Subldx 1: Synchronization type
(Freerun, synchron, DC Sync0, DC Sync1, SyncSm0 .. SyncSm1F)
 - Subldx 2: Cycle time
 - Subldx 3: Shift time

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Configuration Tool**EtherCAT Master****Standards&Implementation**

- No mapping protocol implemented for very simple devices
 - Fixed process data
 - Readable via EEPROM – no SDO protocol necessary
- Readable PDO Mapping
 - Fixed process data mapping
 - Readable via SDO
- Selectable PDO Mapping
 - Multiple fixed PDO – selectable via CoE object (1C1xh)
 - Selectable via SDO (required)
- Variable PDO Mapping
 - Configurable via CoE or SoE required
 - Writable PDO content

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DC
Startup
CoE - Online
Online

Sync Manager:

SM	Size	Type	Flags
0	246	MbxOut	
1	246	MbxIn	
2	3	Outputs	
3	5	Inputs	

PDO List:

Index	Size	SM	Name	Flags
0x1600	3.0	2	RxPDO 001 mapping	F
0x1610	4.0		RxPDO 017 mapping	F
0x1A00	5.0	3	TxPDO 001 mapping	F
0x1A10	6.0		TxPDO 017 mapping	F

PDO Assignment (0x1C12):

☒ 0x1600
☐ 0x1610 (excluded by 0x1600)

Download

☒ PDO Assignment
☒ PDO Configuration

PDO Content (0x1600):

Index	Size	Offs	Name	Type
0x3001:1	1.0	0.0	Control	USINT
0x3001:2	2.0	1.0	Data	UINT
		3.0		

Load PDO info from device

CoE – Emergency Message

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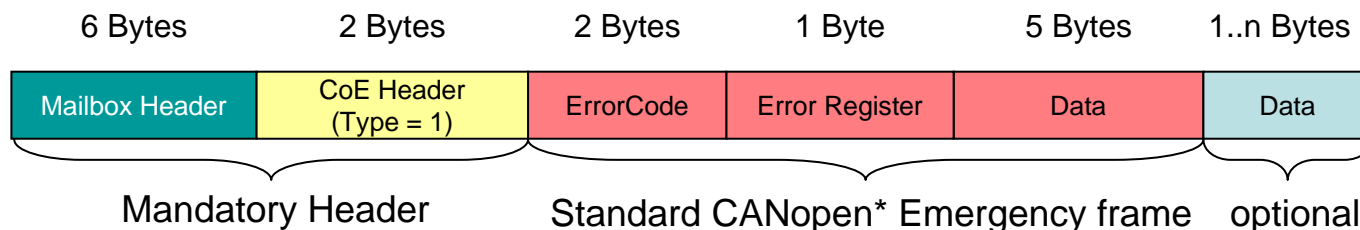
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EtherCAT Master

Standards&Implementation



- Standard CANopen* Emergency frames can be used
- More than 5 bytes of data can be send optionally with one frame
- New: Diagnosis Object

File Access over EtherCAT (FoE)

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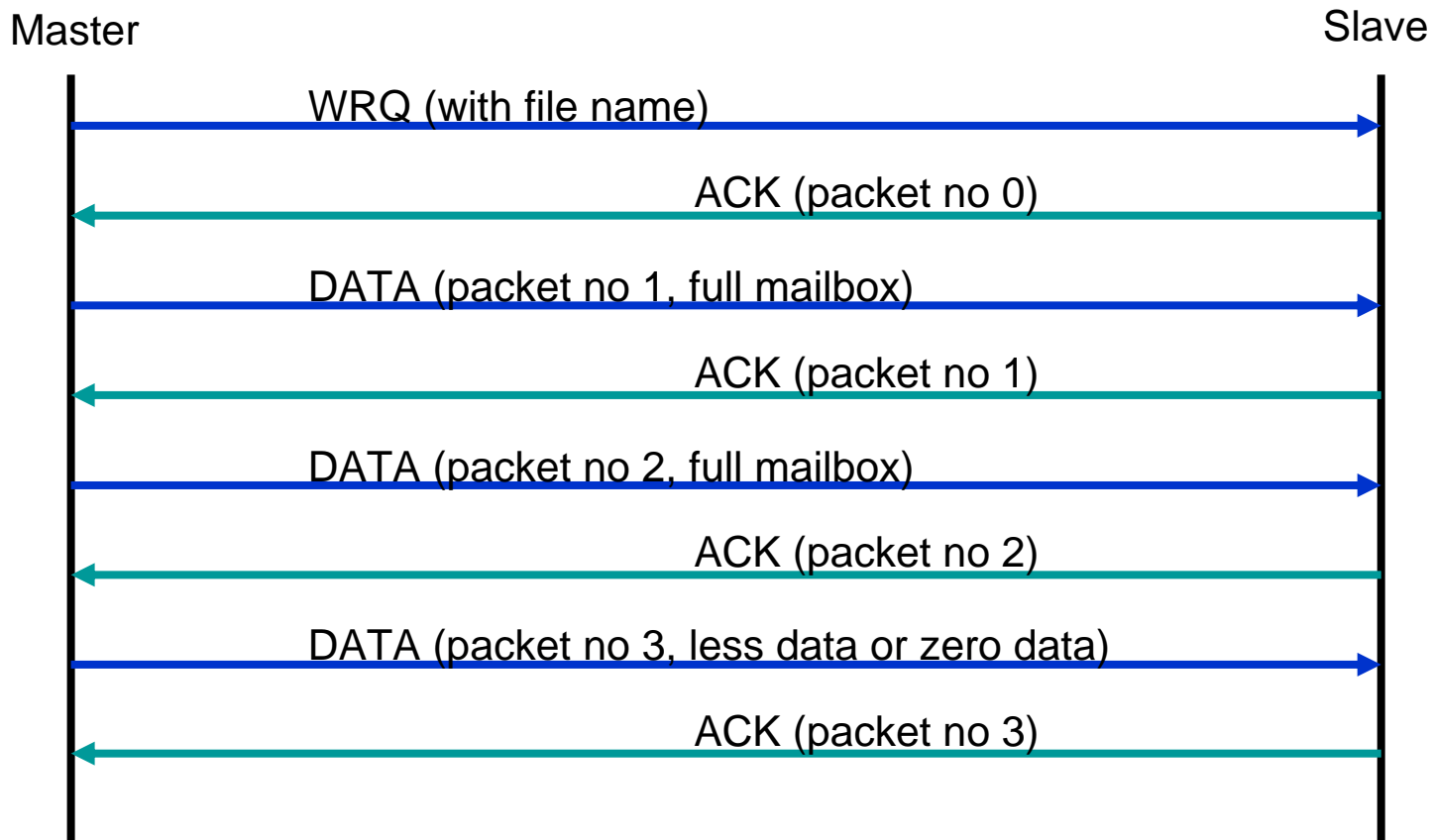
Configuration Tool

EtherCAT Master

Standards&Implementation

- Similar to TFTP (Trivial File Transfer Protocol, RFC 1350)
- Simple to implement – suitable for bootstrap loaders
- 6 Services are defined:
 - WRQ: Write request with “file name”
 - RRQ: Read request with “file name”
 - DATA: Data block (full mailbox size used)
 - ACK: Acknowledgment of DATA and WRQ requests
 - ERR: Error notification with predefined error codes
 - BUSY: Busy notification in case of longer procedures, extension to TFTP (e.g. erasing of flash modules)
- Special mailbox configuration for bootstrap mode possible
 - Fixed addresses and fixed size of the mailbox
 - Configuration defined by device (EEPROM)

File Access over EtherCAT (WRQ, normal)



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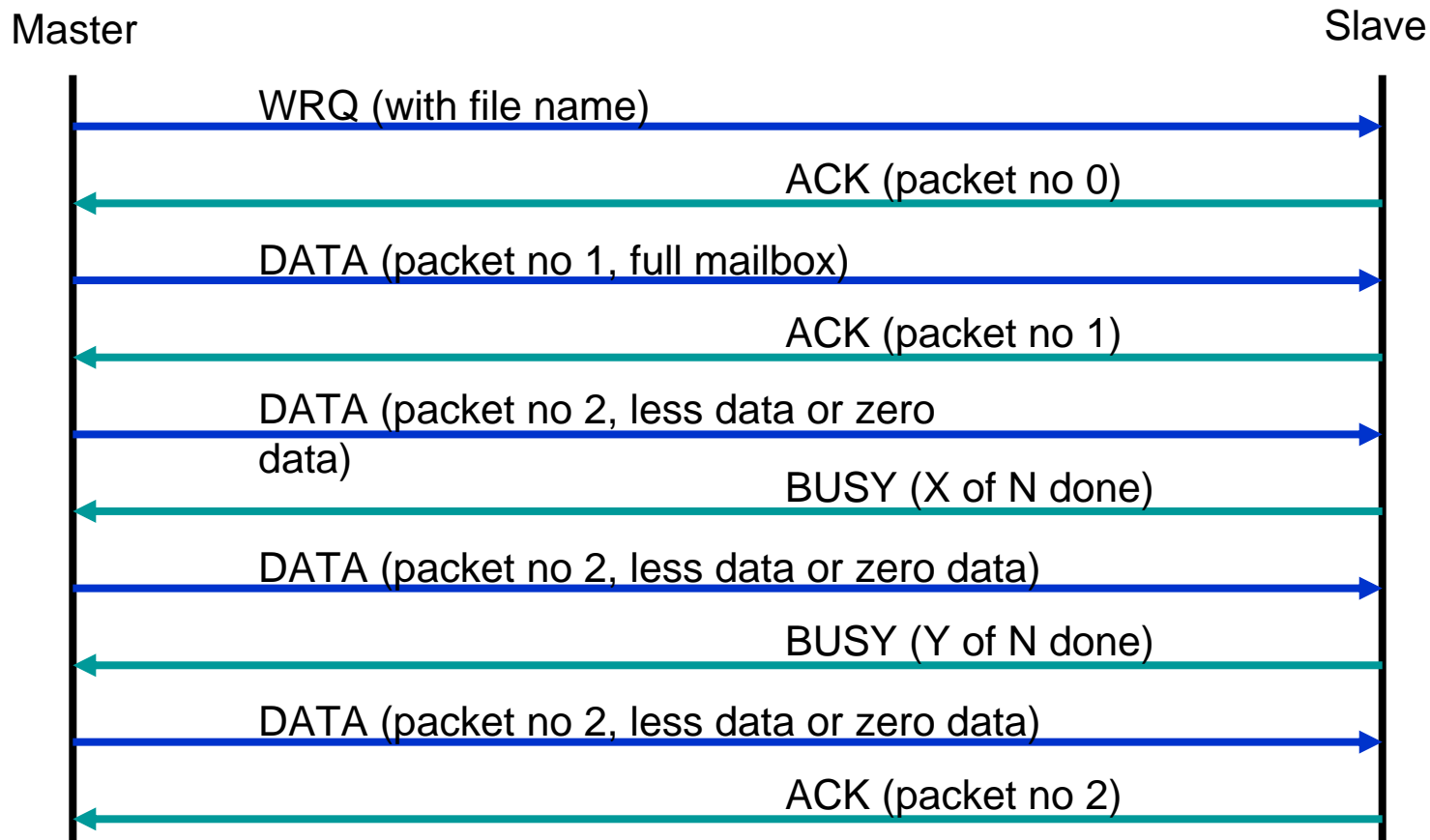
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File Access over EtherCAT (WRQ, with busy)



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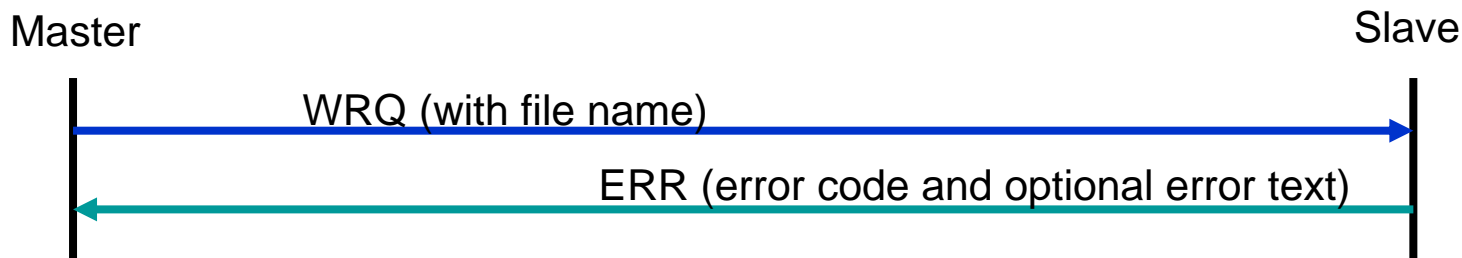
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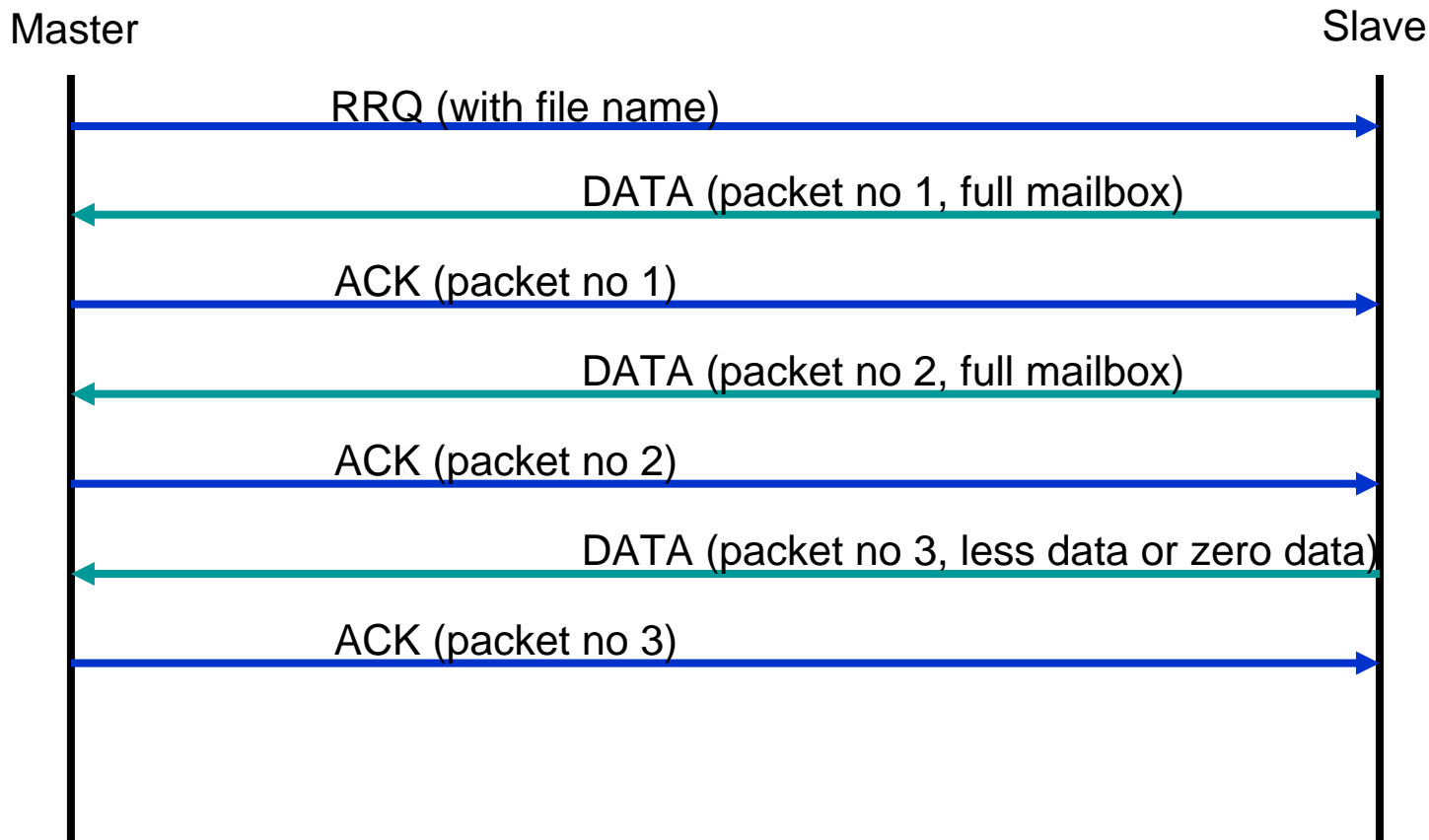
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Servo Drive over EtherCAT (SoE)

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Standards&Implementation

- Implements Service Channel
 - Read / Write to several elements of an IDN (Ident number)
 - Support of Procedure Commands
 - Slave Info
- The mapping of the IEC 61800-7-1 Annex D (SERCOS™) on EtherCAT is described in IEC 61800-7-3 Annex D

SoE – Frame Header

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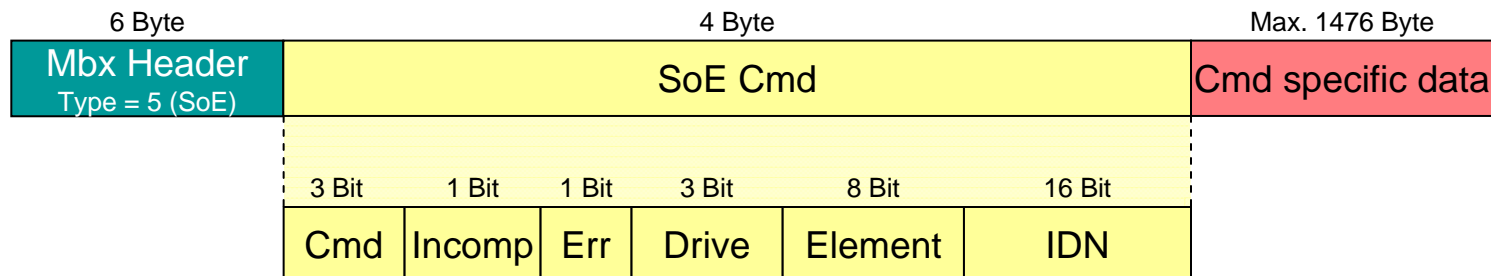
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Standards&Implementation



Command	Command Type
	Read Request, Read Response, Write Request, Write Response, Notification, SlaveInfo
Incomplete	Indicates if execution of another service is needed to complete the operation
Error	Indicates if an Error has occurred
Drive	Contains the address of the drive inside the slave device that is addressed
Element	Contains the ElementFlags. There is a single Flag for each element of an IDN indicating which elements of the object addressed by the IDN are accessed
IDN	contains IDN according to IEC 61800-7-2 Annex D or an indicator for fragments left in case of segmented service

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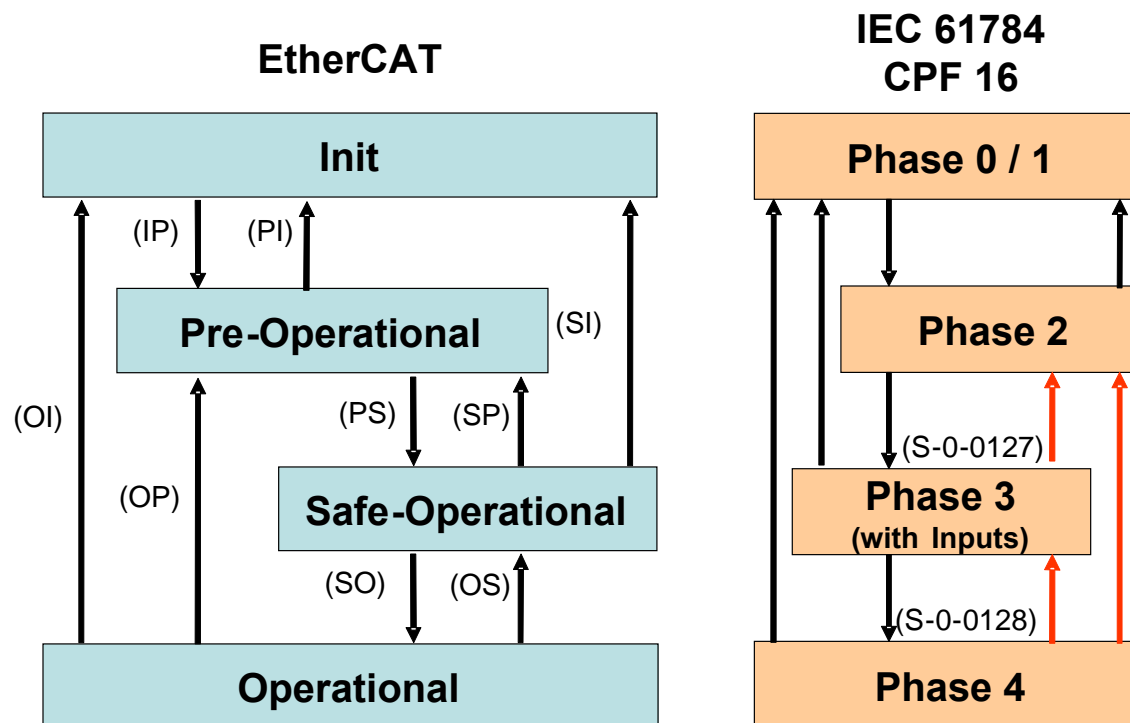
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Standards&Implementation



- SERCOS communication phases (CPs) comparable to EtherCAT state machine
- Phases 0 and 1 covered by the 'Init'
- Phase 2 corresponds to 'Pre-Operational'
 - allows access to the IDNs via 'service channel' (SoE).
- Phase 3 mapped to 'Safe-Operational'
 - slave shall transmit valid inputs, ignore outputs from the master.
- 'Operational' corresponds to phase 4
 - all inputs and outputs are valid.

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IDN	Description
S-0-0003	Minimum AT transmit starting time (T1min)
S-0-0004	Transmit/receive transition time (TATMT)
S-0-0005	Minimum feedback acquisition time (T4min)
S-0-0009	Beginning address in master data telegram (MDT POS)
S-0-0010	Length of master data telegram (MDT LEN)
S-0-0088	Receive to receive recovery time (TMTSG)
S-0-0090	Command value transmit time (TMTSG)
S-0-0127	C100 Communication phase 3 transition check Functionality done in EtherCAT transition from 'Pre-Operational' to 'Safe-Operational'. If the transition fails, the reason for this failure can be evaluated via S-0-0021.
S-0-0128	C200 Communication phase 4 transition check Functionality done in EtherCAT transition from 'Safe-Operational' to 'Operational'. If the transition fails, the reason for this failure can be evaluated via S-0-0022.

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IDN	Meaning with SoE
S-0-0006	AT Transmission starting time (T1) T1 specifies the time offset from the EtherCAT sync signal to the time until the application shall provide new AT data inside the EtherCAT slave controller memory.
S-0-0014	Interface status This parameter should reflect DL status, AL status and AL status code of EtherCAT
S-0-0028	MST error counter MST error counter indicates missing Datagrams for cyclic data transfers This parameter should reflect RX-error counter and Lost-link counter of EtherCAT
S-0-0089	MDT Transmit starting time (T2) T2 specifies the time offset from the EtherCAT sync signal to the time until new MDT data are available inside the EtherCAT slave controller memory.

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Configuration Tool**EtherCAT Master****Standards&Implementation**

- CPF16 Phase 0-2
- No synchronisation between master and slave.
- Service channel communication via EtherCAT mailbox.
- CPF16 Phase 3-4
- Synchronisation via 'Distributed Clock' (DC) or by Sync Manager event.
- Master configures the DC unit to generate a sync event.
- The sync event is set typically at the end of communication (Sync Manager event operates in the same way).
- The sync signal compares to the end of MST telegram as defined in SERCOS part of IEC 61158-6.

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Standards&Implementation

- Sync Manager 2 for Output Data buffered mode, contains MDT data
- Sync Manager 3 for Input Data mailbox mode, contains AT data
- MDT, AT configured via S-0-0015, S-0-0016 and S-0-0024.
- Process data consists of drive control/status word followed by S-0-0015, S-0-0016, S-0-0024 defined values.
- Service channel data not included in process data.

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Configuration Tool**EtherCAT Master****Standards&Implementation**

- SoE Service Channel (SSC) equivalent to SERCOS Service Channel (SVC) for non-cyclic data exchange.
- Implemented as mailbox with the SoE protocol type allows to access IDNs and their elements.
- Transfer of multiple elements of an IDN is possible.
- SSC uses
 - confirmed services (Write, Read, Procedure Command)
 - initiated by the Master (Client),
 - unconfirmed services (Abort SSC Command Execution, Write SSC Fragment
 - initiated by the Master
 - unconfirmed services (Read SSC Fragment, Notify SSC Command Execution, SSC Slave Info)
 - initiated by the Slave (server).

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EtherCAT Master

Standards&Implementation

- Device Description File in XML format
- One file suitable for a set of devices (from one Vendor)
- File contains information about:
 - Vendor
 - Vendor ID, Name, Logo, ...
 - Device groups
 - Organization units to help configuration tools
 - Device
 - Device Identity, Name, PDI type
 - PDO Mapping
 - FMMU / SyncManager
 - number and usage
- Schema is defined in “EtherCATInfo.xsd”
- Can be viewed with Browser, Text Editor or XML Editors

General Structure

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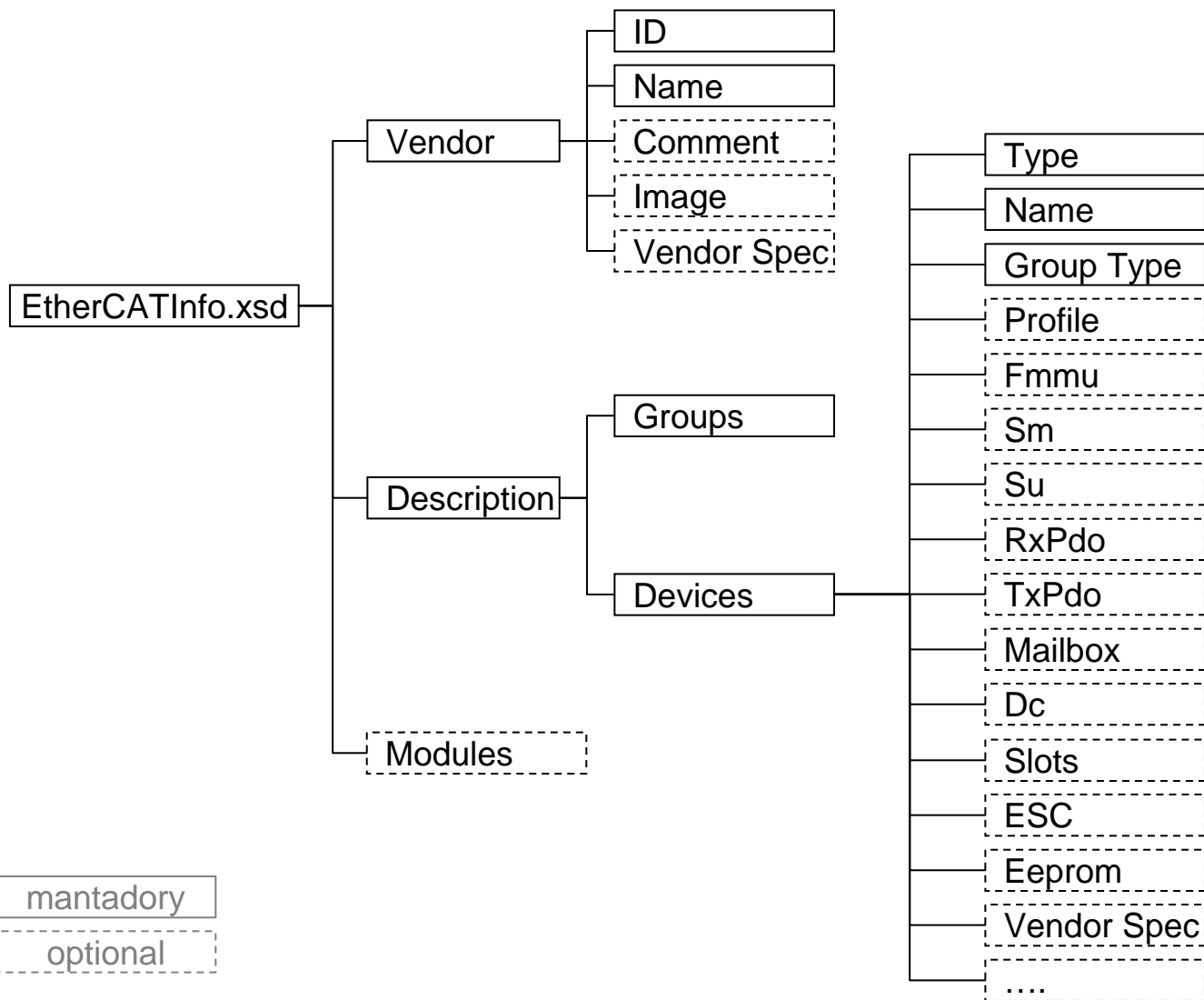
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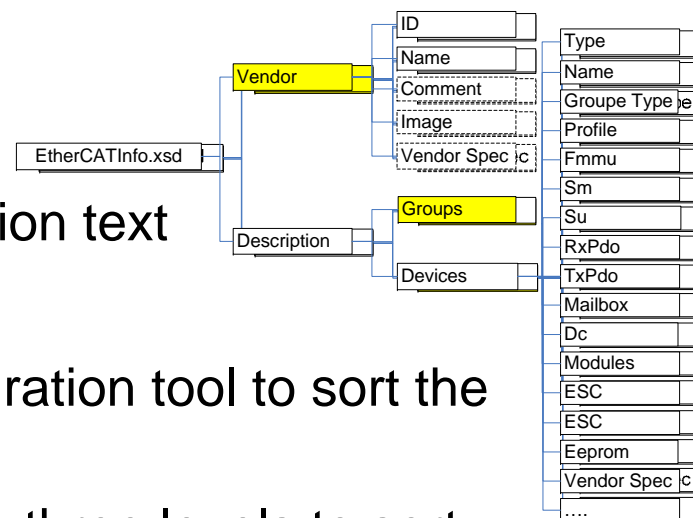
Configuration Tool

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Standards&Implementation

• Vendor

- vendor ID, vendor name
- Bitmap image and information text



• Groups

- Device groups help configuration tool to sort the devices.
- Configuration tool may use three levels to sort devices: vendor, group and device.
- At least one device group must be provided, each device is assigned to one group

• Devices

- Device description shall contain Type, Name and Group Type
- FMMU, SM, PDO, Mailbox and Eeprom are recommended

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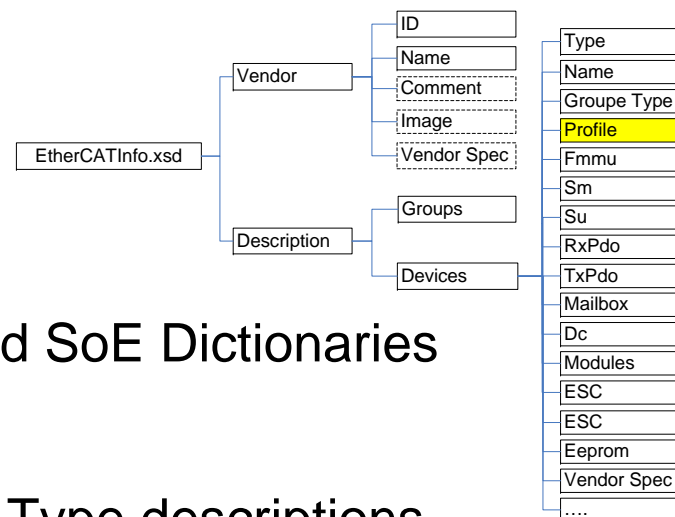
Configuration Tool

EtherCAT Master

Standards&Implementation

• Profile

- Profile Information
- Object Dictionary
- Same Structure for CoE and SoE Dictionaries



- Separation of Object and Data Type descriptions
 - Flat list of Objects
 - All Objects derived from a Data Type
 - ARRAY, RECORD (CoE) and Variable Data (SoE) defined as Data Types
 - Data Type describes the complete data of an object exactly as for a “Complete Access” download or upload

Data Types and Objects

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Element "Profile"

Element "Data Type"

Data Type "STRING(20)"

Element "Object"

Object "Device Name"

Name	BaseType	BitSize	EnumInfo	SubItem	ArrayInfo
1 BYTE		8			
2 UDINT		32			
3 UNT		16			
4 USINT		8			
5 STRING(20)		160			
6 STRING(7)		56			
7 DT0800		8	EnumInfo (4)		
8 DT0801		8	EnumInfo (4)		
9 DT0802		8	EnumInfo (5)		
10 DT1018		144		SubItem (5)	
11 DT1600		80		SubItem (3)	
12 DT1A00		176		SubItem (6)	
13 DT1C00ARR	USINT	32			ArrayInfo
14 DT1C00		48		SubItem (2)	
15 DT1C12ARR	UNT	16			ArrayInfo
16 DT1C12		32		SubItem (2)	
17 DT1C13ARR	UNT	16			ArrayInfo
18 DT1C13		32		SubItem (2)	
19 DT1C32		96		SubItem (4)	
20 DT6000		96		SubItem (6)	
21 DT7000		48		SubItem (3)	
22 DT8000		24		SubItem (2)	
23 DTA000		32		SubItem (3)	

Index	Object Name	Type	BitSize	Info	Flags
1 #x1000	Device Type	UDINT	32	Info	Flags
2 #x1008	Device Name	STRING(20)	160	Info	Flags
3 #x1009	Hardware Version	STRING(7)	56	Info	Flags
4 #x100a	Software Version	STRING(7)	56	Info	Flags
5 #x1018	Identity	DT1018	144		Flags
6 #x1600	Output mapping	DT1600	80		Flags
7 #x1a00	Input mapping	DT1A00	176		Flags
8 #x1c00	Sync Manager Communication Type	DT1C00	48		Flags
9 #x1c12	Sync Manager RxPDO Assign	DT1C12	32		Flags
10 #x1c13	Sync Manager TxPDO Assign	DT1C13	32		Flags
11 #x1c32	Sync Manager 2 Parameter	DT1C32	96		Flags
12 #x1c33	Sync Manager 3 Parameter	DT1C32	96		Flags

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Standards&Implementation

- EtherCAT Device Description contains information about the vendor and the functionality of a device
 - This XML-Description can be used by EtherCAT Configurators to configure the device and the network
 - Specification is fixed in an XSD-Schema
- Defined Extensions
 - Support of Modular Devices
 - Module-Descriptions are selectable in the EtherCAT Configurator
 - Slot-Descriptions for PDO Numbers and Object Indexes depend on module position
 - OP-Modes
 - Predefined Operation Modes
 - How to configure the synchronization modes
 - Supporting Safety-Device-Description
 - Multi language support

Element (Attribute)	Description
(CRC32)	Checksum of the Modules description
Type (ModuleIdent)	(M): Identifies the module - (O): can be downloaded as expected module ident list (objects 0xF03y) - (O): can be uploaded as real module ident list (objects 0xF05y)
Type (ModuleClass)	(O): used to group modules
Type (ModulePdoGroup)	(O): used to define the PDO assign rule
Name	(M): Name of the module
RxPDO	(O): RxPDO(s) of the module - PDO Number of the PDO might be adapted - Index of the PDO entries might be adapted
TxPDO	(O): TxPDO(s) of the module - PDO Number of the PDO might be adapted - Index of the PDO entries might be adapted
SafetyParaMapping	(O): used only for FSoE modules
Mailbox	(O): used to define init commands - Index of the entries might be adapted
Profile	(O): used to define the object dictionary - Index of the entries might be adapted
Image	(O): Images of the module

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CoE CANopen

FoE File Access

SoE Servo Drive

ESI: Device Description

SII: Slave Information /IF

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Standards&Implementation

- The connectable modules are described as Slots
- One Module is related to one Slot
- One Slot Group contains several Slots
- Adapting of Index and PDO-Number
 - Unchanged
 - With every slot (module)
 - With every slot group

Element (Attribute)	Description
(MaxSlotCount)	maximum number of modules (slots) connectable
(SlotPdoIncrement)	defines the increment (multiplied with the slot's (module's) position) of the PDO number
(SlotIndexincrement)	defines the increment (multiplied with the slot's (module's) position) of the Index
(MaxSlotGroupCount)	maximum number of slot groups connectable
(SlotGroupPdoIncrement)	defines the increment (multiplied with the slot group's position) of the PDO number
(SlotGroupIndexincrement)	defines the increment (multiplied with the slot group's position) of the Index
Slot	List of slots (Definition of the slot see next slide)
ModulePdoGroup	One element for each Module PDO Group
ModulePdoGroup (Alignment)	Defines the alignment after the Module PDO Group
ModulePdoGroup (PdoNo)	Defines the PDO Number containing the mapping information of the align

- The Slot Definition contains a list of modules connectable to this slot
- The allowed order of the modules is described with the order of the slot definitions
- Mandatory modules to be connected can be described with **MinInstances=MaxInstances=1**

Element (Attribute)	Description
(SlotGroup)	Slot Group of the module
(MinInstances)	Minimum number of instances of the slot
(MaxInstances)	Maximum number of instances of the slot
(SlotGroupIndexIncrement)	defines the increment (multiplied with the slot group's position) of the Index. If defined, it overwrites SlotGroupIndexIncrement of the Slots definition.
ModuleIdent	List of modules (identified by ModuleIdent) which is connectable to the slot
ModuleClass	List of modules (identified by ModuleClass) which is connectable to the slot

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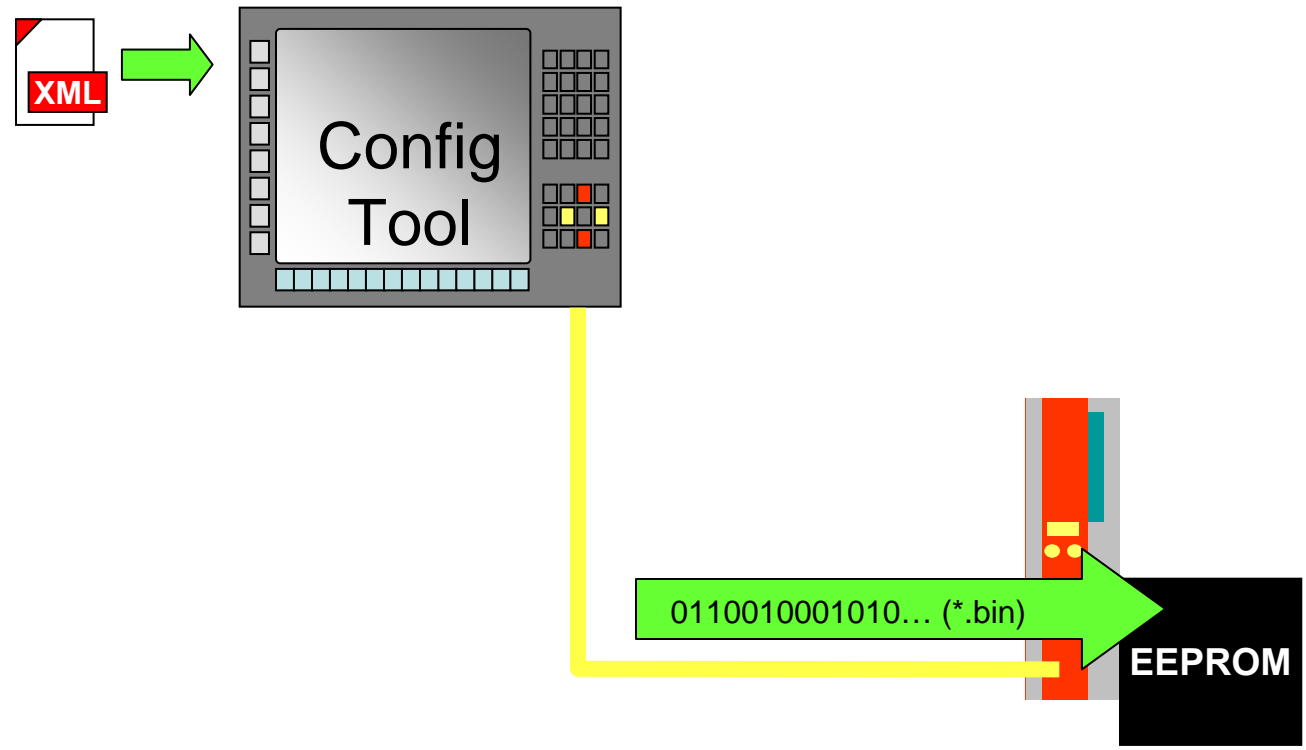
Drives

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Standards&Implementation

Configuration Tool generates binary file from device description to update EEPROM on slave



Slave Information Interface (SII)

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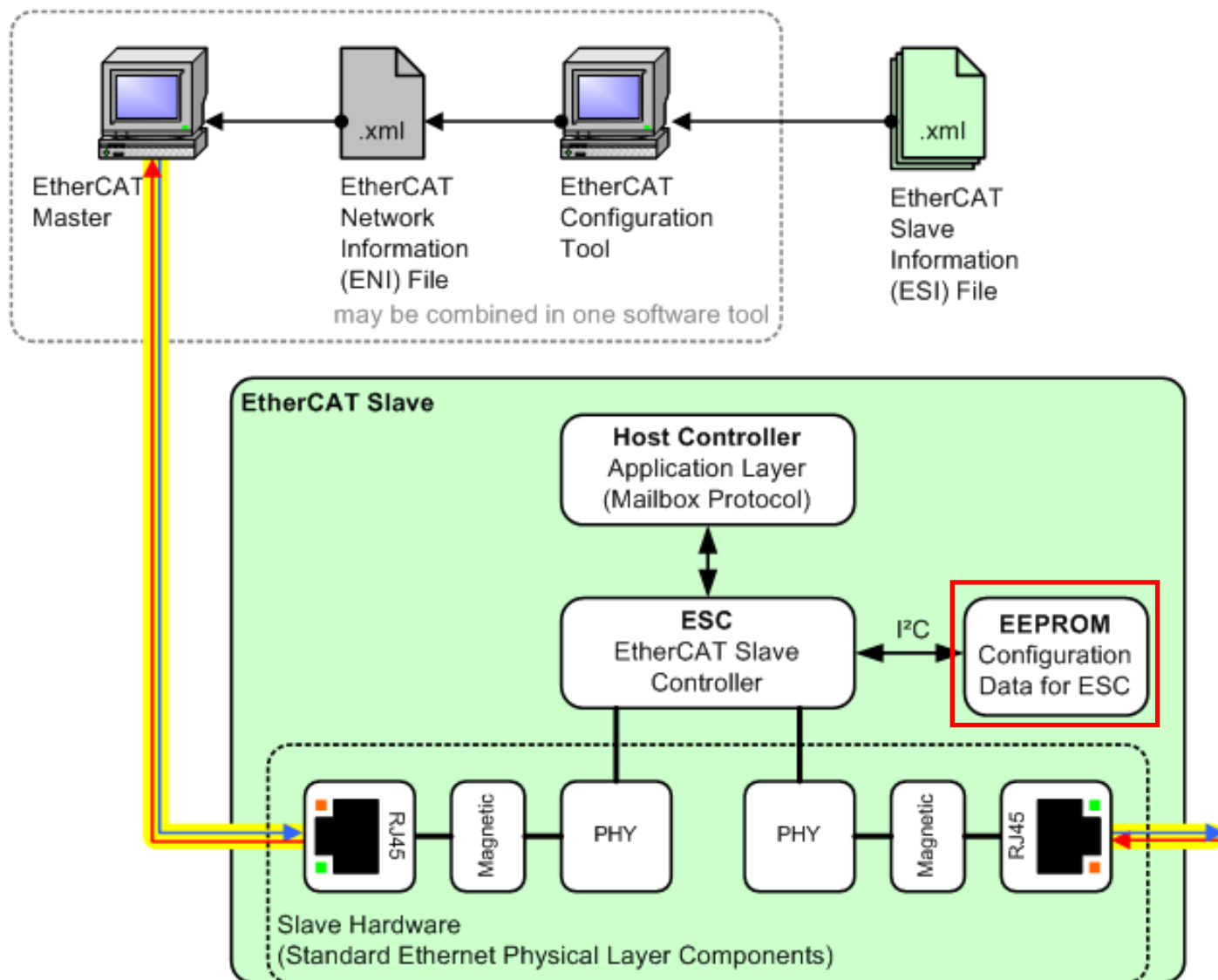
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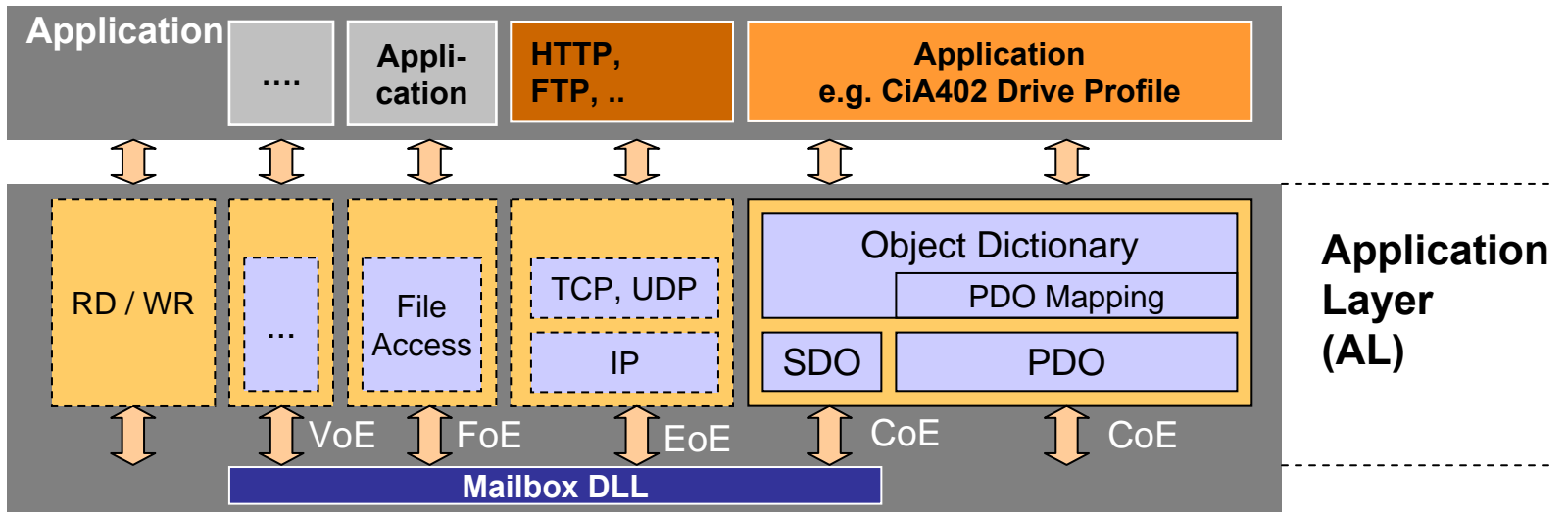
- Modular Devices
- Drives

Configuration Tool

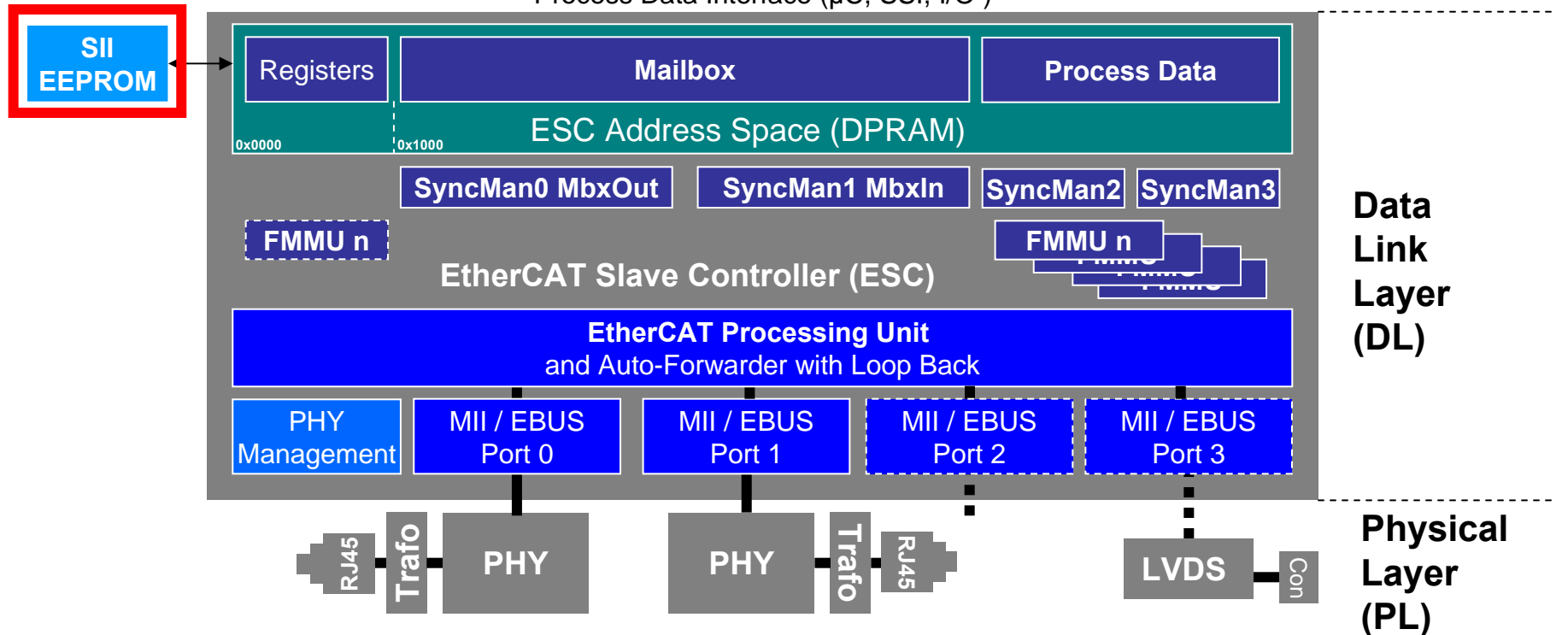
EtherCAT Master

Standards&Implementation





Process Data Interface (μC, SSI, I/O)



Purpose of Slave Information Interface

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Standards&Implementation

- Slave Information Interface (SII) is mandatory for each EtherCAT Slave Device
- Information is stored in an EEPROM
 - I²C Interface
 - 1 kByte ... 4 MByte
- SII contains
 - ESC configuraiton data (mandatory)
 - boot configuration data
 - device identity (mandatory)
 - Vendor Id, Product Code, Revision No, Serial No (same information in CoE object 0x1018)
 - application information data
- Contains additional information
 - subdivided in categories

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Erweiterte Einstellungen

Allgemein

Verhalten

Timeout Einstellungen

FMMU / SM

Init Kommandos

Mailbox

Distributed Clock

ESC Zugriff

E²PROM

Smart View

Hex Editor

FPGA

Memory

Smart View

Config Data (evaluated from ESC)

E²PROM Size (Byte): 128

PDI Type: SPI slave

☐ Device Emulation (state machine emulation)

SPI / 8 / 16 µC Interface

☒ BUSY Open Drain ☒ BUSY High Active
☐ INT Open Drain ☐ INT High Active

32 Bit Interface

☒ WD Open Drain ☒ WD High Active
☐ Input Latch

Sync Signal Configuration

☐ SYNC0 Open Drain ☐ SYNC0 High Active
☐ SYNC0 Enabled ☐ SYNC0 to PDI IRQ
☐ SYNC1 Open Drain ☐ SYNC1 High Active
☐ SYNC1 Enabled ☐ SYNC1 to PDI IRQ

Impulse Length (µs): 0

Write E²PROM...

Read E²PROM...

Device Identity (hex)

Vendor Id: 0x00000002

Product Code: 0x26483052

Revision No.: 0x03200001

Serien Nr.: 0x00000000

Product Revision: EL9800-0001-0800

Mailbox

☒ CoE ☐ SoE ☒ EoE ☒ FoE
☐ AoE

Bootstrap Konfiguration

Out Start/Länge: 4096 532

In Start/Länge: 6144 532

Standard Konfiguration

Out Start/Länge: 6144 192

In Start/Länge: 7168 192

OK

Abbrechen

SII – EEPROM Layout

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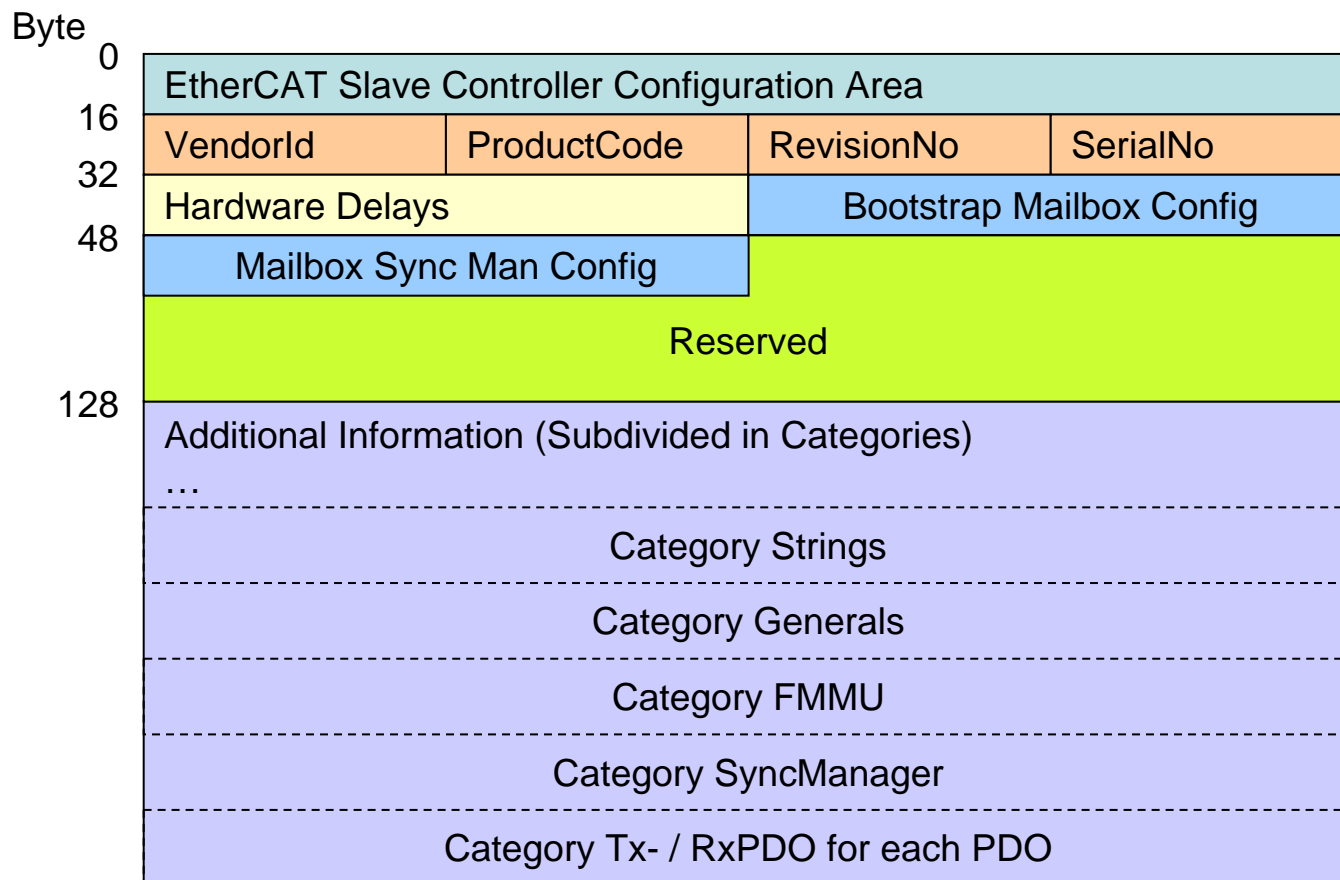
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EEPROM – Slave Configuration Area

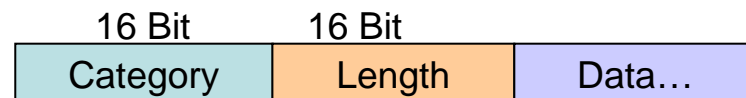
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EEPROM Word Address	Parameter	Description	Corresp. Register
0	PDI Control	Initialization value for PDI Control register (EEPROM ADR 0x0000.9 is also mapped to register 0x0110.2)	0x0140 0x0141
1	PDI configuration,	Initialization value for PDI Configuration register Depends on the selected PDI, Configuration of Sync0 and Sync1 Pin	0x0150 0x0151
2	Pulse length of SYNC signals	Initialization value for Pulse Length of SYNC Signals register in Units of 10 ns	0x0982 0x0983
3	Extended PDI configuration	Initialization value for extended PDI Configuration register	0x0152 0x0153
4	Configured Station Alias	Initialization value for Configured Station Alias Address register	0x0012 0x0013
5, 6	Reserved		
7	Checksum	CRC of first 6 words (x^8+x^2+x+1 , initial value 0xFF)	

SII – Categories

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- Standards&Implementation**

- Contains optional information
- Divided in categories
 - Standard category(s)
 - Vendor category(s)
- Same header for all categories
 - Category Type
 - Word-Length of data



Category	Meaning
STRINGS	Text strings
General	Device Information
FMMU	FMMU usage
SyncManger	Modes of operation, Enable
TxPDO	Transmit PDO Entries
RxPDO	Receive PDO Entries

Device Types:		No PD MBX	Fixed PD No MBX	Fixed PD No OD	Fixed PD OD	Variable PD OD
Info Struct (128 Byte)	ESC Info	M	M	M	M	M
	Identity	M	M	M	M	M
	Bootstrap Mbx Info	O		O	O	O
	Standard Mbx Info	M		M	M	M
Categories	Strings (10)	O	O	O	O	O
	General (30)	M	M	M	M	M
	Sync Mng (41)	O	M	M	M	M
	FMMU (40)	O	O	O	O	M
	PDO (50, 51)		M	M	O (same as OD)	
	OP Modes (60) Discussion?		O	O	O	O
	Timeouts (70)	O	O	O	O	O
	Object Dictionary Entries (80)	O	O	O		

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- Only used for slave devices without any Object Dictionary
- Content of a virtual Object Dictionary – comparable to the virtual OD in the device description files (XML)
- Provides information for the master that are already defined by existing profiles
- Examples are
 - Device type...
 - DC settings (Object 1C32h, 1C33h...)
 - ...

PDI access to SII (E²PROM)

- Standard: EEPROM assigned to EtherCAT Master
- Master can grant access to PDI
 - Change from INIT to PREOP Master
 - PDI can check SII content (e.g. Firmware Revision compatible to Hardware Revision)
(before ALCtrl=2 until ALStatus=2)
 - Change from INIT to BOOT and while in BOOT
 - After Firmware Update PDI can update SII information
(before ALCtrl=3, ALCtrl=1 until ALStatus=1)
- Slave Sample Code V4.0 contains SII access from PDI

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Standards&Implementation

- The main issues of this device model are
 - modeling of structures within a device
 - usable for a large number of devices from very simple one to complex sub-structured
 - easy way for master and configuration devices to handle the device
 - use of similar channel profiles in all device types shown below

→ **Profile for Modular Devices**

Modular Device Profile

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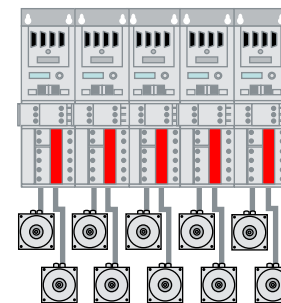
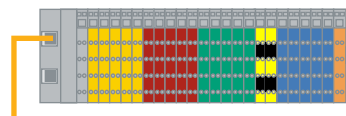
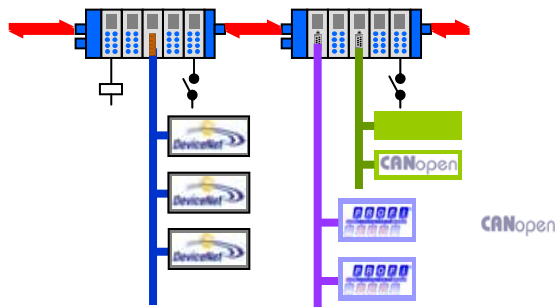
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EtherCAT Master

Standards&Implementation

- EtherCAT supports complex slaves
- E.g. devices with physical modules to be connected (modular device) or devices with different operation modes (complex device).
- The Modular Device Profile defines
 - A modeling of structures within a device, e.g. the Object dictionary
 - An easy way for master and configuration devices to handle the device



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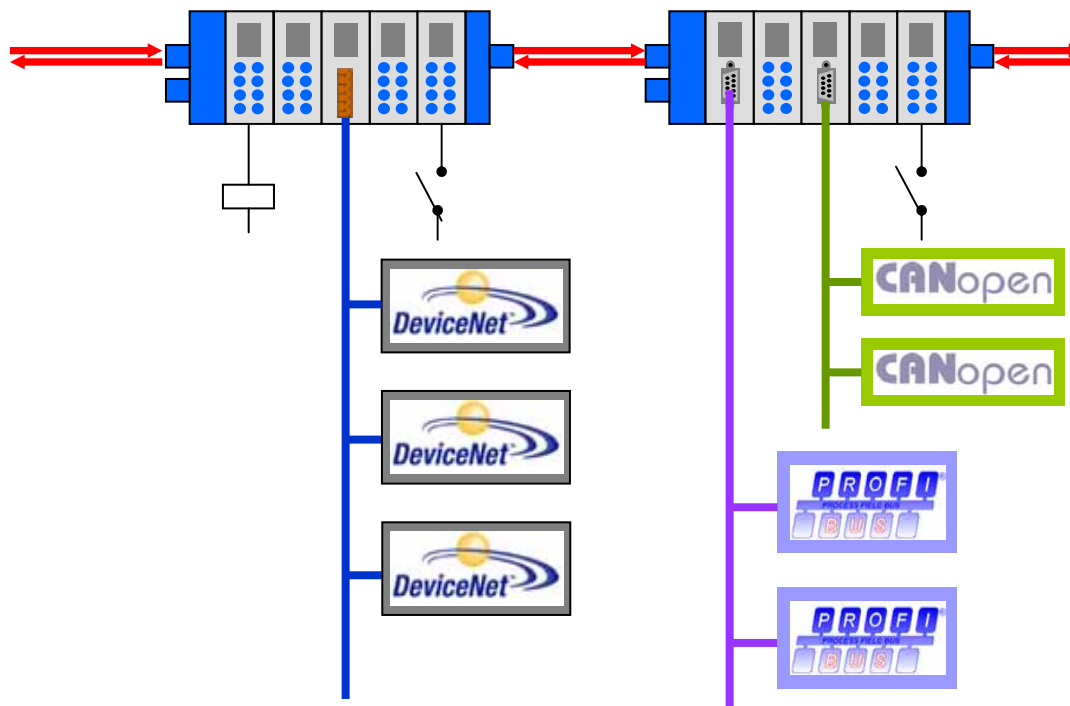
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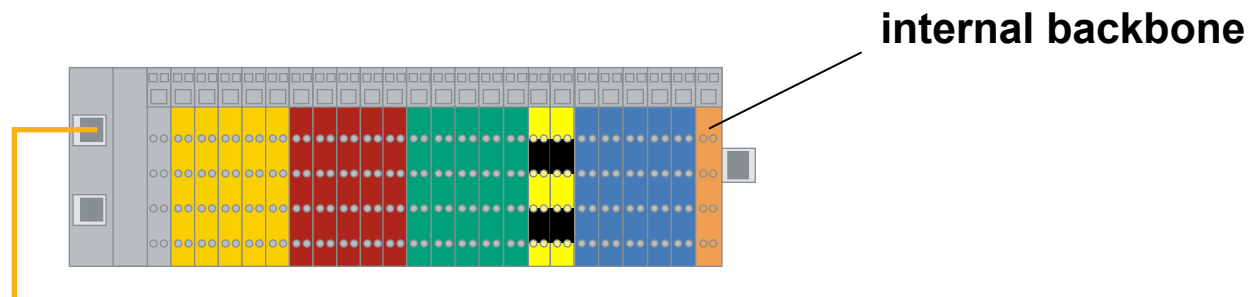
Standards&Implementation

- Gateways from EtherCAT to legacy Fieldbusses like
 - CANopen*
 - Profibus
 - DeviceNet
 - ...

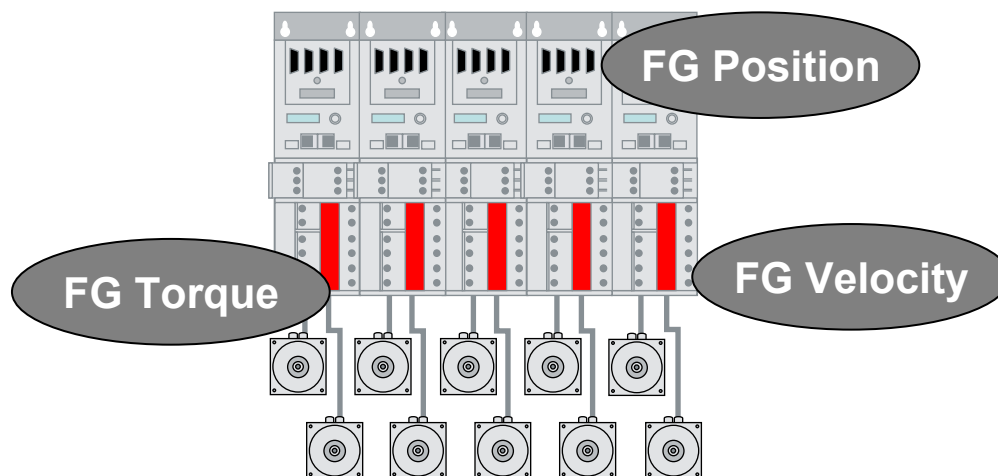


Modular Device Profile

- Extendable Fieldbus coupler with internal backbone



- Multi Axis Servo Drive with different function groups



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Standards&Implementation

- 0x0000 – 0x0FFF Data Type Area
- 0x1000 – 0x1FFF: Communication Area
- 0x2000 – 0x5FFF: Manufacturer specific Area
- **0x6000 – 0x6FFF: Input Area**
- **0x7000 – 0x7FFF: Output Area**
- **0x8000 – 0x8FFF: Configuration Area**
- **0x9000 – 0x9FFF: Information Area**
- **0xA000 – 0xAFFF: Diagnosis Area**
- **0xB000 – 0xBFFF: Service Transfer Area**
- 0xC000 – 0xEFFF: Reserved Area
- **0xF000h – 0xFFFF: Device Area**

Different Ranges according to CANopen* DS301!

Model of a modular device

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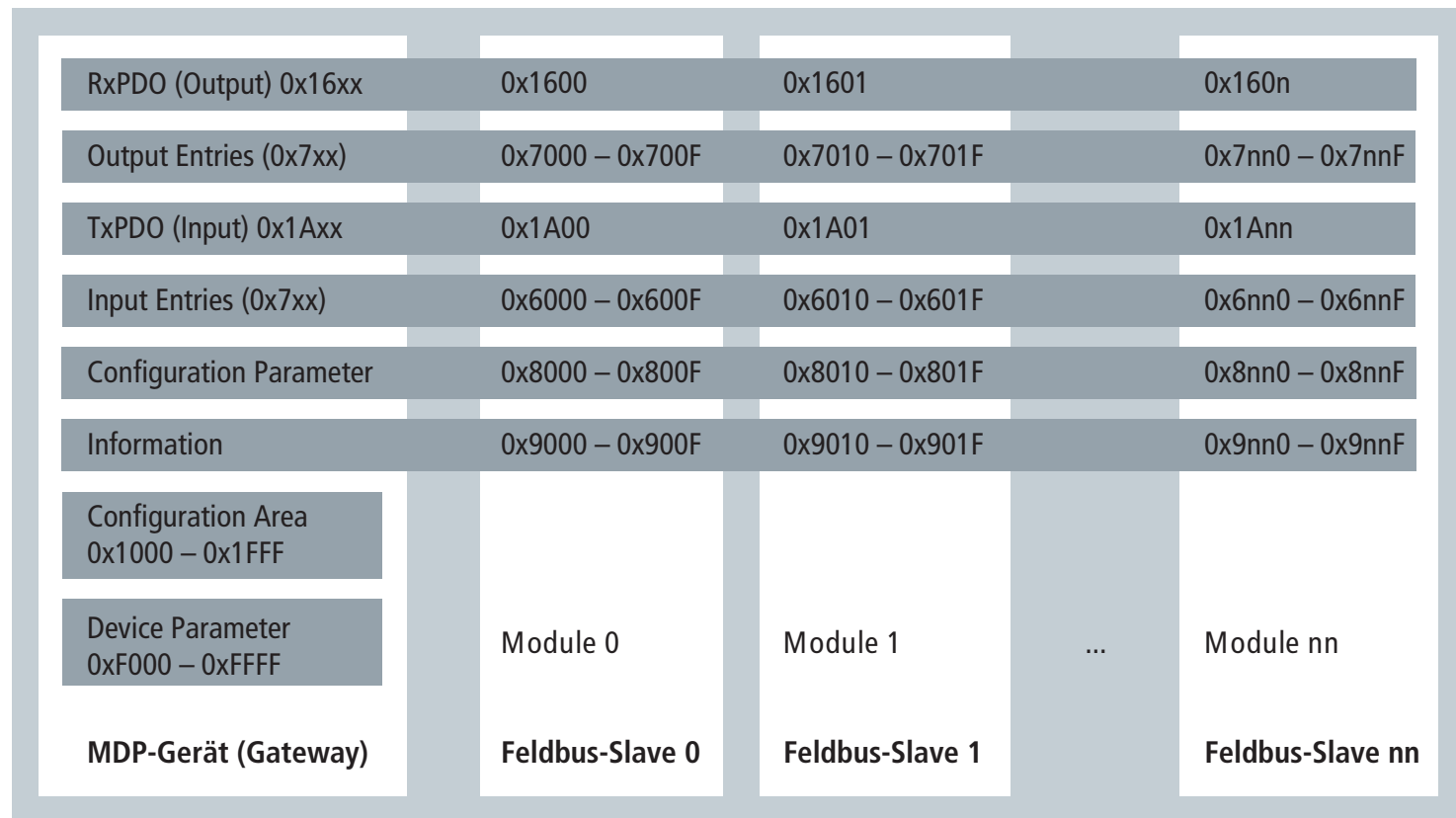
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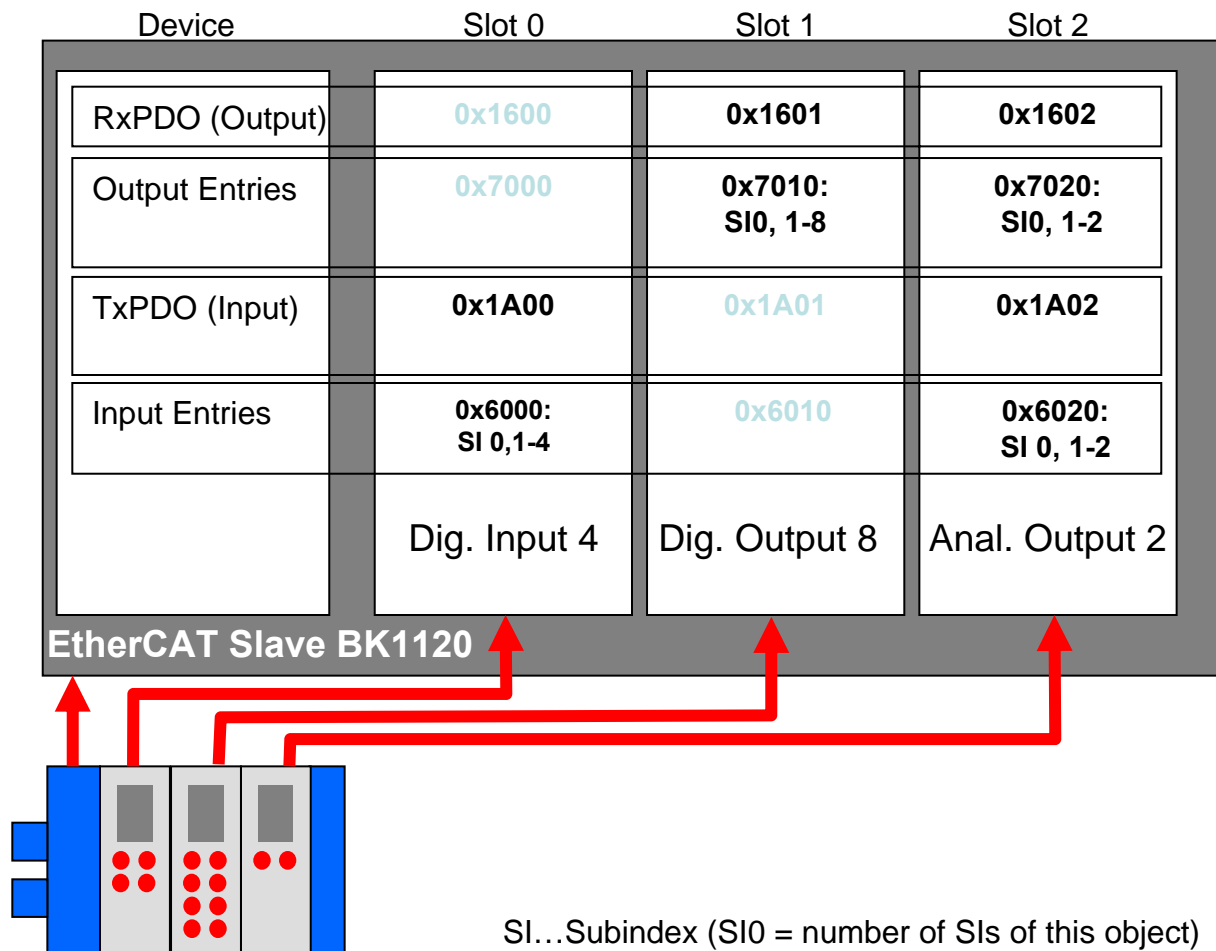
EtherCAT Master

Standards&Implementation

- The modular device profile can be applied for different device types
 - Fieldbus Gateways
Gateways to other fieldbusses
 - Modular Devices
with physical connectable modules and/or several functional modules
 - Module Devices
which is connected directly to EtherCAT and consists of several channels
- Standard configuration
 - 16 objects per module in a specific area
 - Up to 255 modules available
 - This standard can be adapted to the device requirements

Example for Dynamic Device Profile

BK1120 + DI 4 + DO 8 + AO 2



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Standards&Implementation

MDP – Profile Numbering

- MDP Profile Number is 5001 (= Object 0x1000, Bit 0-15)
- Module Profile Number (= Object 0x1000, Bit 16-31)

Module Profile Number	Description
1000	Ethernet gateway (EoE)
1100	EtherCAT Master (for Mailbox Gateway)
1110	EtherCAT Slave
1120	KBus Master
3100	PROFIBUS Master
3110	PROFIBUS Slave
4000	Interbus Master
4010	Interbus Slave
5100	CANopen* Master
5110	CANopen* Slave
5200	DeviceNet Master
5210	DeviceNet Slave
6200	ASI-Master
6220	IO Link-Master

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- IEC 61800-7
Generic interface and use of profiles for power drive systems
 - Part 7-1: Interface definition
 - Part 7-2: Profile specifications
 - Part 7-3: Mapping of profiles to network technologies
 - Mapping CiA402 to EtherCAT
 - Mapping SERCOS profile to EtherCAT
- ETG.6010 Implementation Guideline for the CiA402 Drive Profile
 - Specify a common behavior of EtherCAT CiA402 servo drives according to IEC 61800-7
- Scope
 - EtherCAT CiA402 Servo Drives
 - No frequency converter, no stepper
 - The mapping of the SERCOS profile to EtherCAT described in IEC 61800-7-304 is not part of this guideline
- Sample Implementation
 - Within Slave Sample Code V4.30 according to ETG.6010

Drive Profile

EtherCAT is Part of the drives standard in Annex A and D

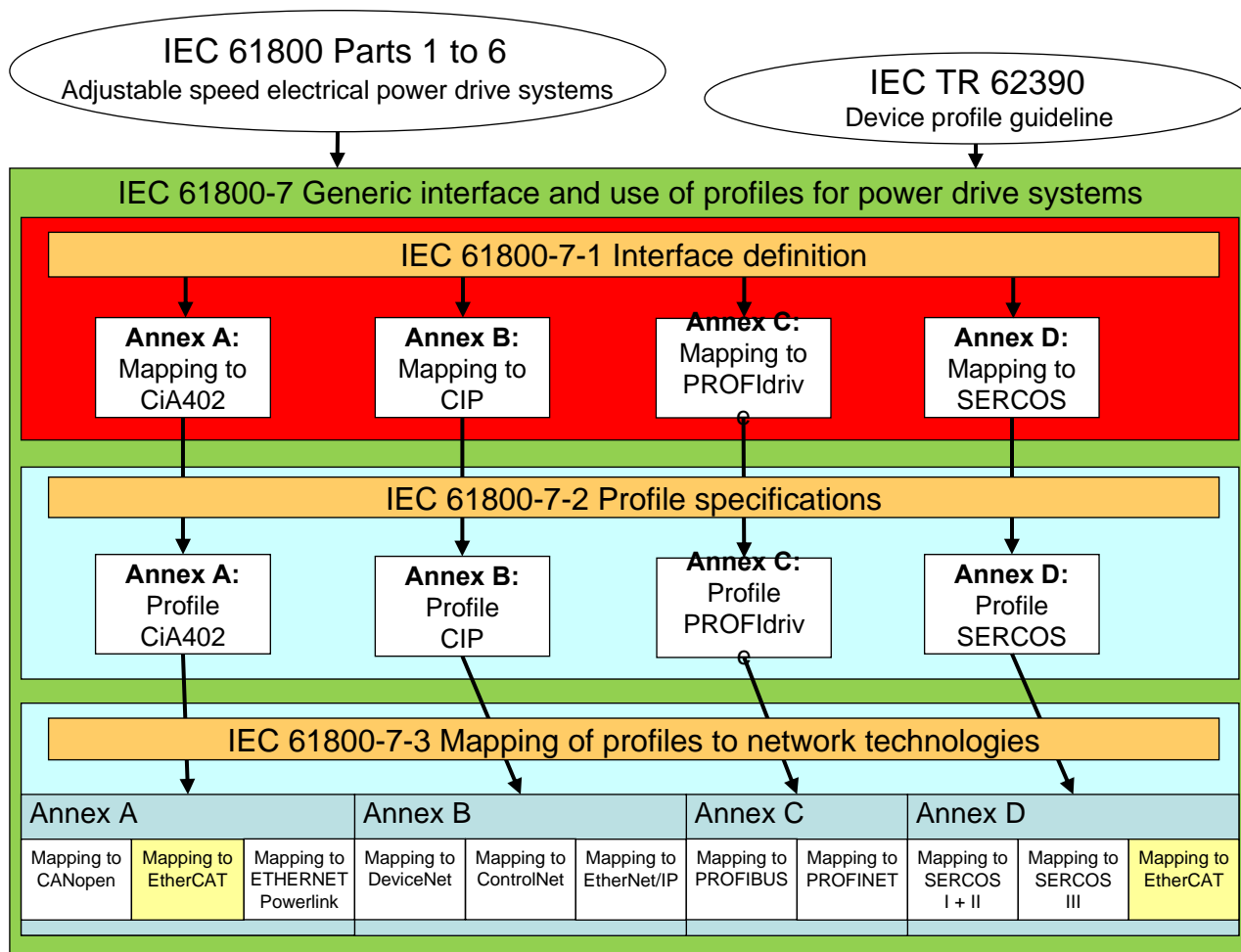
Abstract models

Generic PDS interface

Mapping to solutions (profiles)

Drive profiles

Mapping to network technologies



SERCOS interface™ is a trade name of Interests Group SERCOS interface e.V. Compliance to this profile does not require use of the trade name SERCOS interface. Use of the trade name SERCOS interface requires permission of the trade name holder.

* CANopen is a trademark of the CAN in Automation e.V.

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Drive Control - Modes of Operation

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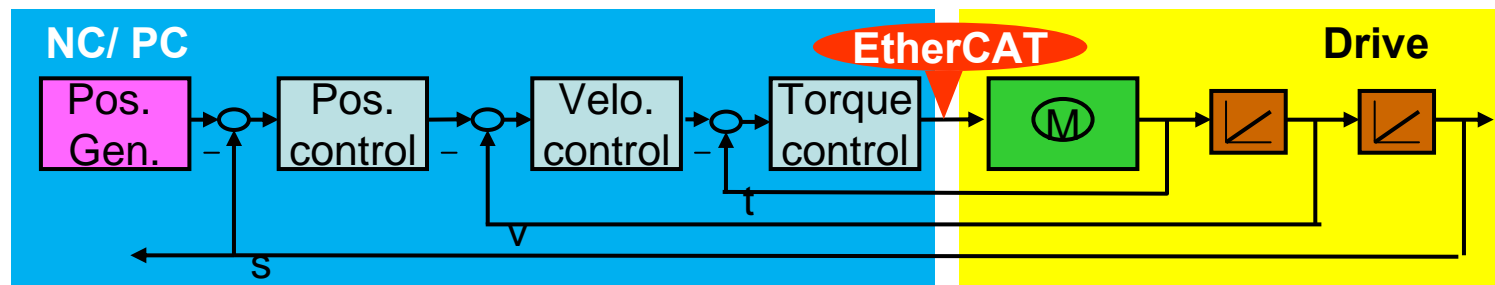
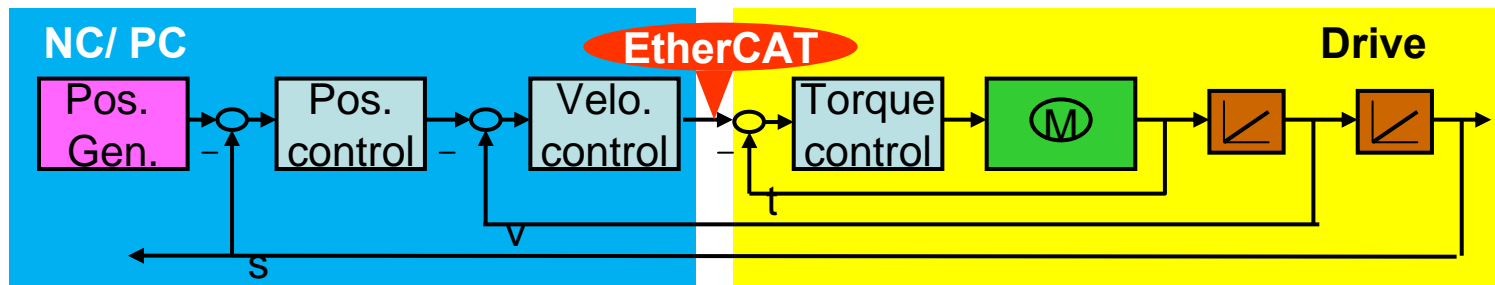
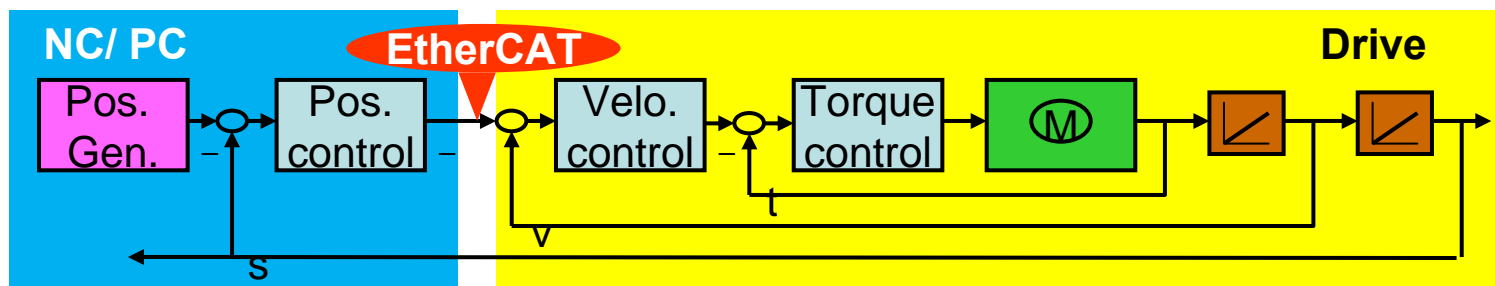
Device Profiles

- Modular Devices
- Drives

Configuration Tool

EtherCAT Master

Standards&Implementation



EtherCAT Basics

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Standards&Implementation

- **Contents**

- Clarifications of the state machine

- Modes of operation

- Function Groups (FG)

- FG Position, FG Velocity, FG Torque

- FG Torque Limiting, FG Homing, FG Touch Probe

- Endless Positioning

- For PDO Mapping only recommendation

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Standards&Implementation

- Configure EtherCAT Slave devices
 - Evaluate XML device description
 - Evaluate EEPROM information – if online
- Generate network initialization commands
 - Information for the EtherCAT driver
 - Initialization commands correspond to State Machine transitions
- Generates cyclic commands
 - Information for the EtherCAT driver

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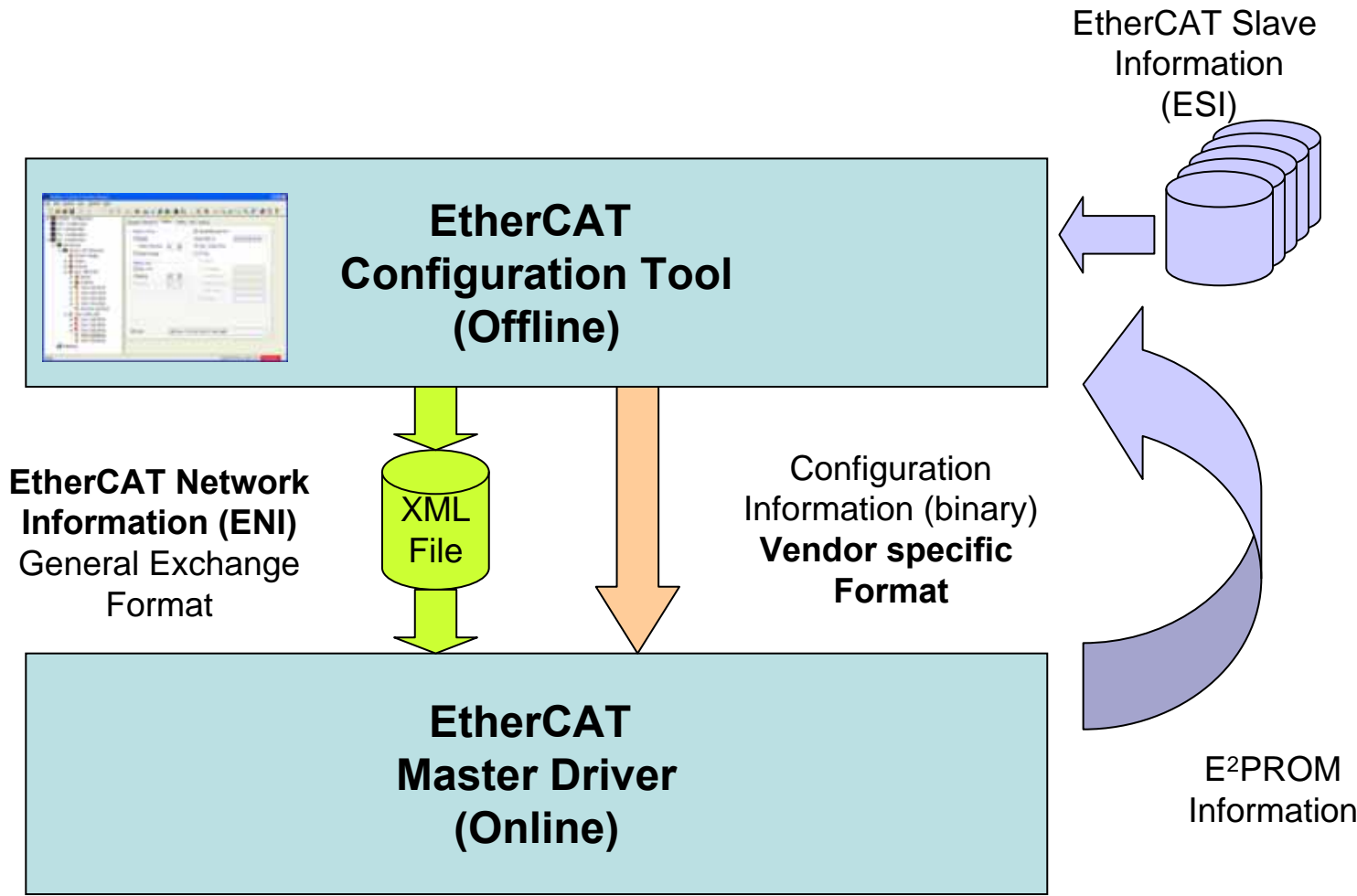
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Standards&Implementation

- Vendor and Driver independent format
- Master Vendor must not imperatively develop an own Configuration Tool
- Contains
 - initialization commands – per slave device
 - cyclic process data commands
 - information about the mailboxes

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Standards&Implementation

- Configuration with help of an EtherCAT configuration XML file
- Send and receive raw Ethernet frames from a network adapter
- Management of the EtherCAT slaves
 - Sending init commands defined in the XML file
- Mailbox Communication
 - CAN application protocol over EtherCAT protocol (CoE)
 - Servo-Profile over EtherCAT protocol (SoE)
 - Ethernet over EtherCAT protocol (EoE)
 - Filetransfer over EtherCAT protocol (FoE)
- Software-integrated switch functionality
- Cyclic process data communication

What does an EtherCAT Master do?

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Standards&Implementation

- Parse XML Hardware configuration file (initialization, state machine, and process data mapping)
- Initialization of Fieldbus
- Runs State Machine
- Interface to application
- Interface to network driver
- Sends cyclic process data commands
- Sends mailbox commands
- Handles various protocols

EtherCAT Master Block Diagram

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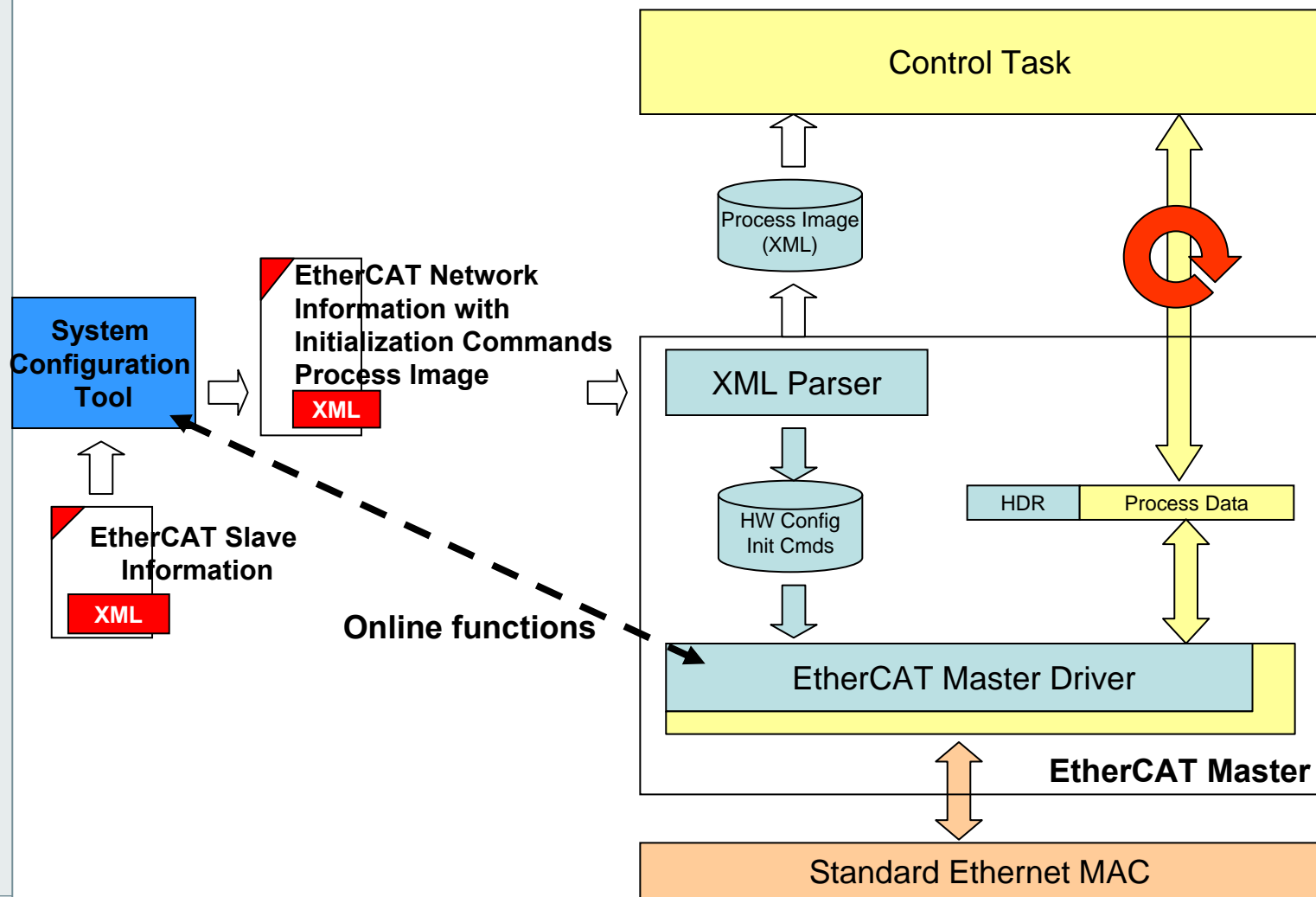
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Standards&Implementation

- **Hardware**

- Standard network controller using DMA
 - **NO** special plug in card needed
 - Speed and Quality important
- No switches or hubs required
- Cache design, CPU
- Low jitter, x86 Dual Xeon < 2 μ s

- **Software**

- Real-time Kernel
- Low Level Network Card Access

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Standards&Implementation

1. XML Parser functionality

2. EtherCAT Master driver

- Interface to configuration tool
- State machine
- Interface to application
- Interface to network card
- NIC Timing Interface

3. Real Time Kernel

4. Hardware Configuration Tool

- 3.rd party configuration tools can be used as the configuration is provided to EtherCAT master in a common format (XML)

necessary

optional

Master Sample Code Structure

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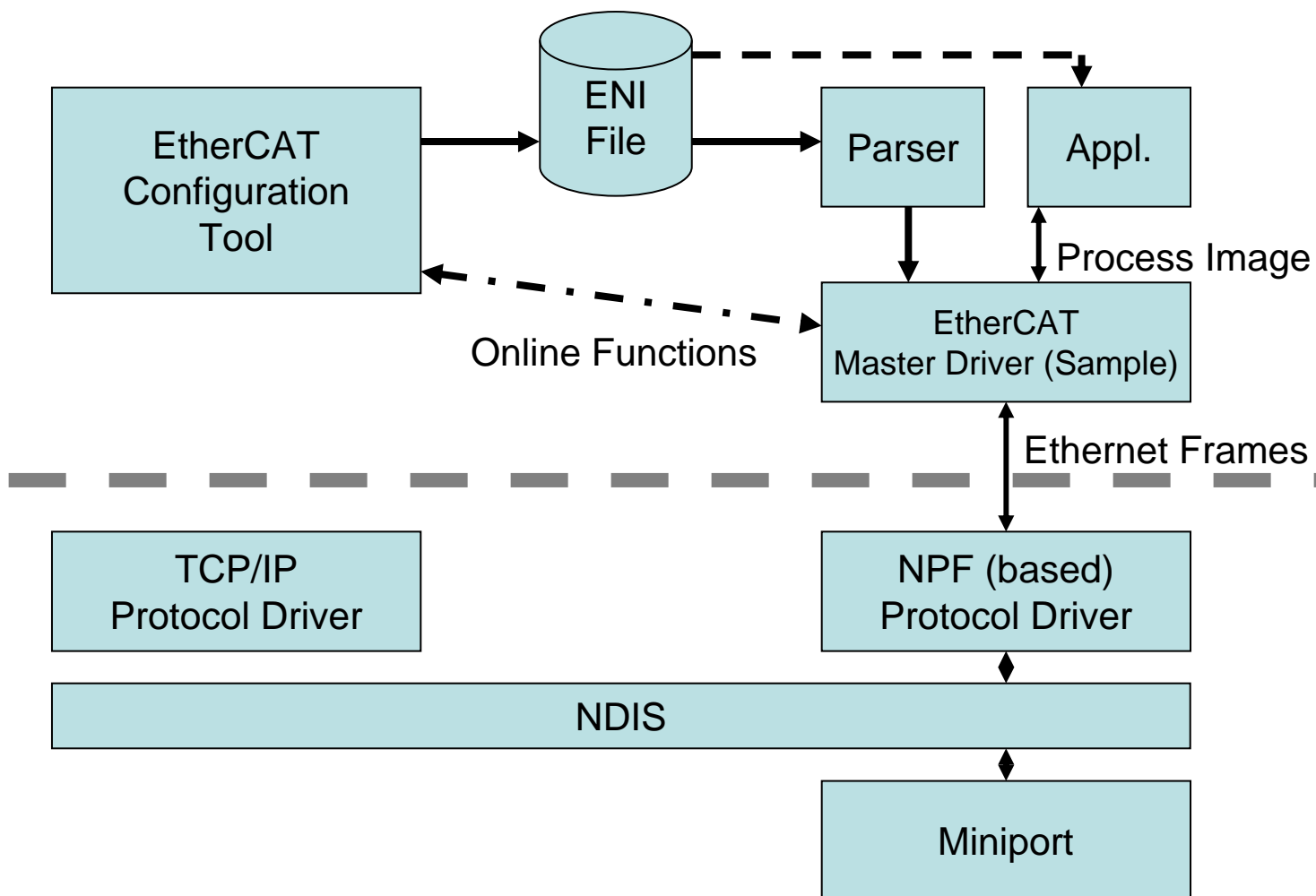
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- Modular Devices
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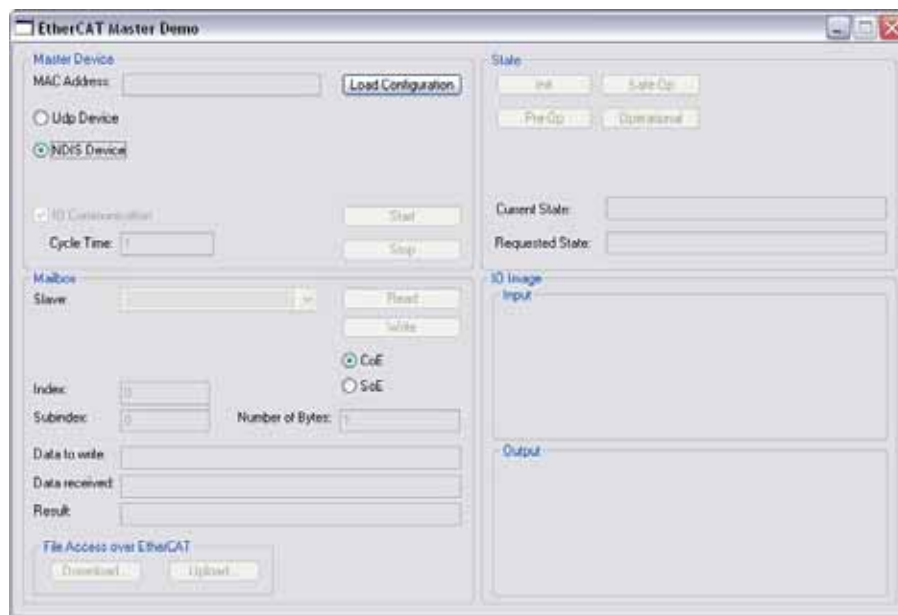
EtherCAT Master

Standards&Implementation



Master Sample Code Overview

- Sample EtherCAT Master Communication Software (including Source Code)
 - Non Real Time
 - Realized as Windows Application Program (MS Windows XP/2000)
 - Source Code MS C++



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EtherCAT Master

Standards&Implementation

- Specification of EtherCAT has been done in the EtherCAT Technology Group (ETG)
- Specifications available at www.EtherCAT.org
 - XML File Style sheet
 - Datasheets of ESC, ...
 - Modular Device Profile
 - Reports of ETG TC meetings
- International standardization

- EtherCAT is part of different international standardization efforts

Standard	Title	Status	Remarks
IEC 61158 and ETG.1000	Digital data communication for measurement and control – Fieldbus for use in industrial control systems	IS	Type 12: EtherCAT Specification
	Part 1: Overview and guidance		
	Part 2: Physical Layer service definition and protocol specification		
	Part 3: Data Link Layer service definition		
	Part 4: Data Link Layer protocol specification		
	Part 5: Application Layer service definition		
	Part 6: Application Layer protocol specification		

IS : International Standard

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Standard	Title	Status	Remarks
IEC 61784	Digital data communication for measurement and control		
	Part 1: Profile sets for continuous and discrete manufacturing relative to fieldbus use in industrial control systems	IS	
	Part 2: Additional profiles for ISO/IEC 8802-3 based communication networks in real-time applications	IS	CPF12: EtherCAT
IEC 61800	Adjustable speed electrical power drive systems		
	Part 7-1: Generic interface and use of profiles for power drive systems – Interface definition	IS	Part 7: Drive Profiles
	Part 7-2: Generic interface and use of profiles for power drive systems – Profile specifications	IS	
	Part 7-3: Generic interface and use of profiles for power drive systems – Mapping of profiles to network technologies	IS	Mapping EtherCAT to CANopen* CiA402 and SERCOS

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Standard	Title	Status	Remarks
ISO 15745	Industrial automation systems and integration - - Open systems application integration framework	Ed 1	CANopen*
	Part 4 Amd 2: Profiles for Modbus TCP, EtherCAT and ETHERNET Powerlink	PRF Amd	Mapping EtherCAT to CANopen* CiA301

PRF Amd: : Proof of a new International Standard, Amendment

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Standards&Implementation

- Digital data communication for measurement and control
– Fieldbus for use in industrial control systems
- *The* communication standard
- EtherCAT is named Type 12 in IEC 61158
(no brand names allowed)
- Transformation of the communication protocol to a common model

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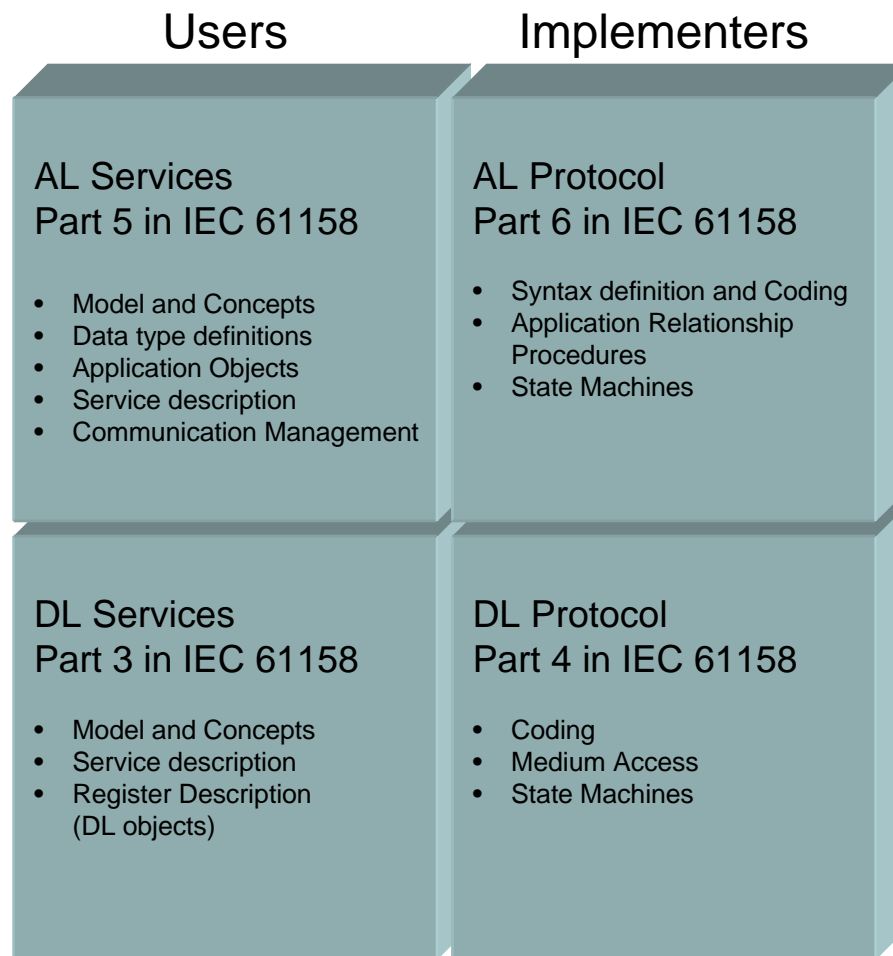
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EtherCAT Basics**Slave Structure****Device Model (ISO/OSI)****Physical Layer****Data Link Layer**

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ESI: Device Description**SII: Slave Information /IF****Device Profiles**

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Configuration Tool**EtherCAT Master****Standards&Implementation**

- ETG.1300 Indicator and Labeling specification
 - defines the implementation of indicators signaling the EtherCAT communication state, errors and the link status.
 - the location, labeling and blink codes of the indicators are defined
 - defines the labeling of the EtherCAT Ports
- ETG.9001 Marking Rules
 - specifies the marking rules for products and the corresponding documentation using the EtherCAT technology
 - Use of trademarks and logo



These specifications can be found on the ETG Website
http://www.ethercat.org/en/publications.html#members_area

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EtherCAT Master

Standards&Implementation

- ETG.2000 EtherCAT Slave Information specification
 - describes the ESI schema
- ETG.2100 EtherCAT Network Information specification
 - describes the ENI schema

These specifications can be found on the ETG Website
http://www.ethercat.org/en/publications.html#members_area

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Standards&Implementation

- ETG.2200 EtherCAT Slave Implementation Guideline
 - describes from a very practical point of view which topics have to be kept in mind for a successful EtherCAT slave implementation
- ETG.6010 CiA402 Implementation Guideline
 - defines a common behavior of an EtherCAT servo drive supporting the CiA402 drive profile

These specifications can be found on the ETG Website
<http://www.ethercat.org/en/publications.html>

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Standards&Implementation

- The EtherCAT Knowledge Base
 - contains guidelines, application notes, recommendations, technical information,
 - <http://www.ethercat.org/infosys.html>
- Content of the Knowledge Base
 - ETG.1400 EtherCAT Technology Description
 - ETG.1020 EtherCAT Guidelines & Protocol Enhancements
 - ETG.5001 EtherCAT Modular Device Profile
 - FAQs
 - ETG.1020 and ETG.5001 are part of the EtherCAT specification!

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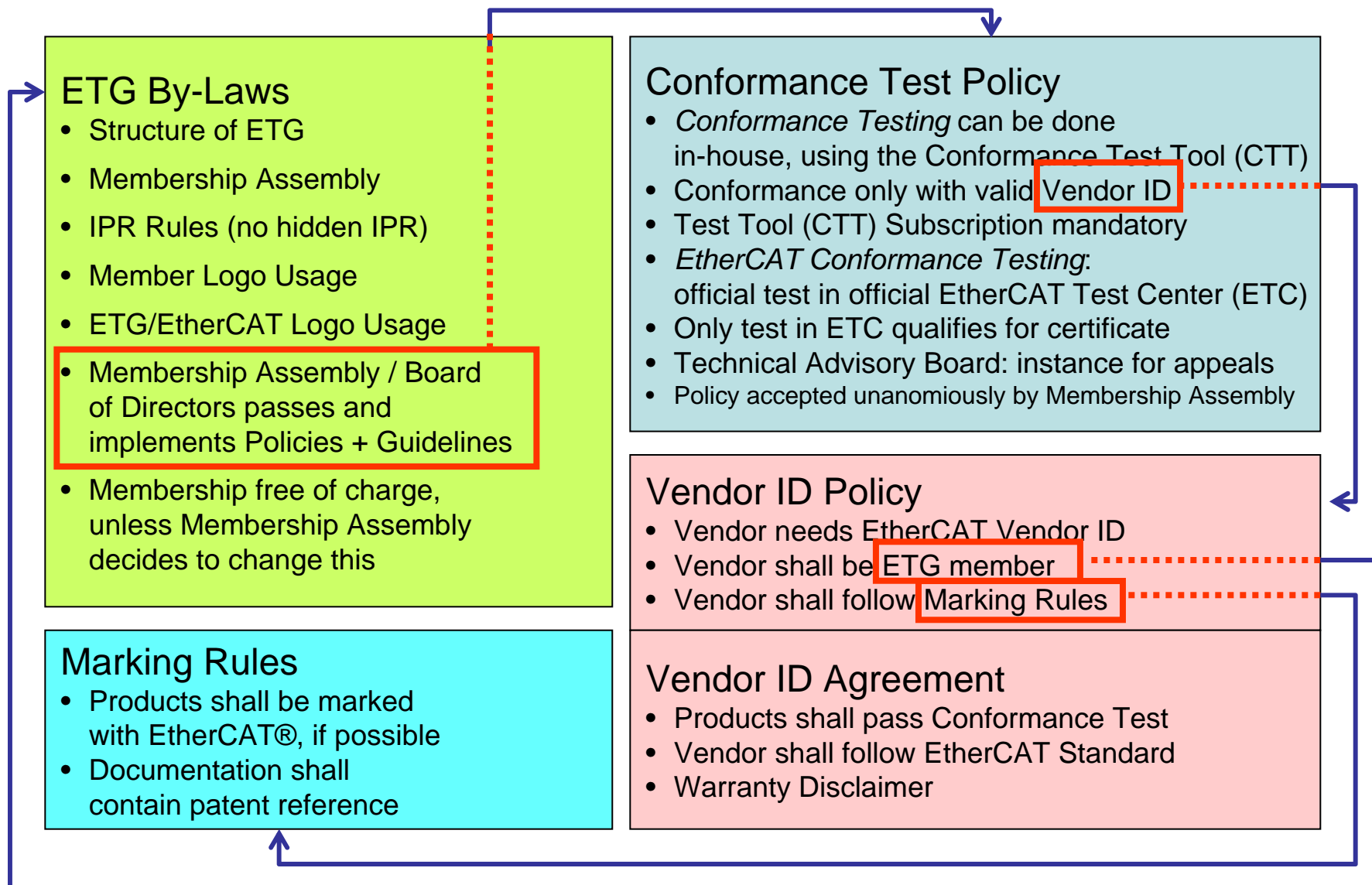
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EtherCAT Master

Standards&Implementation

- EtherCAT Vendor ID Policy
- EtherCAT Conformance Test Policy
- Download
 - <http://www.ethercat.org/en/publications.html>

ETG By-Laws + Policies: Overview



Vendor ID Policy Introduction

- EtherCAT Basics**
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- EtherCAT Master**
- Standards&Implementation**

5.6.7.4.6 Identity Object

The Identity Object dictionary entry (index 0x1018) is specified in Table 72.

Table 72 – Identity Object

Sub-Index	Description	Data type	M/O/C	Access	PDO Mapping	Value
0	Number of entries	UNSIGNED8	M	R	No	4
1	Vendor ID	UNSIGNED32	M	R	No	Assigned uniquely by ETG
2	Product Code	UNSIGNED32	M	R	No	Assigned uniquely by Vendor
3	Revision Number	UNSIGNED32	M	R	No	Assigned uniquely by Vendor Bit 0-15: Minor Revision Number of the device Bit 16-31: Major Revision Number of the device
4	Serial Number	UNSIGNED32	M	R	No	Assigned uniquely for this device by Vendor 0 if there is no serial number given

Vendor ID Principles (I)

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Standards&Implementation

- Each vendor of an EtherCAT Device shall be an ETG member and shall obtain and maintain a Vendor ID from the EtherCAT Technology Group.
- Definition of EtherCAT Device:
 - EtherCAT Device means any device with an EtherCAT master and/or slave interface, excluding infrastructure components such as cables and connectors and also excluding machines or machine lines.
 - Machine or Machine Line means an aggregation of components, optionally including but not limited to EtherCAT Devices, intended for a specific purpose.

Vendor ID Principles (II)

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Standards&Implementation

- Manufacturers of machines or machine lines, which integrate and use EtherCAT Devices such as Automation Products in combination with or in such machines or machine lines, are not required to apply for and use a Vendor ID.
- In other words: End users do not have to be ETG member and do not need an ETG vendor ID – even though they are welcome to join ETG and also may get a vendor ID.

Vendor ID Principles (III)

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Standards&Implementation

- The Vendor ID is free of charge.
- The vendor shall implement the Vendor ID in each EtherCAT Device prior to making it available on the market.
- In case a vendor uses several brand names different from his vendor brand, he shall use an individual vendor ID for each such brand name and the corresponding devices.

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Standards&Implementation

- **Definition of Communication Device:**
 - Communication Device means an EtherCAT device for assembly with or mounting with an Automation Device for the general purpose of communication of the Automation Device via EtherCAT.
- **Definition of Automation Device:**
 - Automation Device means an EtherCAT Device for assembly with or mounting with a machine or machine line intended to use for the general purpose of driving, controlling, monitoring and communication of the machine or machine line.

Typical Examples of *Communication Devices*:

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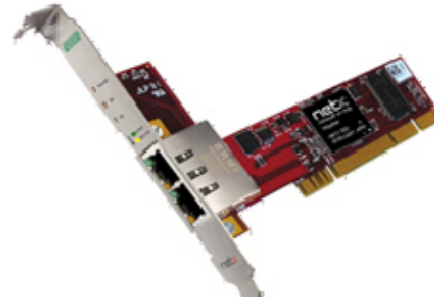
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Vendor ID for Communication Devices (I)

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Standards&Implementation

- Manufacturers of Communication Devices shall distribute those Communication Devices using their Secondary Vendor ID. Optionally they may also use their Vendor ID, e.g. for conformance testing.
- Definition of Secondary Vendor ID:
 - Secondary Vendor ID means a Vendor ID in the range of 0xE0000000:0xFFFFFFFF that the vendor of a Communication Device derives from his Vendor ID by an OR operation of his Vendor ID and 0xE0000000.
 - In the context of conformance testing a Secondary Vendor ID is not considered a valid Vendor ID.
 - Use of a Secondary Vendor ID in an Automation Device is prohibited.

Vendor ID for Communication Devices (II)

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Standards&Implementation

- End Users or Integrators may use such Communication Devices without further modification, however, may re-distribute such devices (e.g. PCI Interface card products) only as part of a machine or machine line.
- Manufacturers of Automation Devices may use such Communication Devices (e.g. communication daughter board for drives) within or combined with their own Automation Devices, and shall re-program the Products with their own Vendor ID before re-distributing such Automation Devices.
- Manufacturers of Communication Devices shall explain the Vendor ID handling in their product documentation accordingly, e.g. like proposed in the annex of the Vendor ID policy.

http://www.ethercat.org/memberarea/vendor_id.asp

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Vendor ID Assignment Form

Each EtherCAT compliant device has to implement the worldwide unique Vendor ID assigned by ETG. This Vendor ID is a numeric value of type UNSIGNED32 and consists of a unique number for each registered company and optionally for each department of that company (only if required).

Please fill out "Department/Branch" only, if you need several Vendor IDs for your company. In this case please pick a specific name, i.e. "Germany", "Medical", etc. Names like "Development" or "Manufacturing" may not be accepted due to their broad range within the company.

31	24 23	0
Department		Company
MSB		LSB

Vendor ID assignment is free of charge.

Please assign an EtherCAT Vendor ID for:

(→) required Field

Company:

Department/Branch:

Name: →

Address: →

ZIP: →

City: →

Country: →

Phone: →

Fax:

Email: →

Desired Vendor ID* (4 Byte Hex-Code)

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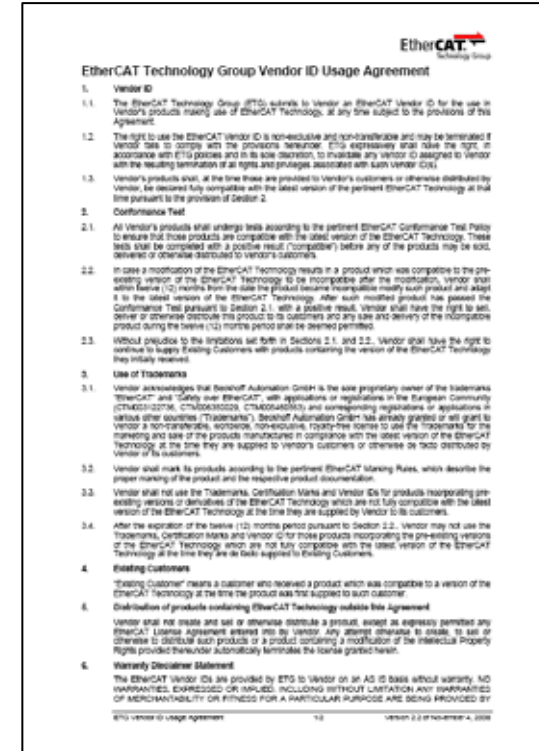
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- Applicants for an EtherCAT Vendor ID have to accept the Vendor ID Agreement

- **The Vendor ID Agreement:**
- Demands Conformance for EtherCAT products
- Governs the use of the EtherCAT Trademarks (including reference to the EtherCAT Marking Rules)
- Contains a Disclaimer (“Technology provided “as is”, with no warranty implied...)



Vendor ID: Summary

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- EtherCAT Devices need an EtherCAT Vendor ID
- End users (such as Machine Builders) do not need an own Vendor ID*
- If you have no own EtherCAT Vendor ID yet, go to EtherCAT Technology Group Website (Members Section), and apply for one!
- And one final remark: A Vendor ID obtained from any other organization is not valid for EtherCAT devices...

* For Details see Vendor ID Policy

Conformance Testing + Product Certification Frequently Asked Questions

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Standards&Implementation

- What is the difference between *Conformance Test* and *EtherCAT Conformance Test*?
 - Within the context of the ETG Conformance Test Policy:
 - *Conformance Test* is the Test, in which the conformance of the Device under Test (DuT) with the EtherCAT Specifications is tested with the help of a Conformance Test Tool.
 - *EtherCAT Conformance Test*: *Conformance Test*, Interoperability Test and Physical Layer Test carried out by an EtherCAT Test Center.
 - So the *EtherCAT Conformance Test* is a superset of the *Conformance Test*.
 - (this wording was introduced for historical reasons)

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Standards&Implementation

- **Test Tool**
 - Validates conformity for protocol layer
 - Helps to find errors during development due to detailed error description
 - Helps to improve support: detailed information saved with CTT project file

Conformance Test Tool (CTT)

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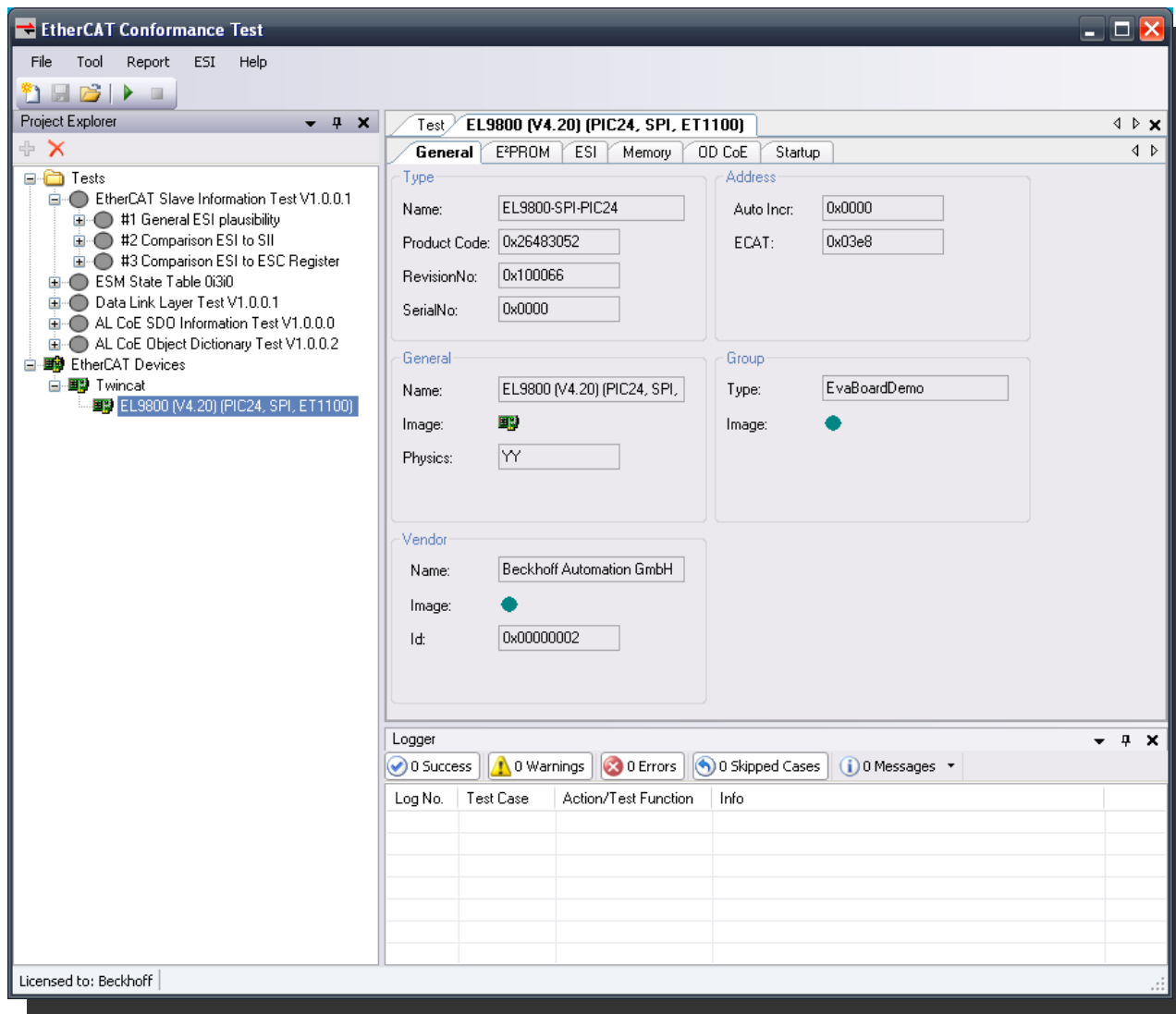
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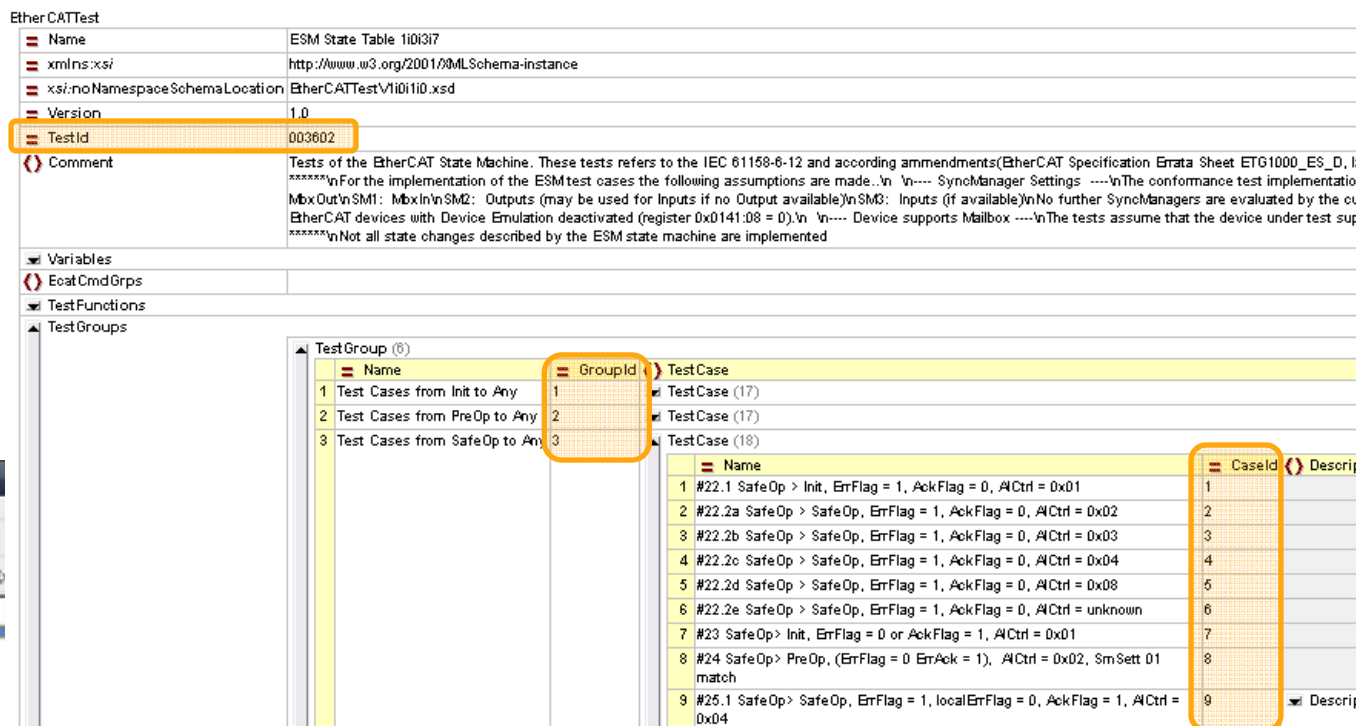
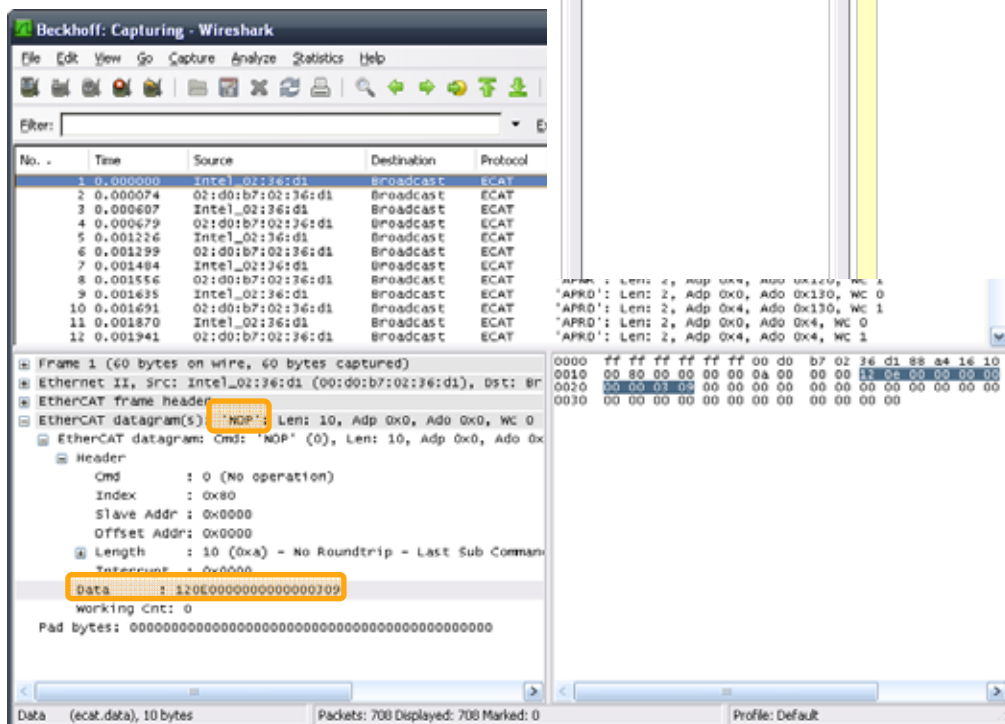
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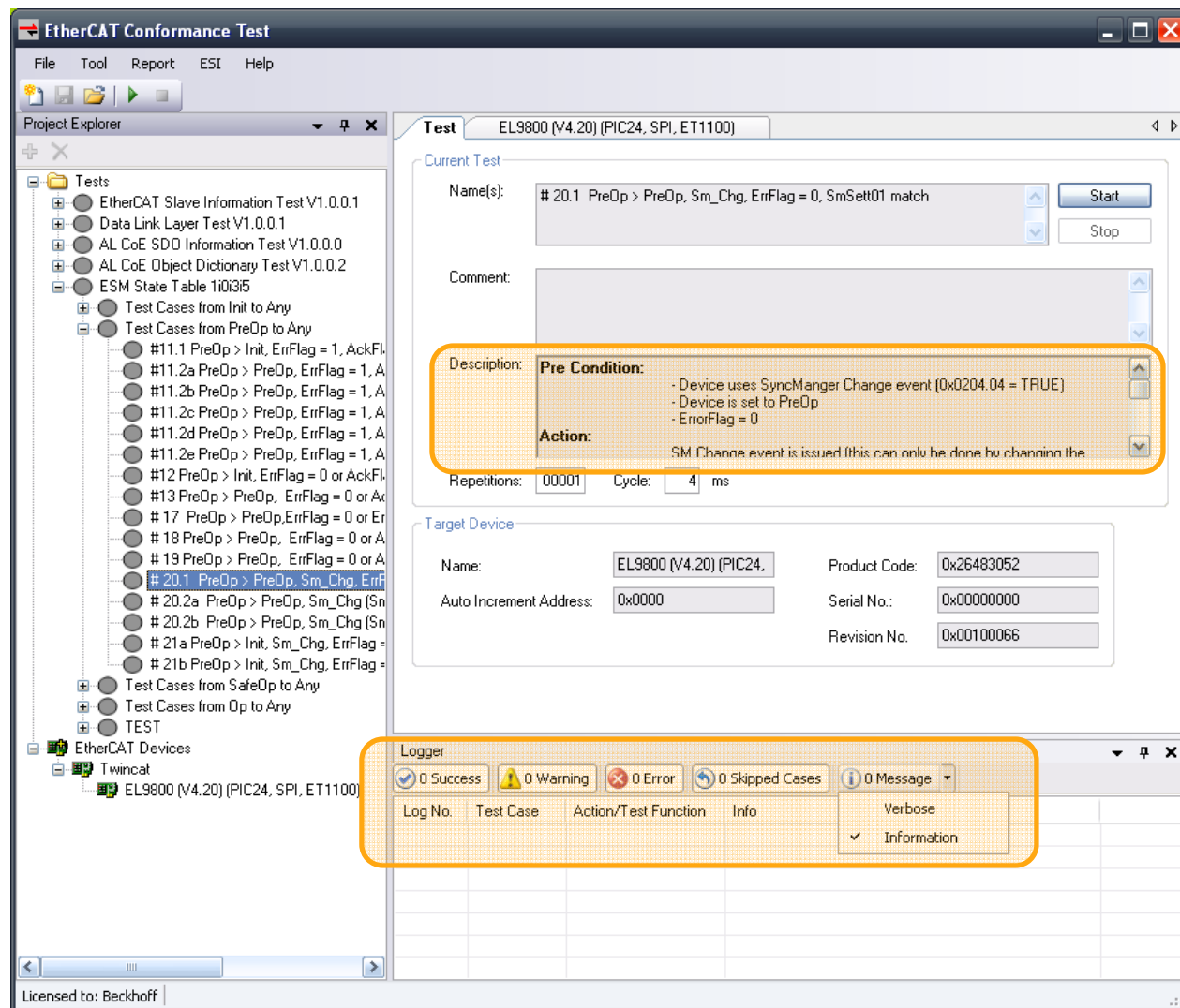
- Test ID: frames of every single test can be easily found in a network scan (e.g. Wireshark) due to a NOP cmd. containing the TestID, GroupID and CaseID



In Wireshark Scan:
NOP Command (Cmd Type 0x00)
Version_TestID_GroupID_CaseID

- Test description:
integrated into test cases
 - Pre Condition
 - Action
 - Post Condition
- Output: messages in
Logger window:
 - warnings
 - errors
 - messages / success

are highly recommended to fix
have to be fixed
information
- Verbose mode:
More information during
the test procedure



Frequently Asked Questions (II)

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- I am an EtherCAT device vendor. Do I have to license the conformance test tool?
 - Yes. The ETG takes conformance very seriously, and the availability of the conformance test tool at each and every device vendors R&D lab is an important cornerstone in this process
- Do I have to submit my EtherCAT device to the EtherCAT Test Center for testing?
 - No. Conformance Testing with the Test Tool „at home“ is sufficient to meet the minimum requirements of the Vendor ID agreement.
- Can I get a Conformance Certificate based on the test results obtained in my R&D lab?
 - No. The Conformance Certificate can only be issued after successfully passing the test at an accredited EtherCAT Test Center.

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- Does the test in the EtherCAT Test Center exceed the test done with the Conformance Test Tool (at home)?
 - Yes. The test in the EtherCAT Test Center also includes an interoperability test, checking for conformance regarding the indicator and labeling spec, the marking rules etc.
- I know about the EtherCAT Test Center in Nuremberg, Germany. Will there be other locations, too?
 - Yes. In 2009 an EtherCAT Test Center in Japan will be established, and there are plans for further test centers in Europe and North America.
- How much is charged for the EtherCAT Conformance Test at the EtherCAT Test Center?
 - Please contact the EtherCAT Test Center for pricing information – pricing is not within the scope of ETG.

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- Does the Certificate expire?
 - No. The Certificate confirms that a device of a certain release has passed the current test version in the EtherCAT Test Center. Of course the Certificate can neither confirm that all future releases of the device will also pass, nor that the current device release will pass all future enhancements of the EtherCAT Conformance Test.
- Do I have to submit my device again once I released a newer version?
 - No. However, according to the Vendor ID agreement, you will have to test future releases of your product against the conformance test tool in your R&D lab. Of course you may also submit your device again to the EtherCAT Test Center and obtain a new certificate!

Conformance Testing Procedure

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- Vendor contacts ETG office if he wants EtherCAT CT
- ETG checks Vendor ID and provides Test Contract – which also allows one to select the EtherCAT Conformance Test Center (ETC).
- Based on choice of vendor, ETG office forwards request to ETC
- ETC provides formal offer to vendor (ETG is not involved in any financial transaction)
- ETC provides checklist to vendor (how to prepare, what to send, etc.)
- Vendor sends device to ETC (or brings it there) .
- ETC tests device.
- ETC sends Test Report to Vendor and to ETG office.
- If test was passed successfully, ETG provides Certificate.

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- Devices that have passed the ETC Conformance Test may carry the official conformance test mark
- End users are encouraged to include the availability of the conformance test mark in their vendor and device selection process.



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Configuration Tool**EtherCAT Master****Standards&Implementation**

- EtherCAT Test Center is operational and available for testing
- Certificate is issued by ETG after passing test at ETC
- Every Vendor shall have (and use!) the Conformance Test Tool
- Testing at the ETC is optional, but highly recommended

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Standards&Implementation

EtherCAT Test Center (ETC), Nuremberg

EtherCAT Test Center

Beckhoff Automation GmbH

Ostendstraße 196

90482 Nuremberg, Germany

Tel.: +49 (911) 5 40 56 20

Fax: +49 (911) 5 40 56 29

etc@beckhoff.com

EtherCAT Test Center (ETC), Kyoto

EtherCAT Test Center

ASTEM RI

134 Chudoji Minami-machi, Shimogyo,

Kyoto 600-8813 Japan

Tel.: +81 (75) 366 0143

Fax: +81 (75) 315 2899

etc@testlab.astem.or.jp

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- If you have comments regarding the Conformance Test Procedure, please address them directly to conformance@ethercat.org

Thanks for your attention! Any Questions?

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